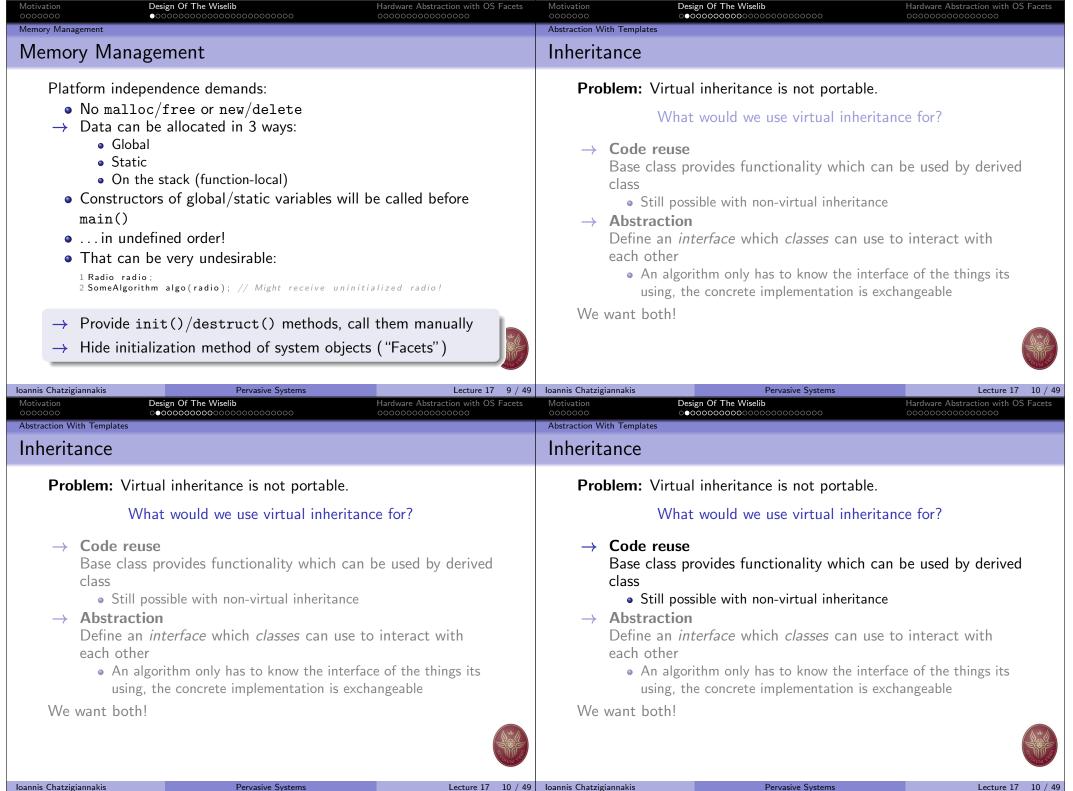
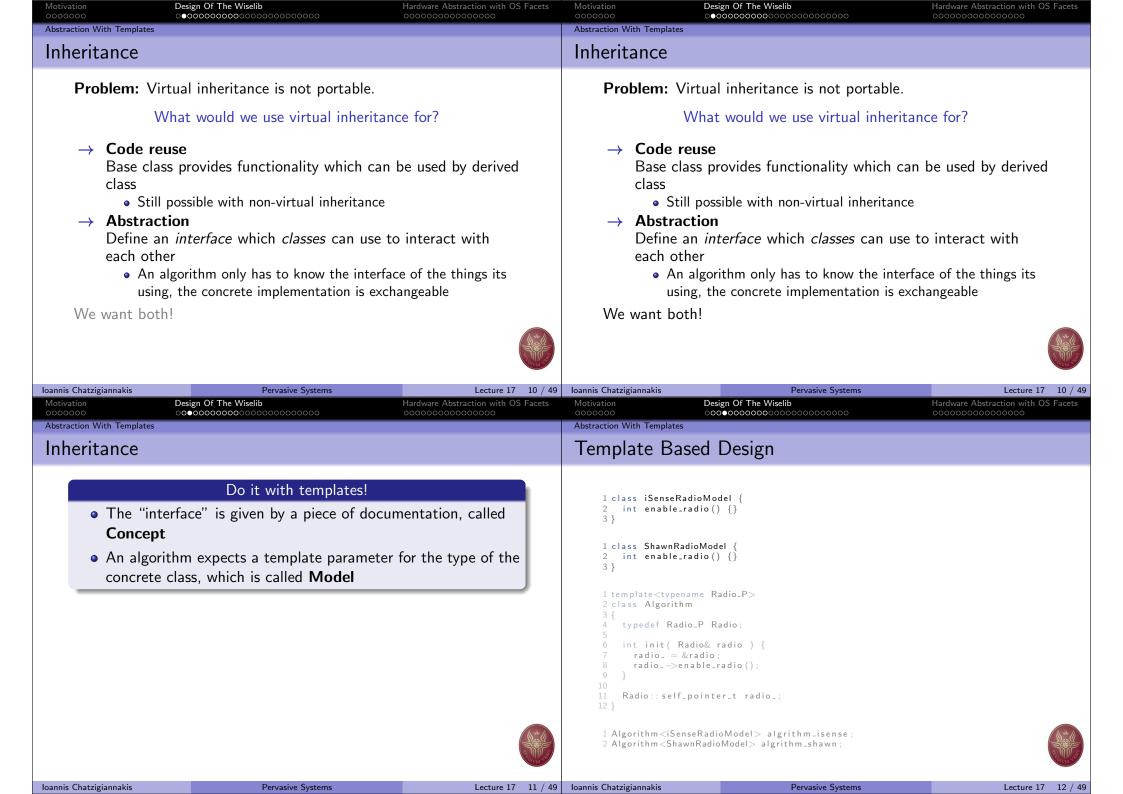


| Motivation<br>●000000<br>A Library Of Algorithr                       | Design Of The Wiselib<br>000000000000000000000000000000000000   |   | tion with OS Facets<br>0000                      | Motivation<br>●000000<br>A Library Of Algorithms  | Design Of The Wiselib<br>೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦  | Hardware Abstraction with OS Facets  |
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| annis Chatzigiannakis<br>lotivation<br>●00000<br>Library Of Algorithr | Design Of The Wiselib<br>000000000000000000000000000000000000   | Hardware Abstract   | Lecture 17 2 / 49<br>tion with OS Facets<br>0000 | Ioannis Chatzigiannakis<br>Motivation<br>oo●0000<br>A Library Of Algorithms   | Pervasive Systems Design Of The Wiselib 000000000000000000000000000000000000   | Lecture 17 2 /<br>Hardware Abstraction with OS Facet:<br>00000000000000000 |
| Solution  |   |   |  | The Wiselib   | IS   |  |
|   | The W   | iselib  |  | ● A C++<br>● Free (a  | + project<br>as in freedom), licensed under L  | _GPL   |
| A library   | / of about 50 algorithms, lot   | ts more to come! These  | are  | · ·   | a middleware (we will see later  |  |
| Con   | ensible<br>nbineable<br>changeable  |   |  |   | http://wiseli  | .b.org   |
| Currently   | y includes the following algo   | orithm categories   |  | There you'l   |  |  |
| • Cluste  | ering   | <ul> <li>Metrics</li> </ul>   |  |   | ocumentation Wiki<br>/iselib Sourcecode  |  |
| <ul> <li>Graph</li> </ul>   | n Coloring  | <ul> <li>Routing</li> </ul>   |  | • The B   |  |  |
| <ul> <li>Crypte</li> </ul>  |   | <ul> <li>Synchronization</li> </ul>   |  | <ul> <li>Instruct</li> </ul>  | tions on how to download & ir  | nstall the Wiselib   |
| <ul> <li>Energ</li> </ul>   | gy Preservation   | <ul> <li>Topology Control</li> </ul>  | A*4  |   |  | Mar 1  |
| <ul> <li>Locali</li> </ul>  | ization   | <ul> <li>Tracking</li> </ul>  | Story and  |   |  | States   |

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| .ibrary Of Algorithms<br>Viselib Distrib  | outions   |                   |   |                  | Platform Independence<br>Platform Inde   | pendence  |   |
| • New thing   | ssarily tested on   | change their inte | erface                                  |                  | use diffe<br>The Wis<br>So i<br>Whi<br>In lots o<br>But  | ientists all over the world work<br>rent experimentation environm<br>elib aims to be <b>versatile</b><br>t can be used for <b>different tasks</b><br>ch also require <b>different hardwar</b><br>f applications we need <b>heterog</b><br>do not want to write the same co<br>node type | ents<br>re<br>geneous nodes                                 |
|   | all supported p<br>will not change                            |                   |   |                  |  | want the Wiselib to be platfo   | rm independent!   |
| nis Chatzigiannakis   | Per   | vasive Systems    |   | ecture 17 5 / 49 | Ioannis Chatzigiannakis  | Pervasive Systems   | Lecture 17 6  |
|   | Design Of The Wiselib   | 0000000           | Hardware Abstraction                    | n with OS Facets | Motivation   | Design Of The Wiselib   | Hardware Abstraction with OS Fac                            |
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| ooo●O<br>tform Independence<br>Iatform Independence<br>iSense<br>iSense<br><u>Hardware</u><br>Jennic<br><u>Operating System</u><br>iSense<br>ROM / RAM                  | occooccooccooccooccooccooccooccooccooc                        | ScatterWeb MSB    | Contiki / TinyOS                        | n with OS Facets | Motivation<br>OCOCOCO<br>Platform Independence<br>Platform Independence<br>OCOCOCO<br>OCOCOCO<br>OCOCOCO<br>OCOCOCO<br>OCOCOCOC  | Design Of The Wiselib<br>COCOCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCC  | c memory<br>ent<br>ual inheritance, etc<br><cmath>)</cmath> |
| form Independence<br>form Independence<br>iSense<br>iSense<br><u>Hardware</u><br>Jennic<br>Operating System<br>iSense<br>ROM / RAM<br>128kB / 92kB<br>Memory Management | occooccooccooccooccooccooccooccooccooc                        | ScatterWeb MSB    | Contiki / TinyOS 48kB / 10kB            | n with OS Facets | Motivation<br>OCOCOCO<br>Platform Independence<br>Platform Independence<br>OCOCOCO<br>OCOCOCO<br>OCOCOCO<br>OCOCOCO<br>OCOCOCOC  | Design Of The Wiselib<br>cococococococococococococococococococo   | c memory<br>ent<br>ual inheritance, etc<br><cmath>)</cmath> |

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| emory Management  | Memory Management   |
| <pre>Platform independence demands:<br/>• No malloc/free or new/delete<br/>→ Data can be allocated in 3 ways:<br/>• Global<br/>• Static<br/>• On the stack (function-local)<br/>• Constructors of global/static variables will be called before<br/>main()<br/>•in undefined order!<br/>• That can be very undesirable:<br/>1 Radio radio;<br/>2 SomeAlgorithm algo(radio); // Might receive uninitialized radio!<br/>→ Provide init()/destruct() methods, call them manually<br/>→ Hide initialization method of system objects ("Facets")</pre> | <pre>Platform independence demands:     • No malloc/free or new/delete     → Data can be allocated in 3 ways:         • Global         • Static         • On the stack (function-local)     • Constructors of global/static variables will be called before         main()     •in undefined order!     • That can be very undesirable:         1 Radio radio;         2 SomeAlgorithm algo(radio); // Might receive uninitialized radio!     → Provide init()/destruct() methods, call them manually     → Hide initialization method of system objects ("Facets")</pre> |
| is Chatzigiannakis Pervasive Systems Lecture 17 9 /<br>ivation Design Of The Wiselib Hardware Abstraction with OS Face  | 49     Ioannis Chatzigiannakis     Pervasive Systems     Lecture 17       s     Motivation     Design Of The Wiselib     Hardware Abstraction with OS Fa  |
| nory Management   | Memory Management   |
| <pre>Platform independence demands:<br/>• No malloc/free or new/delete<br/>→ Data can be allocated in 3 ways:<br/>• Global<br/>• Static<br/>• On the stack (function-local)<br/>• Constructors of global/static variables will be called before<br/>main()<br/>•in undefined order!<br/>• That can be very undesirable:<br/><sup>1</sup>Radio radio;<br/><sup>2</sup>SomeAlgorithm algo(radio); // Might receive uninitialized radio!</pre>   | <pre>Platform independence demands:<br/>• No malloc/free or new/delete<br/>→ Data can be allocated in 3 ways:<br/>• Global<br/>• Static<br/>• On the stack (function-local)<br/>• Constructors of global/static variables will be called before<br/>main()<br/>•in undefined order!<br/>• That can be very undesirable:<br/><sup>1</sup>Radio radio;<br/><sup>2</sup>SomeAlgorithm algo(radio); // Might receive uninitialized radio!</pre>   |
| <ul> <li>→ Provide init()/destruct() methods, call them manually</li> <li>→ Hide initialization method of system objects ("Facets")</li> </ul>  | → Provide init()/destruct() methods, call them manually<br>→ Hide initialization method of system objects ("Facets")  |
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| Motivation         Design Of The Wiselib           0000000         000000000000000000000000000000000000   | Hardware Abstraction with OS Facets  | Motivation         Design Of The Wiselib           0000000         000000000000000000000000000000000000   | Hardware Abstraction with OS Facets |
|---|--|---|-------------------------------------|
| Abstraction With Templates Template Based Design  |  | Abstraction With Templates<br>Template Based Design   |                                     |
| <pre>1 class iSenseRadioModel { 2 int enable_radio() {} 3 } 1 class ShawnRadioModel { 2 int enable_radio() {} 3 }</pre>   |  | <pre>1 class iSenseRadioModel { 2 int enable_radio() {} 3 } 1 class ShawnRadioModel { 2 int enable_radio() {} 3 }</pre>   |                                     |
| <pre>1 template<typename radio_p=""> 2 class Algorithm 3 { 4 typedef Radio_P Radio; 5 6 int init( Radio&amp; radio ) { 7 radio_ = &amp;radio 8 radio&gt;enable_radio(); 9 } 10 11 Radio::self_pointer_t radio_; 12 }</typename></pre> |  | <pre>1 template<typename radio_p=""> 2 class Algorithm 3 { 4 typedef Radio_P Radio; 5 6 int init( Radio&amp; radio ) { 7 radio_ = &amp;radio 8 radio&gt;enable_radio(); 9 } 10 11 Radio::self_pointer_t radio_; 12 }</typename></pre>   |                                     |
| 1 Algorithm <isenseradiomodel> algrithm_isense;<br/>2 Algorithm<shawnradiomodel> algrithm_shawn;</shawnradiomodel></isenseradiomodel>   | and the second sec | 1 Algorithm <isenseradiomodel> algrithm_isense<br/>2 Algorithm<shawnradiomodel> algrithm_shawn;</shawnradiomodel></isenseradiomodel>  | e ;                                 |
| oannis Chatzigiannakis Pervasive Systems<br>Motivation Design Of The Wiselib  | Lecture 17 12 / 49<br>Hardware Abstraction with OS Facets  | Ioannis Chatzigiannakis         Pervasive Syst           Motivation         Design Of The Wiselib   | Hardware Abstraction with OS Facets |
| 00000000     000000000000000000000000000000000000   | 000000000000000000000000000000000000000  | OOOOOOOOOOOOOOOOOOOOOOOOOOOOOOOOOOOO  | > 00000000000000                    |
| Template Based Design   |  | Template Based Design   |                                     |
| 1 class iSenseRadioModel {<br>2 int enable_radio() {}<br>3 }  |  | <pre>1 class iSenseRadioModel { 2 int enable_radio() {} 3 }</pre>   |                                     |
| <pre>1 class ShawnRadioModel { 2 int enable_radio() {} 3 }</pre>  |  | <pre>1 class ShawnRadioModel { 2     int enable_radio() {} 3 }</pre>  |                                     |
| <pre>1 template<typename radio_p=""> 2 class Algorithm 3 { 4 typedef Radio_P Radio; 5 6 int init( Radio&amp; radio ) { 7 radio_ = &amp;radio 8 radio&gt;enable_radio(); </typename></pre>   |  | <pre>1 termilate Commany Radio_P &gt; 2 class Algorithm 3 { 4 typedef Radio_P Radio; 5 6 int init( Radio&amp; radio ) { 7 radio_ = &amp; radio; 8 8 radio = radio; 8 radio = dia combine radio; 9 radio = ra</pre> |                                     |
|   |  | 8 radio_—>enable_radio();   |                                     |
| 9 }<br>10<br>11 <mark>Radio::self_pointer_t_radio_;</mark><br>12 }  |  | 9 }<br>10<br>11 <mark>Radio::self_pointer_t_radio_;</mark><br>12 }  |                                     |
| 10<br>11 Radio::self_pointer_t_radio_;  |  | 10<br>11 Radio::self_pointer_t_radio_;  | e ;                                 |

| 0000000<br>Abstraction | 14/2-1 | <b>T</b> 1. |
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|                        |        |             |
| Motivation             |        |             |

Design Of The Wiselib

Hardware Abstraction with OS Facets

Abstraction

#### Concept

- Describes behaviour of components
- E.g. "A Radio has a void send(char\*) method"
- Only documentation

## Model

- Actual class
- Implements any number of concepts
- E.g. A routing protocol may implement the radio concept
- ...so it can be used like one

# Design Of The Wiselib

Hardware Abstraction with OS Facets

# Abstraction With Templates Abstraction

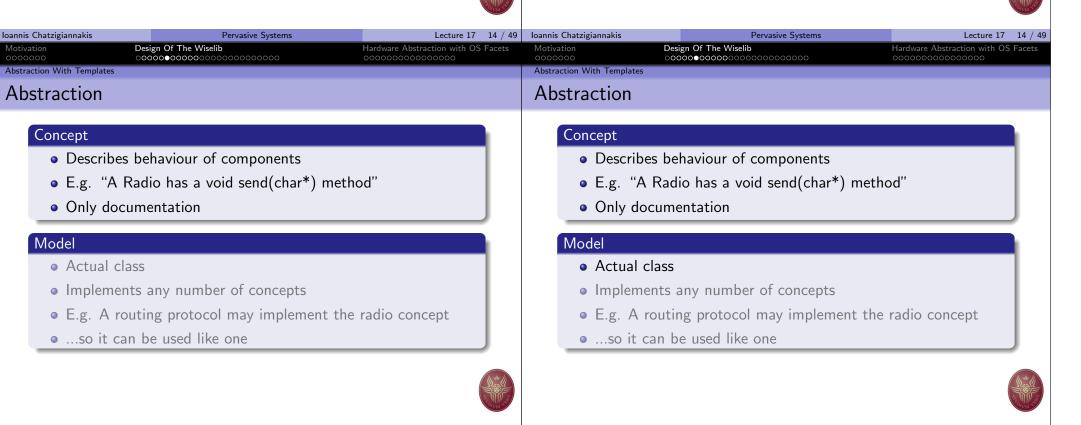
Motivation

### Concept

- Describes behaviour of components
- E.g. "A Radio has a void send(char\*) method"
- Only documentation

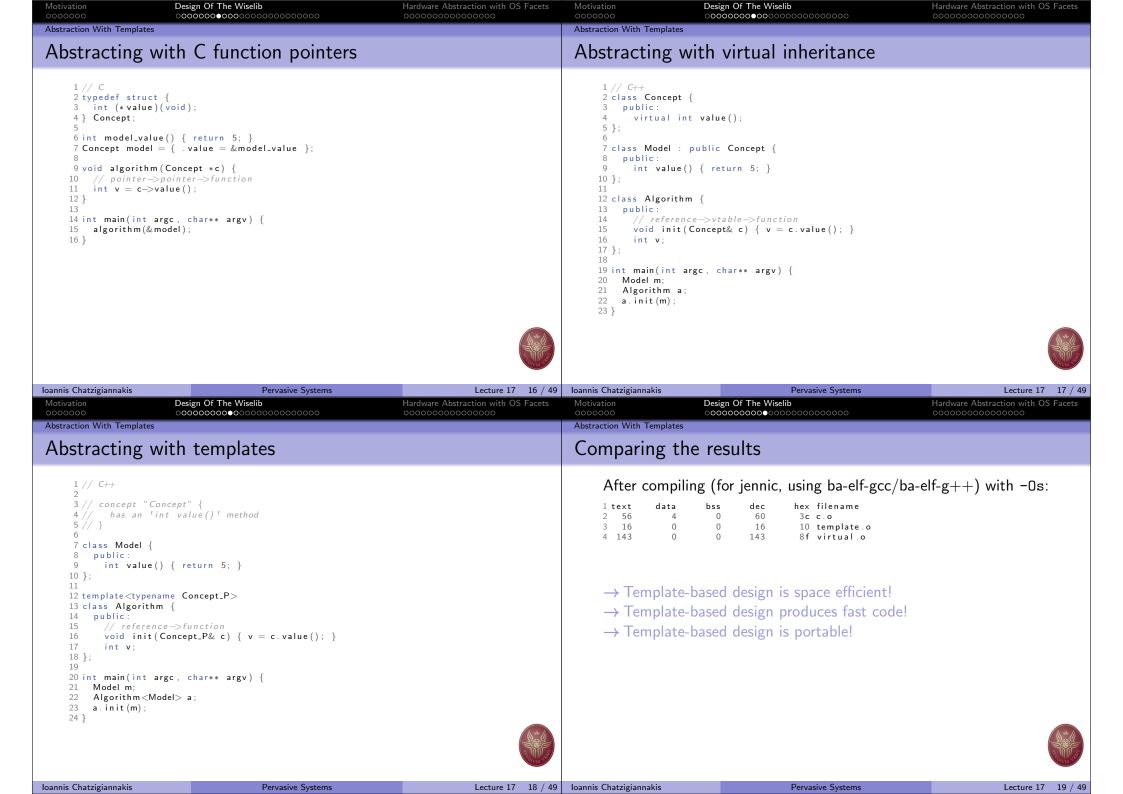
### Model

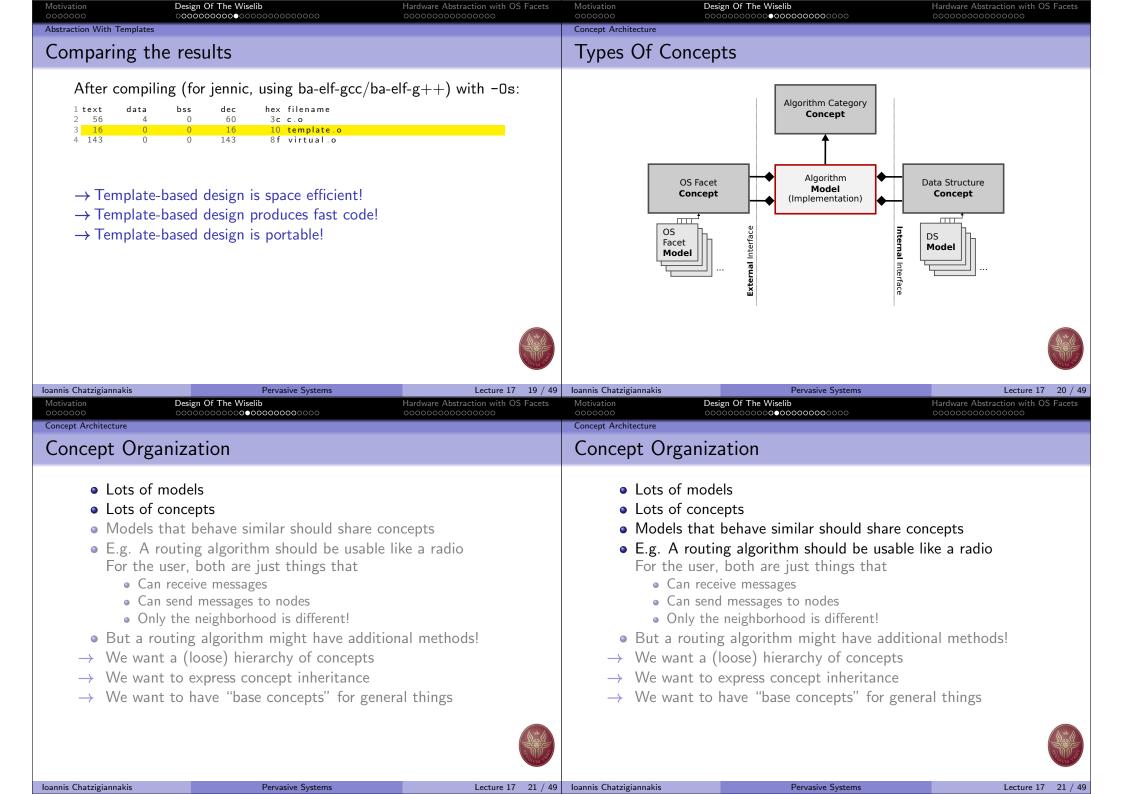
- Actual class
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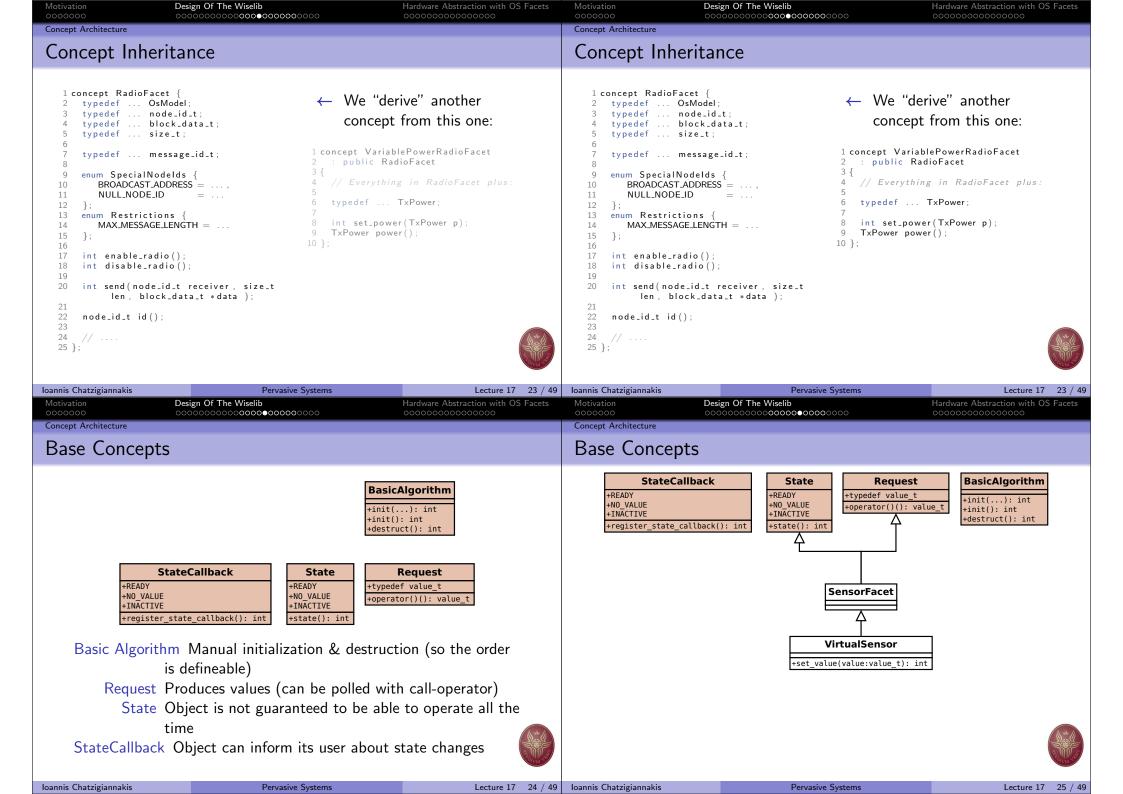
| Notivation   | Design Of The Wiselib<br>○○○○○●○○○○○○○○○○○○○○○○○○○○   | Hardware Abstraction with OS Facets   | Motivat<br>00000            | 00   | Design Of The Wiselib<br>○0000€00000000000000000000000000000000  | Hardware Abstraction with C      | /S Face |
|--|---|---|-----------------------------|--|--|----------------------------------|---------|
| Abstraction With Tem   |   |   |                             | tion With Template   | 25   |                                  |         |
| <ul> <li>E.g.</li> <li>Only</li> </ul> Model <ul> <li>Actu</li> <li>Imp</li> <li>E.g.</li> </ul> | cribes behaviour of components<br>. "A Radio has a void send(char<br>y documentation<br>ual class<br>lements any number of concepts<br>. A routing protocol may implem<br>o it can be used like one |   |                             | <ul> <li>E.g. ",</li> <li>Only d</li> </ul> Model <ul> <li>Actual</li> <li>Implen</li> <li>E.g. A</li> </ul> | bes behaviour of compone<br>A Radio has a void send(c<br>locumentation<br>class<br>nents any number of conce<br>routing protocol may imp<br>can be used like one | har*) method"<br>epts            |         |
| nis Chatzigiannakis<br>tivation<br>00000<br>straction With Tem<br>bstractio                      | Design Of The Wiselib<br>○0000●00000000000000000000000000000000   | Lecture 17 14 / 49<br>Hardware Abstraction with OS Facets<br>000000000000000000000000000000000000 | Motivat<br>00000<br>Abstrac | 100<br>tion With Template  | Pervasive Syste<br>Design Of The Wiselib<br>000000-000000000000000000000000000000  | Hardware Abstraction with C      |         |
| • E.g.   | cribes behaviour of components<br>. "A Radio has a void send(char<br>y documentation  | *) method"  |                             | • In   | are other ways to provide<br>C, one would usually abstra<br>C++ one would use <b>virtua</b> l  | ct with <b>function pointers</b> |         |
| • Imp<br>• E.g.  | ual class<br>lements any number of concepts<br>. A routing protocol may implem<br>o it can be used like one   |   |                             | Ho   | ow do they compare to the  | e template approach?             |         |
|  |   |   |                             |  |  |                                  |         |

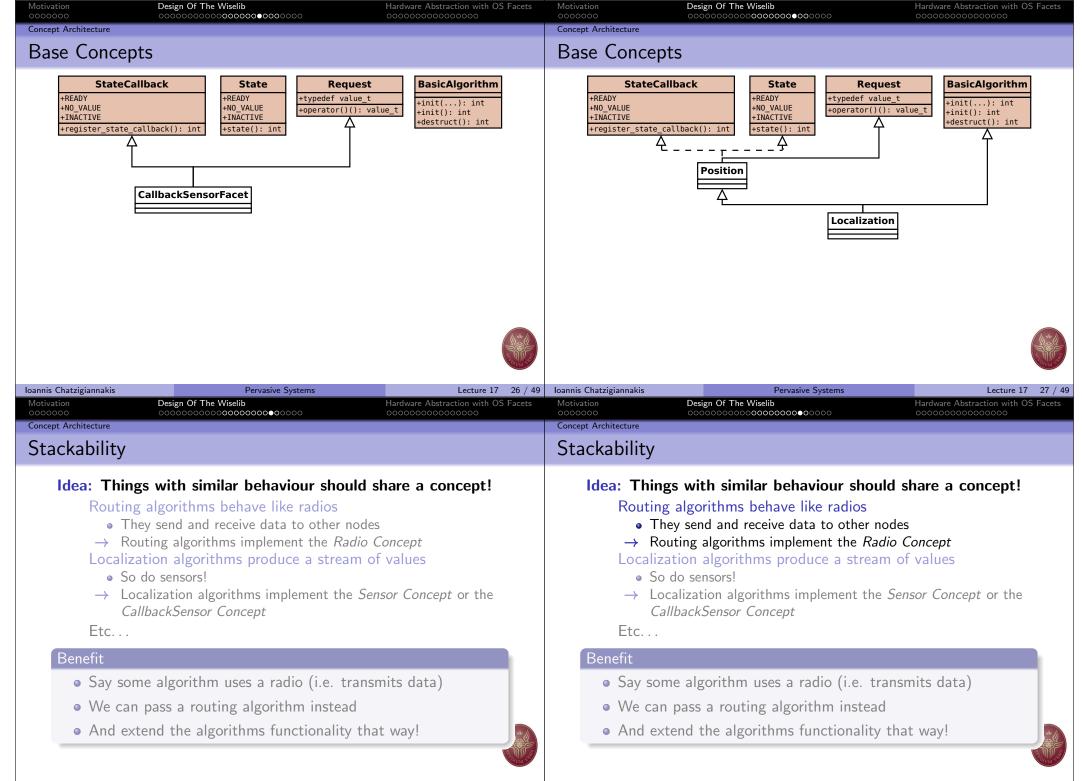
Ioannis Chatzigiannakis



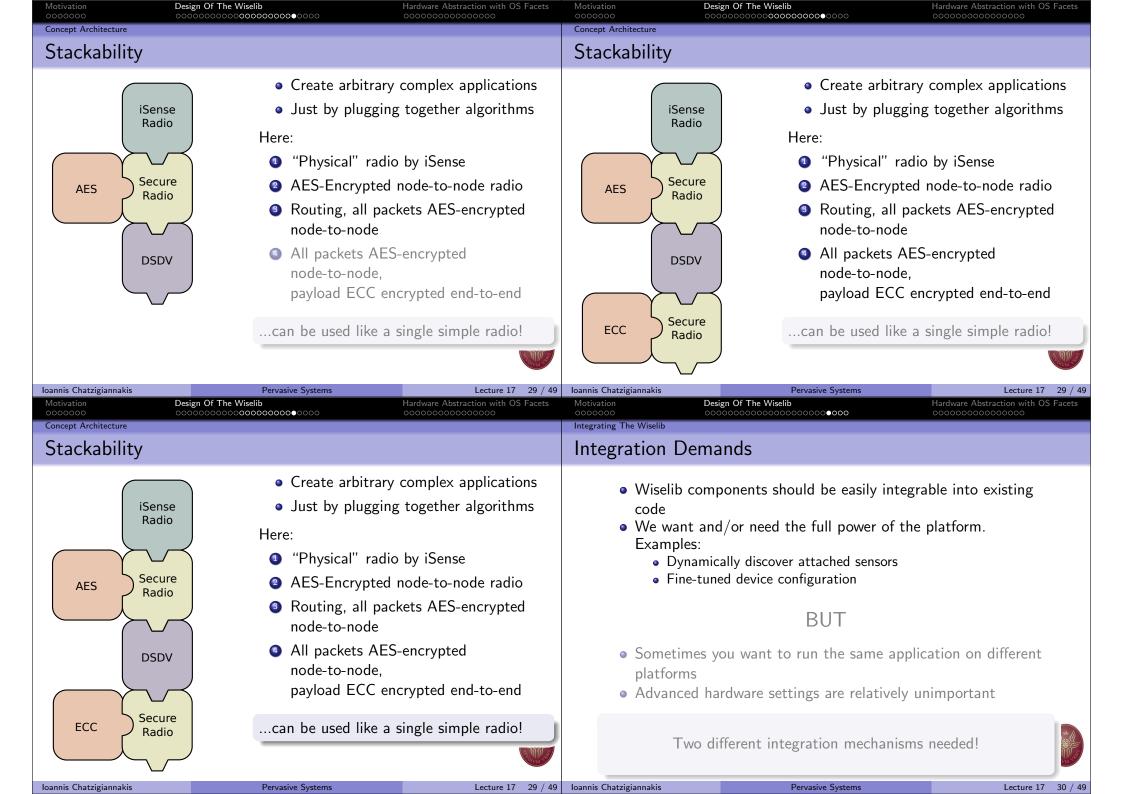


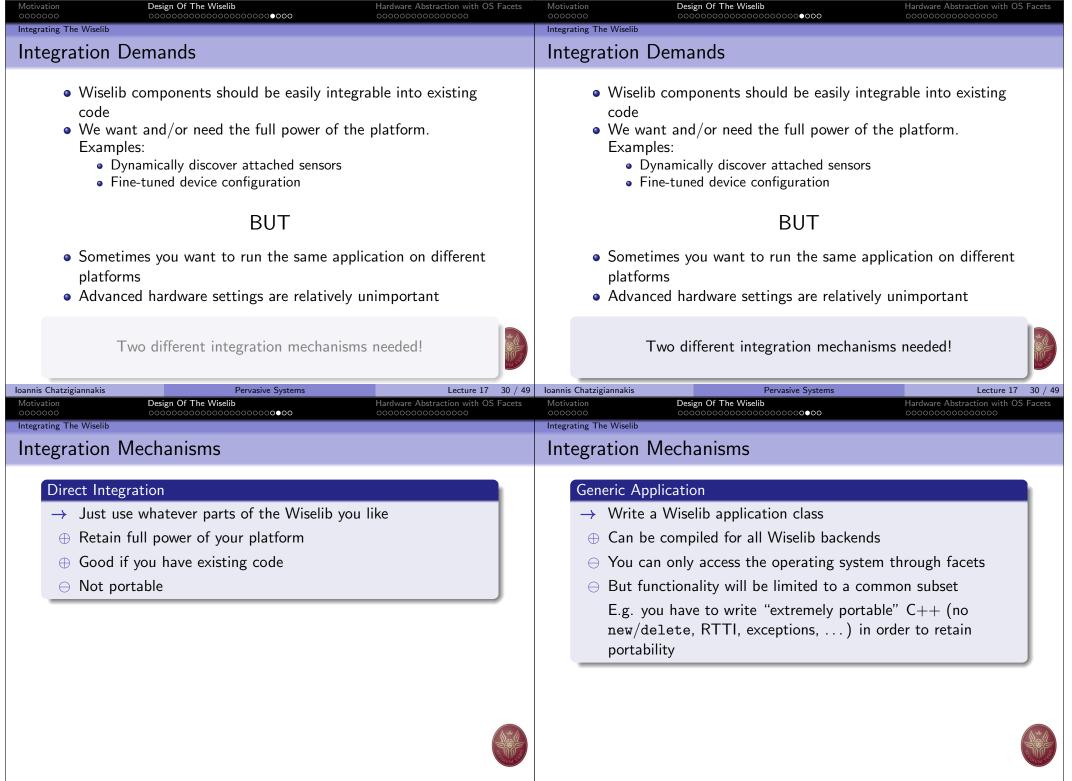
| Motivation<br>0000000<br>Concept Architecture   | Design Of The Wiselib<br>०००००००००० <b>००००००००</b> ००००  | Hardware Abstraction with OS Facets   | Motivation<br>0000000  | Design Of The Wiselib<br>○○○○○○○○○●●○○○○○○○○○○○  | Hardware Abstraction with OS Fa   |
|---|---|---|--|--|---|
|   | rganization   |   | Concept Architecture<br>Concept Org  | anization  |   |
| • Lots<br>• Mod<br>• E.g.<br>For t<br>•<br>•<br>•<br>•<br>•<br>•<br>•<br>•<br>•<br>•<br>•<br>•<br>•<br>•<br>•<br>•<br>•<br>•<br>• | of models<br>of concepts<br>els that behave similar should shar<br>A routing algorithm should be usa<br>the user, both are just things that<br>Can receive messages<br>Can send messages to nodes<br>Only the neighborhood is different!<br>a routing algorithm might have ad<br>want a (loose) hierarchy of concept<br>want to express concept inheritance<br>want to have "base concepts" for g | ble like a radio<br>ditional methods!<br>ts<br>e  | <ul> <li>E.g. A<br/>For the</li> <li>Ca</li> <li>Ca</li> <li>On</li> <li>But a r</li> <li>→ We war</li> <li>→ We war</li> </ul>  |  | able like a radio<br>dditional methods!<br>ots<br>ce                                    |
| annis Chatzigiannakis<br>Notivation<br>Soncept Architecture   | Pervasive Systems<br>Design Of The Wiselib<br>000000000000000000000000000000000000  | Lecture 17 21 / 49<br>Hardware Abstraction with OS Facets<br>000000000000000000000000000000000000 | Ioannis Chatzigiannakis<br>Motivation<br>ooooooo<br>Concept Architecture<br>The OsMode   | Pervasive Systems<br>Design Of The Wiselib<br>000000000000000000000000000000000000   | Lecture 17 2<br>Hardware Abstraction with OS Fa<br>000000000000000000000000000000000000 |
| <ul> <li>Lots</li> <li>Lots</li> <li>Mode</li> <li>E.g.</li> <li>For t</li> <li>But a</li> <li>→ We w</li> <li>→ We w</li> </ul>  | of models<br>of concepts<br>els that behave similar should shar<br>A routing algorithm should be usa<br>the user, both are just things that<br>Can receive messages<br>Can send messages to nodes<br>Only the neighborhood is different!<br>a routing algorithm might have ad<br>want a (loose) hierarchy of concept<br>want to express concept inheritance<br>want to have "base concepts" for g | ble like a radio<br>ditional methods!<br>ts<br>e  | 1 concept OsMoo<br>2 typedef<br>3 typedef<br>4 enum Return<br>5<br>6 typedef<br>7 typedef<br>9<br>10 static cons<br>WISELI<br>11 }<br>• Holds p<br>• Constan<br>• Inco<br>• Ma | del {<br>size_t;<br>block_data_t; // "byte"-like type fo<br>Values { SUCCESS =, ERR_UNSPEC =<br>Radio; // Wireless communication fac | <pre> }; TLE_ENDIAN or ess, size type, etc) NSPEC (unspecified error)</pre>             |
| annis Chatzigiannakis   | Pervasive Systems   | Lecture 17 21 / 49  | Ioannis Chatzigiannakis  | Pervasive Systems  | Lecture 17  |

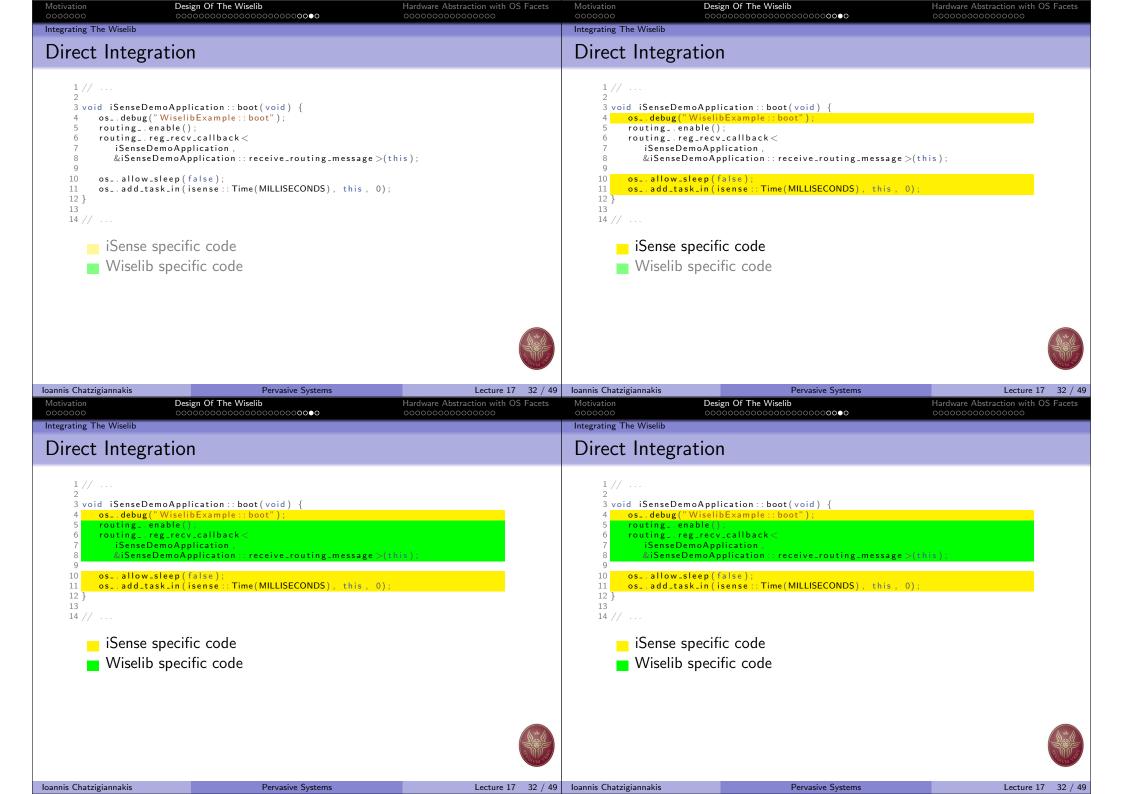




Ioannis Chatzigiannakis







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Design Of The Wiselib
                                                                     Hardware Abstraction with OS Facets
                                                                                                                               Design Of The Wiselib
                                                                                                                                                                           Hardware Abstraction with OS Facets
                                                                                                      Motivation
                         Integrating The Wiselib
                                                                                                      Integrating The Wiselib
Generic Application
                                                                                                      Generic Application
     1#include "external_interface/external_interface.h"
                                                                                                            1#include "external_interface/external_interface.h"
     2#include "external_interface/external_interface_testing.h"
                                                                                                            2 #include "external_interface/external_interface_testing.h"
     3 // ...
                                                                                                            3 // ...
     Δ
     5 typedef wiselib :: PCOsModel Os;
                                                                                                            5 typedef wiselib :: PCOsModel Os;
     6 class DemoApplication {
                                                                                                            6 class DemoApplication {
     7
         public
                                                                                                            7
                                                                                                               public
                                                                                                           8
     8
            void init(Os::AppMainParameter& amp) {
                                                                                                                 void init(Os::AppMainParameter& amp) {
              radio_ = & wiselib :: FacetProvider <Os, Os :: Radio > :: get_facet (amp);
     9
                                                                                                           9
                                                                                                                    radio_ = & wiselib :: FacetProvider < Os, Os :: Radio > :: get_facet (amp);
     10
              debug_ = & wiselib :: FacetProvider < Os, Os :: Debug > :: get_facet (amp);
                                                                                                           10
                                                                                                                    debug_ = & wiselib :: FacetProvider < Os, Os :: Debug > :: get_facet (amp);
     11
                                                                                                           11
     12
              algorithm_.init();
                                                                                                           12
                                                                                                                    algorithm_.init();
     13
                                                                                                           13
     14
              radio_->enable_radio();
                                                                                                           14
                                                                                                                    radio_->enable_radio();
                                                                                                           15
     15
              debug_->debug("Initialized.\n");
                                                                                                                   debug_->debug("Initialized.\n");
     16
                                                                                                           16
    17
                                                                                                           17
     18
         private
                                                                                                           18
                                                                                                               private
    19
           Os::Debug::self_pointer_t debug_;
                                                                                                           19
                                                                                                                 Os::Debug::self_pointer_t debug_;
     20
                                                                                                           20
           Os::Radio::self_pointer_t radio_;
                                                                                                                 Os::Radio::self_pointer_t radio_;
     21
           SomeAlgorithm algorithm_;
                                                                                                           21
                                                                                                                 SomeAlgorithm algorithm_;
     22 };
                                                                                                           22 };
     23
                                                                                                           23
                                                                                                           24 wiselib :: WiselibApplication <0s, DemoApplication > demo_app;
     24 wiselib :: WiselibApplication <Os, DemoApplication > demo_app;
     25 void application_main(Os::AppMainParameter& amp) {
                                                                                                           25 void application_main (Os:: AppMainParameter& amp) {
     26 demo_app.init(amp);
                                                                                                           26 demo_app.init(amp);
                                                                                                           27 }
     27 }
                                                                                                                                           Platform selection
Ioannis Chatzigiannakis
                                                                                   Lecture 17 33 / 49
                                                                                                     Ioannis Chatzigiannakis
                                                                                                                                                                                         Lecture 17 33 / 49
                                          Pervasive Systems
                                                                                                                                                Pervasive Systems
                         Design Of The Wiselib
                                                                     Hardware Abstraction with OS Facets
                                                                                                      Motivation
                                                                                                                               Design Of The Wiselib
                                                                                                                                                                           Hardware Abstraction with OS Facets
Motivation
                                             0000000
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Integrating The Wiselib
                                                                                                      Integrating The Wiselib
Generic Application
                                                                                                      Generic Application
                                                                                                            1#include "external_interface/external_interface.h"
     1#include "external_interface/external_interface.h"
     2#include "external_interface/external_interface_testing.h"
                                                                                                            2#include "external_interface/external_interface_testing.h"
     3 // ...
                                                                                                            3 // ...
     Λ
     5 typedef wiselib :: PCOsModel Os;
                                                                                                            5 typedef wiselib :: PCOsModel Os;
     6 class DemoApplication {
                                                                                                            6 class DemoApplication {
        public :
                                                                                                            7 public:
     7
     8
           void init(Os::AppMainParameter& amp) {
                                                                                                                  void init(Os::AppMainParameter& amp) {
                                                                                                           8
             radio_ = & wiselib :: FacetProvider < Os, Os :: Radio > :: get_facet (amp)
                                                                                                           9
                                                                                                                    radio_ = & wiselib :: FacetProvider < Os, Os :: Radio > :: get_facet (amp);
     10
             debug_ = & wiselib :: FacetProvider < Os, Os :: Debug > :: get_facet (amp)
                                                                                                           10
                                                                                                                    debug_ = & wiselib :: FacetProvider < Os, Os :: Debug > :: get_facet (amp);
     11
                                                                                                           11
                                                                                                           12
     12
              algorithm_.init()
                                                                                                                    algorithm_.init();
     13
                                                                                                           13
     14
              radio_->enable_radio();
                                                                                                           14
                                                                                                                    radio_->enable_radio();
                                                                                                           15
     15
              debug_->debug("Initialized.\n");
                                                                                                                   debug_->debug("Initialized.\n");
     16
                                                                                                           16
     17
                                                                                                           17
     18
         private
                                                                                                           18
                                                                                                               private
     19
           Os::Debug::self_pointer_t debug_;
                                                                                                           19
                                                                                                                 Os::Debug::self_pointer_t debug_;
     20
           Os::Radio::self_pointer_t radio_;
                                                                                                           20
                                                                                                                 Os::Radio::self_pointer_t radio_;
     21
           SomeAlgorithm algorithm_;
                                                                                                           21
                                                                                                                 SomeAlgorithm algorithm_;
     22 };
                                                                                                           22 };
     23
                                                                                                           23
                                                                                                           24 wiselib :: WiselibApplication < Os, DemoApplication > demo_app
     24 wiselib :: WiselibApplication <Os, DemoApplication > demo_app;
     25 void application_main(Os::AppMainParameter& amp) {
                                                                                                           25 void application_main (Os:: AppMainParameter& amp) {
     26 demo_app.init(amp);
                                                                                                           26
                                                                                                              demo_app.init(amp);
                                                                                                           27 }
     27 }
      Initialization: FacetProvider for OS facets / Manual for algorithms
                                                                                                                application main getting called by Wiselib \leftrightarrow OS adaptor
Ioannis Chatzigiannakis
                                                                                   Lecture 17 33 / 49
                                                                                                     Ioannis Chatzigiannakis
                                                                                                                                                                                         Lecture 17 33 / 49
                                          Pervasive Systems
                                                                                                                                                Pervasive Systems
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|  | Abstraction with OS Facets                       |  | Design Of The Wiselib<br>000000000000000000000000000000000000                      | Hardware Abstraction with OS Facet                   |
|--|--|--|--|--|
| Nhat is a Facet?   |  | What is a Facet  | t?   |  |
| <ul> <li>Connection between algorithms and OS</li> <li>OS Facets (Concepts) <ul> <li>OS Facet</li> <li>Radio Facet</li> <li>Timer Facet</li> <li></li> </ul> </li> <li>For each supported OS at least one model per facet</li> <li>iSenseOsModel</li> <li>ContikiRadioModel</li> <li>ShawnTimerModel</li> <li></li> </ul> <li>Possible to provide muliple models per facet <ul> <li>ContikiRimeRadioModel</li> <li>Contiki6LowPanRadioModel</li> <li></li> </ul> </li> | cet  | <ul> <li>OS Facets</li> <li>OS Facets</li> <li>OS Facets</li> <li>Radio</li> <li>Timer</li> <li></li> <li>For each s</li> <li>iSensed</li> <li>Contik</li> <li>Shawn</li> <li></li> <li>Possible to</li> <li>Contik</li> </ul> | cet<br>Facet<br>Facet<br>supported OS at least <b>one mode</b>                     |  |
|  | Lecture 17 34 / 49<br>Abstraction with OS Facets |  | Pervasive Systems<br>Design Of The Wiselib<br>000000000000000000000000000000000000 | Lecture 17 34 /<br>Hardware Abstraction with OS Face |
| oduction<br>/hat is a Facet?   |  | What is a Facet  |  |  |
| <ul> <li>Connection between algorithms and OS</li> <li>OS Facets (Concepts) <ul> <li>OS Facet</li> <li>Radio Facet</li> <li>Timer Facet</li> <li></li> </ul> </li> <li>For each supported OS at least one model per facet</li> <li>iSenseOsModel</li> <li>ContikiRadioModel</li> <li>ShawnTimerModel</li> <li></li> </ul> <li>Possible to provide muliple models per facet <ul> <li>ContikiRimeRadioModel</li> <li>ContikiGLowPanRadioModel</li> <li></li> </ul> </li> | cet  | <ul> <li>OS Facets</li> <li>OS Facets</li> <li>OS Facets</li> <li>Radio</li> <li>Timer</li> <li></li> <li>For each s</li> <li>iSensed</li> <li>Contik</li> <li>Shawn</li> <li></li> <li>Possible to</li> <li>Contik</li> </ul> | cet<br>Facet<br>Facet<br>supported OS at least <b>one mode</b>                     |  |
| nnis Chatzigiannakis Pervasive Systems   | Lecture 17 34 / 49                               | Ioannis Chatzigiannakis  | Pervasive Systems  | Lecture 17 34  |

| Motivation<br>000000                                    |  |   | he Wiselib<br>0000000000  | 000000  | 00  |  |   |                                      | ostraction with OS Facets                        | Motivation<br>0000000   | Design Of The Wiselib<br>000000000000000000000000000000000000  | Hardware Abstraction with OS Face   |
|---|--|---|---|---|---|--|---|--------------------------------------|--|---|--|---|
| OS Facet Overview                                       |  |   |   |   |   |  |   |                                      | Exchangeability with Algorithms                  |   |  |   |
|   | WP2 OSA<br>Contiki<br>TinyOS<br>iSense<br>ScatterWeb<br>Shawn<br>(⊕ = fully sup  | $ \begin{array}{c} \oplus\\ $ |   |   | $ \begin{array}{c} \oplus\\ $ | ⊕•   | ○<br>⊕  | Positif                              | 4/7<br>5/7<br>2/7<br>7/7<br>4/7<br>6/7           | <ul> <li>Pass</li> <li>Exan</li> <li></li> <li< th=""><th>c design issue: Flexibility<br/>an algorithm where a facet<br/>nples<br/>Pass routing algorithm where rac<br/>⇒ Enable flexible multihop neig<br/>Pass time-synchronization algori<br/>⇒ Enable system-wide time basi<br/>Pass localization algorithm wher<br/>⇒ Only some nodes in the netwo<br/>Pass routing-based debug model<br/>⇒ Debug nodes that are not con<br/>antage: Totally transparent for</th><th>dio is expected<br/>hborhoods<br/>thm where clock is expected<br/>is<br/>e position is expected<br/>ork need to know their position<br/>where debug facet is expected<br/>nnected to a gateway position</th></li<></ul> | c design issue: Flexibility<br>an algorithm where a facet<br>nples<br>Pass routing algorithm where rac<br>⇒ Enable flexible multihop neig<br>Pass time-synchronization algori<br>⇒ Enable system-wide time basi<br>Pass localization algorithm wher<br>⇒ Only some nodes in the netwo<br>Pass routing-based debug model<br>⇒ Debug nodes that are not con<br>antage: Totally transparent for | dio is expected<br>hborhoods<br>thm where clock is expected<br>is<br>e position is expected<br>ork need to know their position<br>where debug facet is expected<br>nnected to a gateway position  |
| innis Chatzigiann<br>otivation<br>ocococo<br>troduction | akis<br>Desig  | n Of Tł   | Perv<br>he Wiselib<br>000000000   | vasive Sys  | stems   |  | Har   | dware A                              | Lecture 17 35 / 49<br>Destraction with OS Facets | Ioannis Chatzigiannakis<br>Motivation<br>οοοοοοο<br>Introduction<br>Exchanαea   | Pervasive Systems<br>Design Of The Wiselib<br>000000000000000000000000000000000000   | Lecture 17 36<br>Hardware Abstraction with OS Face<br>00€0000000000000000000000000000000000   |
| • B<br>• Pa<br>• E                                      | asic design i<br>ass an algor<br>kamples<br>● Pass routi<br>⇒ Enable<br>● Pass time<br>⇒ Enable<br>● Pass local<br>⇒ Only so<br>● Pass routi | ing a<br>ing a<br>flexi<br>syst<br>izatio<br>ome<br>ing-b   | :: Flexib<br>n where<br>Igorithm<br>ible multi<br>chronizati<br>chronizati<br>con algorit<br>nodes in<br>pased deb<br>es that a | ility<br>a fac<br>where<br>ihop n<br>ion alg<br>time<br>thm w<br>the ne<br>bug mo | e radio<br>neight<br>gorith<br>basis<br>/here<br>etwor<br>odel v  | o is exp<br>oorhood<br>m whe<br>positio<br>k need<br>where d<br>nected | bected<br>ds<br>re clo<br>to kn<br>ebug<br>to a g | ock is<br>expect<br>low the<br>facet | ed<br>neir position                              | • Basia<br>• Pass<br>• Exan<br>•  | c design issue: <b>Flexibility</b><br>an <b>algorithm</b> where a <b>facet</b>   | dio is expected<br>hborhoods<br>thm where clock is expected<br>is<br>re position is expected<br>ork need to know their position<br>where debug facet is expected<br>nnected to a gateway position |
| nnis Chatzigiann  |  |   |   | vasive Sys  |   |  |   |                                      | Lecture 17 36 / 49                               | Ioannis Chatzigiannakis   | Pervasive Systems  | Lecture 17 36   |

| Motivation<br>0000000  | Design Of The Wiselib<br>೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦   | Hardware Abstraction with OS Facets<br>00●0000000000000000000000000000000000   | Motivation<br>0000000   | Design Of The Wiselib<br>೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦   | Hardware Abstraction with OS Facets  |  |  |
|--|--|--|---|---|--|--|--|
| Introduction<br>Exchange                                     | eability with Algorithms   |  | Exchangeab  | ility with Algorithms   |  |  |  |
| • Pa<br>• E>   | <ul> <li>asic design issue: Flexibility</li> <li>ass an algorithm where a facet is examples</li> <li>Pass routing algorithm where radio i ⇒ Enable flexible multihop neighbor</li> <li>Pass time-synchronization algorithm ⇒ Enable system-wide time basis</li> <li>Pass localization algorithm where pot ⇒ Only some nodes in the network in</li> <li>Pass routing-based debug model where ⇒ Debug nodes that are not connected wantage: Totally transparent for a</li> </ul> | s expected<br>hoods<br>where clock is expected<br>sition is expected<br>need to know their position<br>ere debug facet is expected<br>tted to a gateway position | <ul> <li>Exchangeability with Algorithms</li> <li>Basic design issue: Flexibility</li> <li>Pass an algorithm where a facet is expected</li> <li>Examples <ul> <li>Pass routing algorithm where radio is expected</li> <li>Enable flexible multihop neighborhoods</li> <li>Pass time-synchronization algorithm where clock is expected</li> <li>Enable system-wide time basis</li> <li>Pass localization algorithm where position is expected</li> <li>Only some nodes in the network need to know their position</li> <li>Pass routing-based debug model where debug facet is expected</li> <li>Debug nodes that are not connected to a gateway position</li> </ul> </li> <li>Advantage: Totally transparent for algorithm</li> </ul> |   |  |  |  |
| oannis Chatzigianna<br>Motivation<br>ooooooo<br>Introduction | Design Of The Wiselib<br>000000000000000000000000000000000000  | Lecture 17 36 / 49<br>Hardware Abstraction with OS Facets<br>OO●○○○○○○○○○○○  | Ioannis Chatzigiannakis<br>Motivation<br>0000000<br>Introduction  | Pervasive Systems<br>Design Of The Wiselib<br>000000000000000000000000000000000000  | Lecture 17 36 /<br>Hardware Abstraction with OS Face<br>000000000000000000000000000000000000 |  |  |
| Exchange   | eability with Algorithms   |  | Exchangeab  | ility with Algorithms   |  |  |  |
| • Pa   | asic design issue: <b>Flexibility</b><br>ass an <b>algorithm</b> where a <b>facet</b> is examples<br>• Pass routing algorithm where radio i<br>$\Rightarrow$ Enable flexible multihop neighbor<br>• Pass time-synchronization algorithm<br>$\Rightarrow$ Enable system-wide time basis<br>• Pass localization algorithm where po<br>$\Rightarrow$ Only some nodes in the network p   | s expected<br>hoods<br>where clock is expected<br>sition is expected   | ● Pass a<br>● Examp<br>● P<br>=<br>● P<br>=<br>● P  | design issue: <b>Flexibility</b><br>an <b>algorithm</b> where a <b>facet</b> is end<br>ples<br>ass routing algorithm where radio<br>> Enable flexible multihop neighbor<br>ass time-synchronization algorithm<br>> Enable system-wide time basis<br>ass localization algorithm where pre-<br>> Only some nodes in the network | is expected<br>rhoods<br>where clock is expected<br>osition is expected                      |  |  |

- $\Rightarrow$  Only some nodes in the network need to know their position
- Pass routing-based debug model where debug facet is expected
  - $\Rightarrow$  Debug nodes that are not connected to a gateway position
- Advantage: Totally transparent for algorithm

• Advantage: Totally transparent for algorithm

• Pass routing-based debug model where debug facet is expected

 $\Rightarrow$  Debug nodes that are not connected to a gateway position

```
Hardware Abstraction with OS Facets
                                                              Hardware Abstraction with OS Facets
Motivation
                                                                                           Motivation
                                                              Important Facets
                                                                                           Important Facets
The OS Facet
                                                                                           The OS Facet
     1 concept OsFacet
                                                                                                1 concept OsFacet {
     2 typedef ... size_t;
                                                                                                2 typedef ... size_t;
       typedef ... block_data_t; // "byte"-like type for buffers
                                                                                                   typedef ... block_data_t; // "byte"-like type for buffers
     3
        enum ReturnValues { SUCCESS, EUNSPEC, ... }; // Define constants for return
                                                                                                   enum ReturnValues { SUCCESS, EUNSPEC, ... }; // Define constants for return
             values
                                                                                                         values
     6
        typedef ... Radio; // Wireless communication facet
                                                                                                6
                                                                                                   typedef ... Radio; // Wireless communication facet
        typedef ... Timer;
                                                                                                    typedef ... Timer;
     7
        typedef ... Debug; // Send debug messages
                                                                                                   typedef ... Debug; // Send debug messages
     8
                                                                                                8
                                                                                                Q
        static const Endianess endianess; // WISELIB_LITTLE_ENDIAN or
                                                                                                  static const Endianess endianess; // WISELIB_LITTLE_ENDIAN or
    10
                                                                                               10
             WISELIB_BIG_ENDIAN
                                                                                                         WISELIB_BIG_ENDIAN
    11 }
                                                                                               11 }
        • Only facet which does not need to be instantiated
                                                                                                   • Only facet which does not need to be instantiated
        • Provide type definitions and constants
                                                                                                   • Provide type definitions and constants
        • Platform properties (endianess, size type, ...)
                                                                                                   • Platform properties (endianess, size type, ...)

    Constants for return values.

    Constants for return values

             • Include at least SUCCESS and ERR_UNSPEC (unspecified error)

    Include at least SUCCESS and ERR_UNSPEC (unspecified error)

             • May/will include more, similar to errno
                                                                                                         • May/will include more, similar to errno
                                                                                                   • Default types for basic OS Facets
        • Default types for basic OS Facets
                                                                          Lecture 17 37 / 49
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                                                                                                                                                                      Lecture 17 37 / 49
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                                     Pervasive Systems
                                                                                                                                 Pervasive Systems
                      Design Of The Wiselib
                                                              Hardware Abstraction with OS Facets
                                                                                           Motivation
                                                                                                                  Design Of The Wiselib
                                                                                                                                                         Hardware Abstraction with OS Facets
Motivation
                                                              Important Facets
                                                                                           Important Facets
The OS Facet
                                                                                           The OS Facet
     1 concept OsFacet {
                                                                                                1 concept OsFacet {
     2 typedef ... size_t;
                                                                                                2 typedef ... size_t;
       typedef ... block_data_t; // "byte"-like type for buffers
                                                                                                   typedef ... block_data_t; // "byte"-like type for buffers
     3
        enum ReturnValues { SUCCESS, EUNSPEC, ... }; // Define constants for return
                                                                                                   enum ReturnValues { SUCCESS, EUNSPEC, ... }; // Define constants for return
             values
                                                                                                         values
     5
                                                                                                5
     6
        typedef ... Radio; // Wireless communication facet
                                                                                                6
                                                                                                   typedef ... Radio; // Wireless communication facet
        typedef ... Timer
                                                                                                    typedef ... Timer;
     7
        typedef ... Debug; // Send debug messages
                                                                                                   typedef ... Debug; // Send debug messages
     8
                                                                                                8
                                                                                                0
    10
        static const Endianess endianess; // WISELIB_LITTLE_ENDIAN or
                                                                                               10
                                                                                                   static const Endianess endianess; // WISELIB_LITTLE_ENDIAN or
             WISELIB_BIG_ENDIAN
                                                                                                         WISELIB_BIG_ENDIAN
    11 }
                                                                                               11 }
        • Only facet which does not need to be instantiated
                                                                                                   • Only facet which does not need to be instantiated
        • Provide type definitions and constants
                                                                                                   • Provide type definitions and constants
        • Platform properties (endianess, size type, ...)
                                                                                                   • Platform properties (endianess, size type, ...)

    Constants for return values

    Constants for return values

    Include at least SUCCESS and ERR_UNSPEC (unspecified error)

             • Include at least SUCCESS and ERR_UNSPEC (unspecified error)
             • May/will include more, similar to errno

    May/will include more, similar to errno

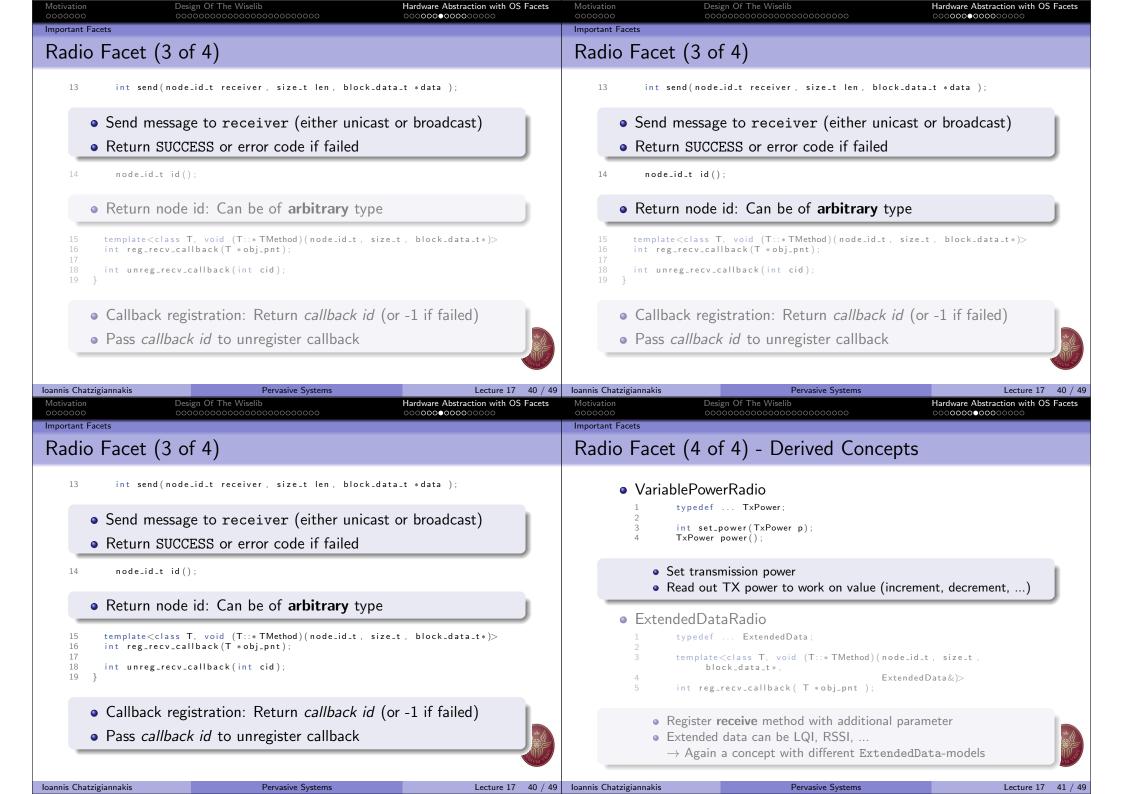
        • Default types for basic OS Facets

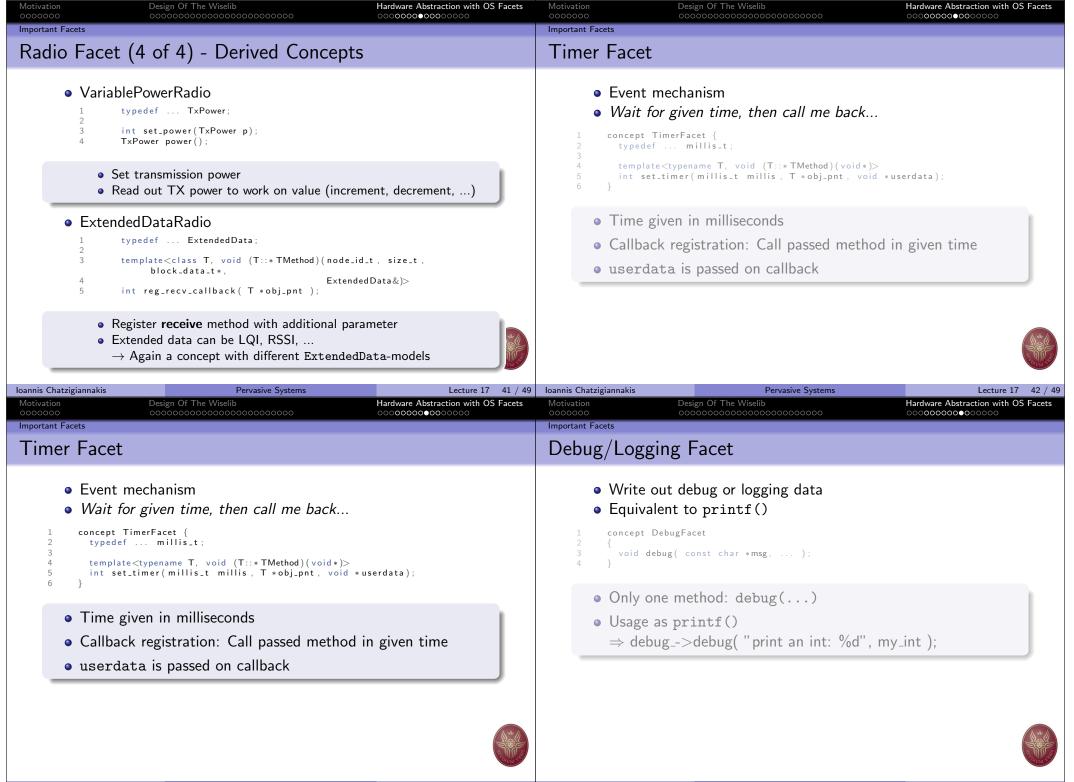
    Default types for basic OS Facets

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                                                                                                                                                                      Lecture 17 37 / 49
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                                      Pervasive Systems
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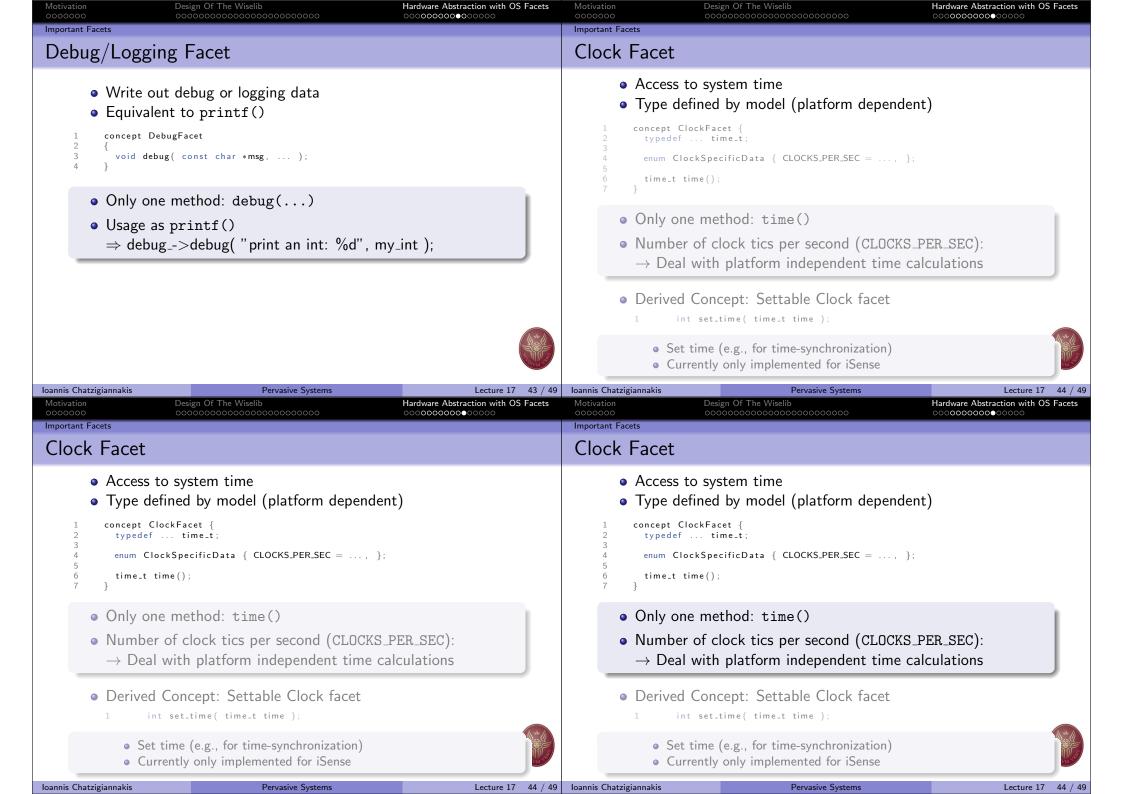
| Motivation<br>0000000  | Design Of The Wiselib<br>000000000000000000000000000000000000  | Hardware Abstraction with OS Facets   | Motivation<br>0000000  | Design Of The Wiselib<br>೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦  | Hardware Abstraction with OS Facets                    |
|--|--|---|--|---|--|
| mportant Facets<br>The OS Fa   | acet   |   | Important Facets<br>Radio Facet (  | (1 of 4)  |  |
| 3 typedef<br>4 enum Ret<br>val<br>5<br>6 typedef<br>7 typedef<br>9<br>10 static c<br>WIS<br>11 }<br>• Only<br>• Prov<br>• Platf<br>• Cons<br>• | SFacet {<br>size_t;<br>block_data_t; // "byte"-like type for<br>turnValues { SUCCESS, EUNSPEC, }; // I<br>lues<br>Radio; // Wireless communication face<br>Timer;<br>Debug; // Send debug messages<br>const Endianess endianess; // WISELIB_LITT<br>SELIB_BIG_ENDIAN<br>y facet which does <b>not need</b> to be<br>vide <b>type definitions</b> and <b>constan</b><br>form properties (endianess, size type<br>stants for return values<br>Include at least SUCCESS and ERR_UN<br>May/will include more, similar to error<br>oult types for basic OS Facets | Define constants for return<br>et<br>"LE_ENDIAN or<br>e instantiated<br>ts<br>De,)<br>ISPEC (unspecified error) | <ul> <li>Con</li> <li>Virt</li> <li>Send me</li> <li>Callback</li> <li>Provide <ul> <li>Nod</li> <li>E.g.</li> </ul> </li> </ul> | ssues<br>straction to underlying hardware<br>nplex routing algorithms<br><i>tual</i> radio providing <i>virtual</i> ids<br>essages to other nodes<br>(registration for received mes<br>node id (and its <b>type</b> !)<br>de id type is defined <b>per radio</b><br>., provide IP addresses, but run o<br>y restriction: Be passed to size  | ssages<br>on 16-bit addresses                          |
| oannis Chatzigiannakis<br>Motivation<br>0000000<br>Important Facets  | Pervasive Systems<br>Design Of The Wiselib<br>000000000000000000000000000000000000   | Lecture 17 37 / 49<br>Hardware Abstraction with OS Facets<br>○○○○●○○○○○○○○                                      | Ioannis Chatzigiannakis<br>Motivation<br>0000000<br>Important Facets   | Pervasive Systems<br>Design Of The Wiselib<br>000000000000000000000000000000000000  | Lecture 17 38 /<br>Hardware Abstraction with OS Facets |
| Radio Face   | et (1 of 4)  |   | Radio Facet (  | (1 of 4)  |  |
| • Send<br>• Callb<br>• Prov  | gn issues<br>Abstraction to underlying hardware ra<br>Complex routing algorithms<br><i>Virtual</i> radio providing <i>virtual</i> ids<br>I messages to other nodes<br>back registration for received messe<br>ride node id (and its <b>type</b> !)<br>Node id type is defined <b>per radio</b><br>E.g., provide IP addresses, but run or<br>Only restriction: Be passed to sizeo   | sages<br>n 16-bit addresses   | <ul> <li>Con</li> <li>Virt</li> <li>Send me</li> <li>Callback</li> <li>Provide <ul> <li>Nod</li> <li>E.g.</li> </ul> </li> </ul> | ssues<br>straction to underlying hardware<br>nplex routing algorithms<br><i>tual</i> radio providing <i>virtual</i> ids<br>essages to other nodes<br>c registration for received mes<br>node id (and its <b>type</b> !)<br>de id type is defined <b>per radio</b><br>., provide IP addresses, but run o<br>y restriction: Be passed to size | <b>ssages</b><br>on 16-bit addresses                   |
| oannis Chatzigiannakis   | Pervasive Systems  | Lecture 17 38 / 49  | Ioannis Chatzigiannakis  | Pervasive Systems   | Lecture 17 38 /  |

| Motivation<br>0000000                           | Design Of The Wiselib<br>000000000000000000000000000000000000  | Hardware Abstraction with OS Facets<br>○○○○●○○○○○○○○○○    | Motivation<br>0000000  | Design Of The Wiselib<br>೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦   | Hardware Abstraction with OS Facets<br>○○○○○●○○○○○○○○○○                |
|---|--|---|--|--|--|
| Radio Face                                      | et (1 of 4)  |   | Important Facets<br>Radio Face   | et (2 of 4)  |  |
| •<br>• Senc<br>• Call<br>• Prov<br>•            | ign issues<br>Abstraction to underlying hardware radi<br>Complex routing algorithms<br><i>Virtual</i> radio providing <i>virtual</i> ids<br>d messages to other nodes<br>back registration for received messsag<br>vide node id (and its <b>type</b> !)<br>Node id type is defined <b>per radio</b><br>E.g., provide IP addresses, but run on 1<br>Only restriction: Be passed to sizeof ( | ges<br>6-bit addresses                                    | 2 type<br>3 type<br>4 type<br>5 type<br>• Abili<br>• Mess<br>9 enum<br>10 enum<br>• Basic<br>• Max<br>11 int<br>12 int<br>• Turn | <pre>tr RadioFacet {     def node_id_t;     def block_data_t;     def block_data_t;     def size_t;     def message_id_t;  ty to provide arbitrary node ID sage ID type to identify received     SpecialNodelds { BROADCAST_ADDRESS =     Restrictions { MAX_MESSAGE_LENGTH =     c constants for broadcasting and     imal message length defined pe     enable_radio();     disable_radio();     on/off radio     rn SUCCESS or error code if fail</pre> | d messages<br>, NULL_NODE_ID = };<br>};<br>d unknown nodes<br>er radio |
| oannis Chatzigiannakis<br>Motivation<br>0000000 | Pervasive Systems<br>Design Of The Wiselib<br>000000000000000000000000000000000000   | Lecture 17 38 / 49<br>Hardware Abstraction with OS Facets | loannis Chatzigiannakis<br>Motivation<br>0000000   | Pervasive Systems Design Of The Wiselib 000000000000000000000000000000000000   | Lecture 17 39 /<br>Hardware Abstraction with OS Facets                 |
| Important Facets                                | et (2 of 4)  |   | Important Facets<br>Radio Face   |  |  |
| 1 conce<br>2 typ<br>3 typ<br>4 typ              | <pre>pt RadioFacet { edef node_id_t; edef block_data_t; edef size_t; edef message_id_t;</pre>  |   | 1 concep<br>2 type<br>3 type<br>4 type   | ot RadioFacet {<br>def node_id_t;<br>def block_data_t;<br>def size_t;<br>def message_id_t;   |  |
| • Mes   | ity to provide <b>arbitrary</b> node ID type<br>sage ID type to identify received mes<br><sup>n</sup> SpecialNodelds { BROADCAST_ADDRESS =,<br><sup>n</sup> Restrictions { MAX_MESSAGE_LENGTH =, };  | sages   | • Mess   | ty to provide <b>arbitrary</b> node ID<br>sage ID type to identify received<br>SpecialNodelds { BROADCAST_ADDRESS =<br>Restrictions { MAX_MESSAGE_LENGTH =   | d messages   |
| • Basi  | ic constants for broadcasting and unk<br>simal message length defined <b>per rad</b>   |   | • Basi   | c constants for broadcasting and<br>imal message length defined <b>pe</b>  | d unknown nodes  |
| 12 int<br>• Turr                                | enable_radio();<br>disable_radio();<br>n on/off radio<br>urn SUCCESS or error code if failed<br>Pervasive Systems  | Lecture 17 39 / 49  | 12 int<br>• Turn   | enable_radio();<br>disable_radio();<br>n on/off radio<br>ern SUCCESS or error code if fail<br>Pervasive Systems  | ed   |



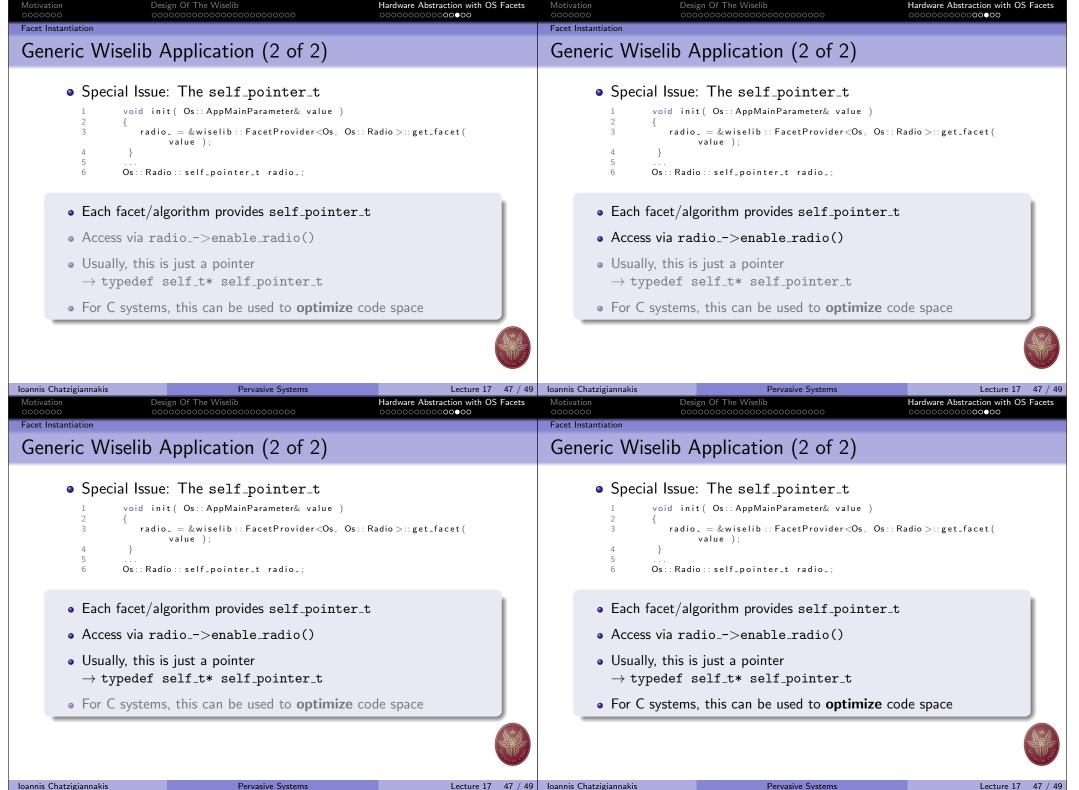


Ioannis Chatzigiannakis



| Motivation<br>0000000  | Design Of The Wiselib<br>000000000000000000000000000000000000                      | Hardware Abstraction with OS Facets   | 0000000 00   | esign Of The Wiselib<br>000000000000000000000000000000000000                       | Hardware Abstraction with OS Facets                    |
|--|--|---|--|--|--|
| Important Facets Clock Facet   |  |   | Facet Instantiation<br>Facet Structure   |  |  |
| <pre>• Access to system time<br/>• Type defined by model (platform dependent)<br/>concept ClockFacet {<br/>typedef time_t;<br/>enum ClockSpecificData { CLOCKS_PER_SEC =, };<br/>time_t time();<br/>}</pre>  |  |   | <ul> <li>Construction of facets system dependent         <ul> <li>Shawn: A facet needs to know to which processor it belongs</li> <li>iSense: Require access to isense::0s</li> <li>Contiki: Only calls to C functions</li> </ul> </li> <li>Each system with own constructors</li> <li>Generic Wiselib Application         <ul> <li>Construction must be hidden for user</li> </ul> </li> </ul>  |  |  |
| <ul> <li>Only one method: time()</li> <li>Number of clock tics per second (CLOCKS_PER_SEC):         <ul> <li>→ Deal with platform independent time calculations</li> </ul> </li> </ul>   |  | <ul> <li>Solution: Template based facet provider</li> <li>Direct Integration <ul> <li>Facets are known to user</li> <li>Directly initialize facets</li> </ul> </li> </ul> |  |  |  |
|  | d Concept: Settable Clock facet<br>nt set_time( time_t time );                     |   |  |  |  |
|  | t time (e.g., for time-synchronization)<br>urrently only implemented for iSense    |   |  |  |  |
| annis Chatzigiannakis<br>Activation<br>0000000   | Pervasive Systems<br>Design Of The Wiselib<br>000000000000000000000000000000000000 | Lecture 17 44 / 49<br>Hardware Abstraction with OS Facets   |  | Pervasive Systems<br>ssign Of The Wiselib<br>2000000000000000000000000000000000000 | Lecture 17 45 /<br>Hardware Abstraction with OS Facets |
| acet Instantiation   |  |   | Facet Instantiation  |  |  |
| acet Struct  | ure  |   | Facet Structure  |  |  |
| <ul> <li>Construction of facets system dependent <ul> <li>Shawn: A facet needs to know to which processor it belongs</li> <li>iSense: Require access to isense::0s</li> <li>Contiki: Only calls to C functions</li> </ul> </li> <li>Each system with own constructors</li> <li>Generic Wiselib Application <ul> <li>Construction must be hidden for user</li> <li>Solution: Template based facet provider</li> </ul> </li> <li>Direct Integration <ul> <li>Facets are known to user</li> <li>Directly initialize facets</li> </ul> </li> </ul> |  |   | <ul> <li>Construction of facets system dependent <ul> <li>Shawn: A facet needs to know to which processor it belongs</li> <li>iSense: Require access to isense::0s</li> <li>Contiki: Only calls to C functions</li> </ul> </li> <li>Each system with own constructors</li> <li>Generic Wiselib Application <ul> <li>Construction must be hidden for user</li> <li>Solution: Template based facet provider</li> </ul> </li> <li>Direct Integration <ul> <li>Facets are known to user</li> <li>Directly initialize facets</li> </ul> </li> </ul> |  |  |
| annis Chatzigiannakis  | Pervasive Systems  | Lecture 17 45 / 49  | loannis Chatzigiannakis  | Pervasive Systems  | Lecture 17 45 /  |

| Motivation<br>0000000<br>Facet Instantiation  | Design Of The Wiselib<br>೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦   | Hardware Abstraction with OS Facets<br>○○○○○○○○○●○○○○     | Motivation<br>0000000<br>Facet Instantiation  | Design Of The Wiselib<br>000000000000000000000000000000000000   | Hardware Abstraction with OS Facets<br>○○○○○○○○○○●○○○    |
|---|--|---|---|---|--|
| Facet Instantiation<br>Facet Structur   | re   |   | Generic Wisel   | lib Application (1 of 2)  |  |
| <ul> <li>Shaw</li> <li>iSens</li> <li>Cont</li> <li>Each syst</li> <li>Generic V</li> <li>Cons</li> <li>Solut</li> <li>Direct Into</li> <li>Face</li> </ul>   | ction of facets system dependent<br>wn: A facet needs to know to which<br>se: Require access to isense::Os<br>tiki: Only calls to C functions<br>tem with own constructors<br>Wiselib Application<br>struction must be hidden for user<br>tion: Template based facet provide<br>tegration<br>ets are known to user<br>ectly initialize facets  | processor it belongs                                      | $\rightarrow \text{Intern}$ $\stackrel{1}{} \stackrel{\text{tern}}{} \stackrel{2}{} \stackrel{\text{class}}{} \stackrel{2}{} \stackrel{\text{class}}{} \stackrel{2}{} \stackrel{\text{class}}{} \stackrel{\text{class}}{ \stackrel{\text{class}}{} \text$ | <pre>se FacetProvider nals in Session 4 mplate<typename facet_p="" osmodel_p,="" typename=""> ass FacetProvider { static Facet&amp; get_facet( AppMainParam se specialization for different p get_facet() returns reference id init( Os::AppMainParameter&amp; value radio_ = &amp;wiselib ::FacetProvider<os, );<="" td="" value=""><td>blatforms<br/>ce to facet</td></os,></typename></pre>    | blatforms<br>ce to facet                                 |
| annis Chatzigiannakis<br>Motivation<br>5000000<br>Facet Instantiation<br>Generic Wiseli   | Pervasive Systems<br>Design Of The Wiselib<br>000000000000000000000000000000000000   | Lecture 17 45 / 49<br>Hardware Abstraction with OS Facets | Ioannis Chatzigiannakis<br>Motivation<br>0000000<br>Facet Instantiation<br>Generic Wisel  | Pervasive Systems<br>Design Of The Wiselib<br>000000000000000000000000000000000000  | Lecture 17 46 / 4<br>Hardware Abstraction with OS Facets |
| <ul> <li>Template<br/>→ Intern         <sup>1</sup>/<sub>2</sub> <sup>1</sup>/<sub>3</sub> <sup>1</sup>/<sub>4</sub> <sup>1</sup>/<sub>2</sub> <sup>1</sup>/<sub>4</sub> <sup>1</sup>/<sub>4</sub> <sup>1</sup>/<sub>4</sub> <sup>1</sup>/<sub>4</sub> <sup>1</sup>/<sub>4</sub> <sup>1</sup>/<sub>4</sub> <sup>1</sup>/<sub>4</sub> <sup>1</sup>/<sub>5</sub> <sup>1</sup>/<sub>4</sub> <sup>1</sup>/<sub>4</sub> <sup>1</sup>/<sub>5</sub> <sup>1</sup>/<sub>4</sub> <sup>1</sup>/<sub>5</sub> <sup>1</sup>/<sub>4</sub> <sup>1</sup>/<sub>5</sub> <sup>1</sup>/<sub>4</sub> <sup>1</sup>/<sub>5</sub> <sup>1</sup>/<sub>4</sub> <sup>1</sup>/<sub>5</sub> <sup>1</sup>/<sub>4</sub> <sup>1</sup>/<sub>5</sub> <sup>1</sup>/<sub>4</sub> <sup>1</sup>/<sub>4</sub> <sup>1</sup>/<sub>5</sub> <sup>1</sup>/<sub>4</sub> <sup>1</sup>/<sub>5</sub> <sup>1</sup>/<sub>4</sub> <sup>1</sup>/<sub></sub></li></ul> | e FacetProvider<br>hals in Session 4<br>iplate <typename osmodelp,<br="">typename Facet_P&gt;<br/>ss FacetProvider {<br/>tatic Facet&amp; get_facet( AppMainParameter<br/>e specialization for different plat:<br/>get_facet() returns reference t<br/>d init( Os::AppMainParameter&amp; value )<br/>radio_ = &amp;wiselib::FacetProvider<os, os:<br="">value );<br/>:Radio::self_pointer_t radio_;</os,></typename> | forms<br>to facet   | • Templat<br>$\rightarrow$ Intern<br>1 ten<br>2 cla<br>4 5<br>5 }<br>• Templat<br>• Method<br>1 voi<br>2 {<br>3 cla<br>4 5<br>5 }   | <pre>te FacetProvider nals in Session 4 mplate<typename facet_p="" osmodelp,="" typename=""> ass FacetProvider { static Facet&amp; get_facet( AppMainParan te specialization for different p get_facet() returns reference id init( Os::AppMainParameter&amp; value radio_ = &amp;wiselib::FacetProvider<os, );="" ::radio::self_pointer_t="" pre="" radio_;<="" value=""></os,></typename></pre> | platforms<br>ce to facet                                 |
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| 000000  | Design Of The Wiselib<br>೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦  | Hardware Abstraction with OS Facets<br>○○○○○○○○○○○○●○     | Motivation<br>0000000  | Design Of The Wiselib<br>೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦   | Hardware Abstraction with OS Facet:<br>○○○○○○○○○○○○○○○ |  |  |  |
|---|--|---|--|---|--|--|--|--|
| Sense Ap  | plication  |   | Facet Instantiation  | lication  |  |  |  |  |
| 1<br>2<br>3<br>4<br>5<br>6<br>7<br>8<br>9<br>10<br>11<br>11<br>2<br>3<br>4<br>5<br>6<br>7<br>8<br>9<br>10<br>11 | <pre>https://www.secking.com/secking/s</pre> | );<br>nse_radio.h"  | 1<br>2<br>3<br>4<br>5<br>6<br>7<br>8<br>9<br>10<br>11<br>2<br>3<br>4<br>5<br>6<br>7<br>8<br>9<br>10<br>11<br>2<br>3<br>4<br>5<br>6<br>7<br>8<br>9<br>10<br>11<br>2<br>3<br>4<br>5<br>6<br>7<br>8<br>9<br>10<br>11<br>2<br>3<br>4<br>5<br>6<br>7<br>8<br>9<br>10<br>11<br>2<br>3<br>4<br>5<br>6<br>7<br>8<br>9<br>10<br>11<br>2<br>11<br>11<br>11<br>11<br>11<br>11<br>11<br>11 | <pre>te facets usually expect isense::<br/>template<typename osmodel_p=""><br/>class iSenseRadioModel<br/>: public isense::Receiver<br/>{<br/>iSenseRadioModel( isense::Os&amp; os )<br/>: os_(os)<br/>{<br/>osdispatcher().add_receiver( this<br/>}<br/><br/>}<br/>ttly used as members<br/>#include "external_interface/isense/ise<br/>typedef wiselib::iSenseOsModel Os;<br/>class iSenseDemoApplication {<br/>iSenseDemoApplication {<br/>isense::Application(os),<br/>radio_(os)<br/>{}<br/>Os::Radio radio_;</typename></pre> | s );<br>ense_radio.h"                                  |  |  |  |
| 12<br>Dannis Chatzigiannakis<br>Motivation  | }<br>s Pervasive Systems<br>Design Of The Wiselib  | Lecture 17 48 / 49<br>Hardware Abstraction with OS Facets | 12<br>Ioannis Chatzigiannakis<br>Motivation  | } Pervasive Systems Design Of The Wiselib   | Lecture 17 48 /<br>Hardware Abstraction with OS Face   |  |  |  |
| acet Instantiation  | 000000000000000000000000000000000000000  | 000000000000000   | 0000000<br>Facet Instantiation   | 000000000000000000000000000000000000000   | 00000000000 <b>0000</b>                                |  |  |  |
| Shawn Ap  | plication  | hawn Application  |  |   | Shawn Application                                      |  |  |  |
|   |  |   |  |   |  |  |  |  |
| → 1<br>2<br>3<br>4<br>5<br>6<br>7   | <pre>wwn facets usually expect ShawnOs i<br/>Defined in external_interface/s<br/>template<typename osmodel_p=""><br/>class ShawnRadioModel {<br/>ShawnRadioModel( ShawnOs&amp; os )<br/>: os_(os)<br/>{}<br/>ShawnOs&amp; os_;</typename></pre>  |   | • Shaw<br>$\rightarrow D$<br>1<br>2<br>3<br>4<br>5<br>6<br>7   | <pre>/n facets usually expect ShawnOs efined in external_interface/s template<typename osmodel_p=""> class ShawnRadioModel {     ShawnRadioModel ( ShawnOs&amp; os )         : os_(os)     {}      ShawnOs&amp; os_; </typename></pre>  |  |  |  |  |
| → 1<br>2<br>3<br>4<br>5<br>6<br>7   | Defined in external_interface/s<br>template <typename osmodel_p=""><br/>class ShawnRadioModel {<br/>ShawnRadioModel(ShawnOs&amp;os)<br/>: os-(os)<br/>{}<br/><br/>ShawnOs&amp; os-;<br/>ectly used as members<br/>#include "external_interface/shawn/shaw</typename>   | hawn/shawn_types.h  | • Shaw<br>$\rightarrow D$<br>1<br>2<br>3<br>4<br>5<br>6<br>7   | <pre>vn facets usually expect ShawnOs efined in external_interface/s template<typename osmodel_p=""> class ShawnRadioModel {    ShawnRadioModel(ShawnOs&amp; os)         : os_(os)    {}   </typename></pre>  | shawn/shawn_types.h                                    |  |  |  |
| → 1<br>2<br>3<br>4<br>5<br>6<br>7   | Defined in external_interface/s<br>template <typename osmodel_p=""><br/>class ShawnRadioModel {<br/>ShawnRadioModel(ShawnOs&amp; os)<br/>: os.(os)<br/>{}<br/>ShawnOs&amp; os.;<br/>ectly used as members</typename>   | hawn/shawn_types.h  | • Shaw<br>$\rightarrow D$<br>1<br>2<br>3<br>4<br>5<br>6<br>7   | <pre>vn facets usually expect ShawnOs efined in external_interface/s template<typename osmodel_p=""> class ShawnRadioModel {    ShawnRadioModel(ShawnOs&amp; os)         : os_(os)         {}     ShawnOs&amp; os_; etly used as members</typename></pre>   | shawn/shawn_types.h                                    |  |  |  |