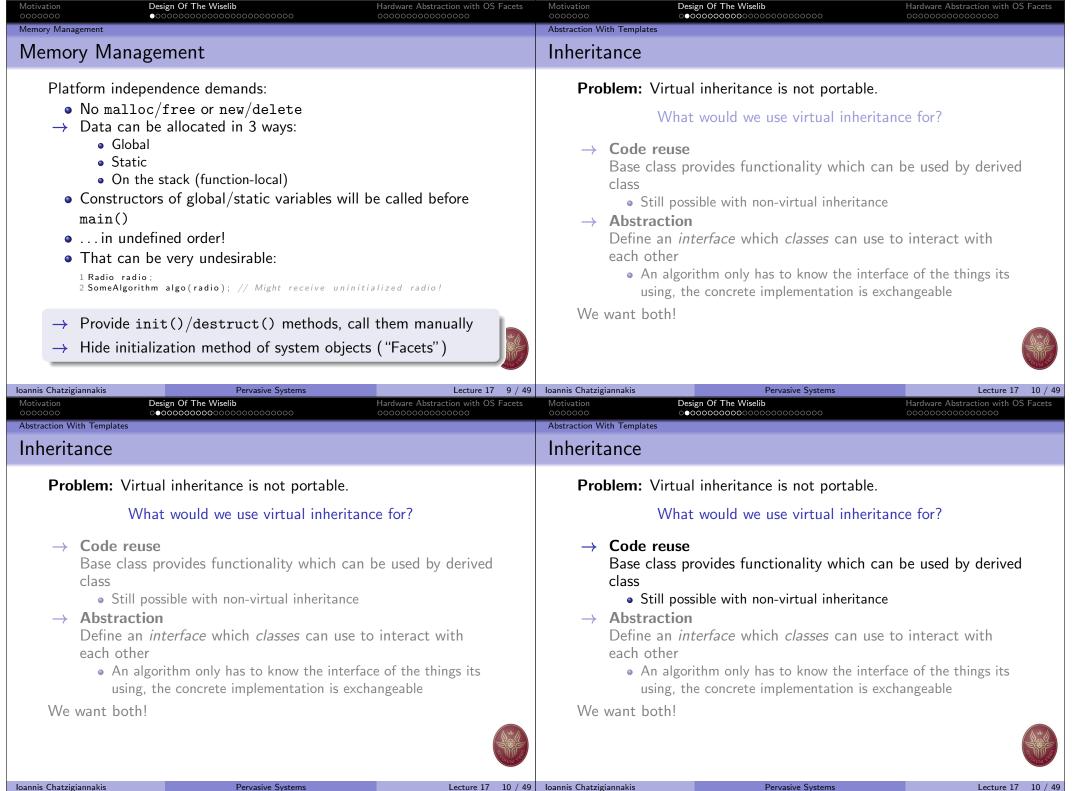
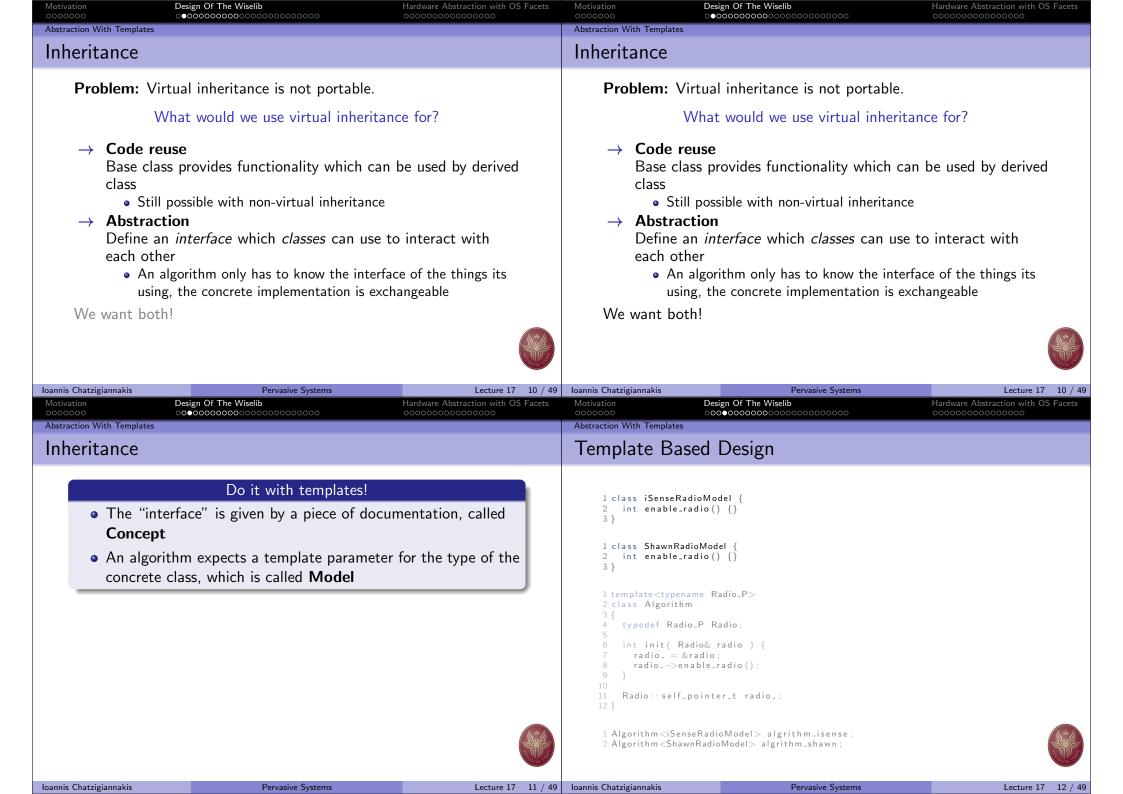


Motivation ●000000 A Library Of Algorithr	Design Of The Wiselib 000000000000000000000000000000000000		tion with OS Facets 0000	Motivation ●000000 A Library Of Algorithms	Design Of The Wiselib ೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦	Hardware Abstraction with OS Facets
	roblems In WSN Prog	ramming			olems In WSN Programn	ning
● Prae ● Prae ● ● ● There ● Wit ● Tha ● And	eoreticians are not interested Ideally they just have to writ And do not need to care about octioners are not interested i Just need a good algorithm f Without having to study the e is need for an algorithm lift th lots of algorithms for all l at are easy to integrate into d are combinable d easily enhanceable	te their algorithms out boilerplate code n theory for their task field for years brary kinds of tasks		<ul> <li>Id</li> <li>Ar</li> <li>Practic</li> <li>Ju</li> <li>W</li> <li>⇒ There is</li> <li>With Id</li> <li>That a</li> <li>And ar</li> </ul>	eticians are not interested in pro- eally they just have to write their nd do not need to care about boil- oners are not interested in theo ust need a good algorithm for their /ithout having to study the field for need for an algorithm library ots of algorithms for all kinds of are easy to integrate into existing re combinable asily enhanceable	algorithms erplate code ry r task or years of tasks
annis Chatzigiannakis lotivation ●00000 Library Of Algorithr	Design Of The Wiselib 000000000000000000000000000000000000	Hardware Abstract	Lecture 17 2 / 49 tion with OS Facets 0000	Ioannis Chatzigiannakis Motivation oo●0000 A Library Of Algorithms	Pervasive Systems Design Of The Wiselib 000000000000000000000000000000000000	Lecture 17 2 / Hardware Abstraction with OS Facet: 00000000000000000
Solution				The Wiselib	IS	
	The W	iselib		● A C++ ● Free (a	+ project as in freedom), licensed under L	_GPL
A library	/ of about 50 algorithms, lot	ts more to come! These	are	· ·	a middleware (we will see later	
Con	ensible nbineable changeable				http://wiseli	.b.org
Currently	y includes the following algo	orithm categories		There you'l		
• Cluste	ering	<ul> <li>Metrics</li> </ul>			ocumentation Wiki /iselib Sourcecode	
<ul> <li>Graph</li> </ul>	n Coloring	<ul> <li>Routing</li> </ul>		• The B		
<ul> <li>Crypte</li> </ul>		<ul> <li>Synchronization</li> </ul>		<ul> <li>Instruct</li> </ul>	tions on how to download & ir	nstall the Wiselib
<ul> <li>Energ</li> </ul>	gy Preservation	<ul> <li>Topology Control</li> </ul>	A*4			Mar 1
<ul> <li>Locali</li> </ul>	ization	<ul> <li>Tracking</li> </ul>	Story and			States

	Design Of The Wiselib 000000000000000000000000000000000000	0000000	Hardware Abstraction		Motivation 0000000	Design Of The Wiselib 000000000000000000000000000000000000	Hardware Abstraction with OS Face
.ibrary Of Algorithms Viselib Distrib	outions				Platform Independence Platform Inde	pendence	
• New thing	ssarily tested on	change their inte	erface		use diffe The Wis So i Whi In lots o But	ientists all over the world work rent experimentation environm elib aims to be <b>versatile</b> t can be used for <b>different tasks</b> ch also require <b>different hardwar</b> f applications we need <b>heterog</b> do not want to write the same co node type	ents re geneous nodes
	all supported p will not change					want the Wiselib to be platfo	rm independent!
nis Chatzigiannakis	Per	vasive Systems		ecture 17 5 / 49	Ioannis Chatzigiannakis	Pervasive Systems	Lecture 17 6
	Design Of The Wiselib	0000000	Hardware Abstraction	n with OS Facets	Motivation	Design Of The Wiselib	Hardware Abstraction with OS Fac
ooooo tform Independence	000000000000000000000000000000000000000	0000000		n with OS Facets	Motivation ○○○○○○○ Platform Independence	Design Of The Wiselib ೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦	
tform Independence Iatform Independence iSense	000000000000000000000000000000000000000	0000000		n with OS Facets	Motivation OCOCOO Platform Independence Platform Independence • Some plate • And/or H • Some do • No H • So re • So re • C (exceptedence)	Design Of The Wiselib cococococococococococococococococococo	c memory ent
tform Independence	bendence		000000000000000000000000000000000000000	n with OS Facets	Motivation OCOCOO Platform Independence Platform Independence • Some plate • And/or H • Some do • No H • So re • So re • C (exceptedence)	Design Of The Wiselib cococococococococococococococococococo	c memory ent
Hardware Jennic Operating System	occoocococococococococococococococococ	<image/> <image/>	00000000000000000000000000000000000000	n with OS Facets	Motivation OCOCOO Platform Independence Platform Independence • Some plate • And/or H • Some do • No H • So r The "extreme • C (except • Static m	Design Of The Wiselib cococococococococococococococococococo	c memory ent
ooo●O tform Independence iSense Weight of the sense Hardware Jennic Operating System iSense	occooccooccooccooccooccooccooccooccooc	ScatterWeb MSB	Tmote Sky	n with OS Facets	Motivation OCOCOO Platform Independence Platform Independence • Some plate • And/or H • Some do • No H • So r The "extreme • C (except • Static m	Design Of The Wiselib cococcoccoccoccoccoccoccoccoccoccoccocc	c memory ent
Docoo tform Independence Iatform Independence iSense iSense <u>Hardware</u> Jennic Operating System	occoocococococococococococococococococ	<image/> <image/>	00000000000000000000000000000000000000	n with OS Facets	Motivation OCOCOO Platform Independence Platform Independence OCOCOO OCOCOO OCOCOO Platform Independence OCOCOO OCOCOO OCOCOO OCOCOO Platform Independence OCOCOO OCOCOCO OCOCOCO OCOCOCOC	Design Of The Wiselib cococococococococococococococococococo	c memory ent ual inheritance, etc
ooo●O tform Independence Iatform Independence iSense iSense <u>Hardware</u> Jennic <u>Operating System</u> iSense ROM / RAM	occooccooccooccooccooccooccooccooccooc	ScatterWeb MSB	Contiki / TinyOS	n with OS Facets	Motivation OCOCOCO Platform Independence Platform Independence OCOCOCO OCOCOCO OCOCOCO OCOCOCO OCOCOCOC	Design Of The Wiselib COCOCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCC	c memory ent ual inheritance, etc <cmath>)</cmath>
form Independence form Independence iSense iSense <u>Hardware</u> Jennic Operating System iSense ROM / RAM 128kB / 92kB Memory Management	occooccooccooccooccooccooccooccooccooc	ScatterWeb MSB	Contiki / TinyOS 48kB / 10kB	n with OS Facets	Motivation OCOCOCO Platform Independence Platform Independence OCOCOCO OCOCOCO OCOCOCO OCOCOCO OCOCOCOC	Design Of The Wiselib cococococococococococococococococococo	c memory ent ual inheritance, etc <cmath>)</cmath>

Design Of The Wiselib         Hardware Abstraction with OS Face           0000         000000000000000000000000000000000000	s Motivation Design Of The Wiselib Hardware Abstraction with OS Fa
emory Management	Memory Management
<pre>Platform independence demands: • No malloc/free or new/delete → Data can be allocated in 3 ways: • Global • Static • On the stack (function-local) • Constructors of global/static variables will be called before main() •in undefined order! • That can be very undesirable: 1 Radio radio; 2 SomeAlgorithm algo(radio); // Might receive uninitialized radio! → Provide init()/destruct() methods, call them manually → Hide initialization method of system objects ("Facets")</pre>	<pre>Platform independence demands:     • No malloc/free or new/delete     → Data can be allocated in 3 ways:         • Global         • Static         • On the stack (function-local)     • Constructors of global/static variables will be called before         main()     •in undefined order!     • That can be very undesirable:         1 Radio radio;         2 SomeAlgorithm algo(radio); // Might receive uninitialized radio!     → Provide init()/destruct() methods, call them manually     → Hide initialization method of system objects ("Facets")</pre>
is Chatzigiannakis Pervasive Systems Lecture 17 9 / ivation Design Of The Wiselib Hardware Abstraction with OS Face	49     Ioannis Chatzigiannakis     Pervasive Systems     Lecture 17       s     Motivation     Design Of The Wiselib     Hardware Abstraction with OS Fa
nory Management	Memory Management
<pre>Platform independence demands: • No malloc/free or new/delete → Data can be allocated in 3 ways: • Global • Static • On the stack (function-local) • Constructors of global/static variables will be called before main() •in undefined order! • That can be very undesirable: <sup>1</sup>Radio radio; <sup>2</sup>SomeAlgorithm algo(radio); // Might receive uninitialized radio!</pre>	<pre>Platform independence demands: • No malloc/free or new/delete → Data can be allocated in 3 ways: • Global • Static • On the stack (function-local) • Constructors of global/static variables will be called before main() •in undefined order! • That can be very undesirable: <sup>1</sup>Radio radio; <sup>2</sup>SomeAlgorithm algo(radio); // Might receive uninitialized radio!</pre>
<ul> <li>→ Provide init()/destruct() methods, call them manually</li> <li>→ Hide initialization method of system objects ("Facets")</li> </ul>	→ Provide init()/destruct() methods, call them manually → Hide initialization method of system objects ("Facets")
nis Chatzigiannakis Pervasive Systems Lecture 17 9 /	49 Ioannis Chatzigiannakis Pervasive Systems Lecture 17





Motivation         Design Of The Wiselib           0000000         000000000000000000000000000000000000	Hardware Abstraction with OS Facets	Motivation         Design Of The Wiselib           0000000         000000000000000000000000000000000000	Hardware Abstraction with OS Facets
Abstraction With Templates Template Based Design		Abstraction With Templates Template Based Design	
<pre>1 class iSenseRadioModel { 2 int enable_radio() {} 3 } 1 class ShawnRadioModel { 2 int enable_radio() {} 3 }</pre>		<pre>1 class iSenseRadioModel { 2 int enable_radio() {} 3 } 1 class ShawnRadioModel { 2 int enable_radio() {} 3 }</pre>	
<pre>1 template<typename radio_p=""> 2 class Algorithm 3 { 4 typedef Radio_P Radio; 5 6 int init( Radio&amp; radio ) { 7 radio_ = &amp;radio 8 radio&gt;enable_radio(); 9 } 10 11 Radio::self_pointer_t radio_; 12 }</typename></pre>		<pre>1 template<typename radio_p=""> 2 class Algorithm 3 { 4 typedef Radio_P Radio; 5 6 int init( Radio&amp; radio ) { 7 radio_ = &amp;radio 8 radio&gt;enable_radio(); 9 } 10 11 Radio::self_pointer_t radio_; 12 }</typename></pre>	
1 Algorithm <isenseradiomodel> algrithm_isense; 2 Algorithm<shawnradiomodel> algrithm_shawn;</shawnradiomodel></isenseradiomodel>	and the second sec	1 Algorithm <isenseradiomodel> algrithm_isense 2 Algorithm<shawnradiomodel> algrithm_shawn;</shawnradiomodel></isenseradiomodel>	e ;
oannis Chatzigiannakis Pervasive Systems Motivation Design Of The Wiselib	Lecture 17 12 / 49 Hardware Abstraction with OS Facets	Ioannis Chatzigiannakis         Pervasive Syst           Motivation         Design Of The Wiselib	Hardware Abstraction with OS Facets
00000000     000000000000000000000000000000000000	000000000000000000000000000000000000000	OOOOOOOOOOOOOOOOOOOOOOOOOOOOOOOOOOOO	> 00000000000000
Template Based Design		Template Based Design	
1 class iSenseRadioModel { 2 int enable_radio() {} 3 }		<pre>1 class iSenseRadioModel { 2 int enable_radio() {} 3 }</pre>	
<pre>1 class ShawnRadioModel { 2 int enable_radio() {} 3 }</pre>		<pre>1 class ShawnRadioModel { 2     int enable_radio() {} 3 }</pre>	
<pre>1 template<typename radio_p=""> 2 class Algorithm 3 { 4 typedef Radio_P Radio; 5 6 int init( Radio&amp; radio ) { 7 radio_ = &amp;radio 8 radio&gt;enable_radio(); </typename></pre>		<pre>1 termilate Commany Radio_P &gt; 2 class Algorithm 3 { 4 typedef Radio_P Radio; 5 6 int init( Radio&amp; radio ) { 7 radio_ = &amp; radio; 8 8 radio = radio; 8 radio = dia combine radio; 9 radio = ra</pre>	
		8 radio_—>enable_radio();	
9 } 10 11 <mark>Radio::self_pointer_t_radio_;</mark> 12 }		9 } 10 11 <mark>Radio::self_pointer_t_radio_;</mark> 12 }	
10 11 Radio::self_pointer_t_radio_;		10 11 Radio::self_pointer_t_radio_;	e ;

0000000 Abstraction	14/2-1	<b>T</b> 1.
Motivation		

Design Of The Wiselib

Hardware Abstraction with OS Facets

Abstraction

#### Concept

- Describes behaviour of components
- E.g. "A Radio has a void send(char\*) method"
- Only documentation

## Model

- Actual class
- Implements any number of concepts
- E.g. A routing protocol may implement the radio concept
- ...so it can be used like one

# Design Of The Wiselib

Hardware Abstraction with OS Facets

# Abstraction With Templates Abstraction

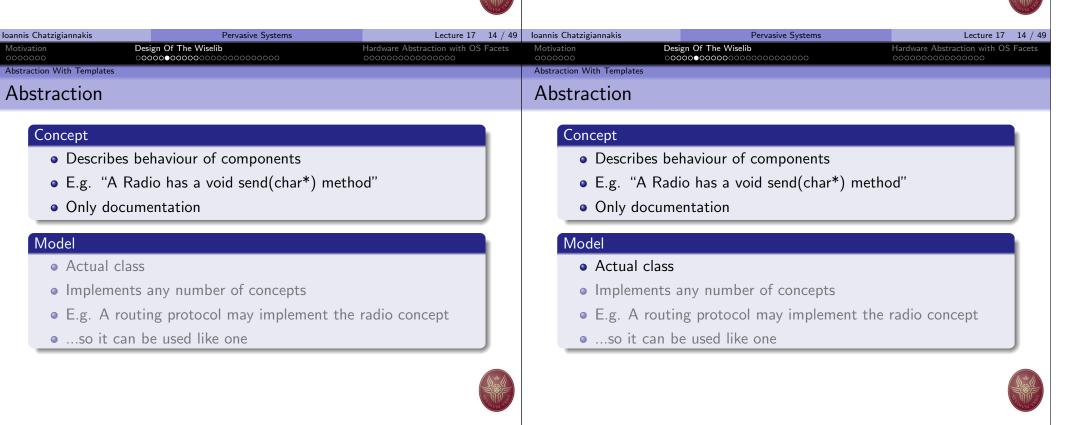
Motivation

### Concept

- Describes behaviour of components
- E.g. "A Radio has a void send(char\*) method"
- Only documentation

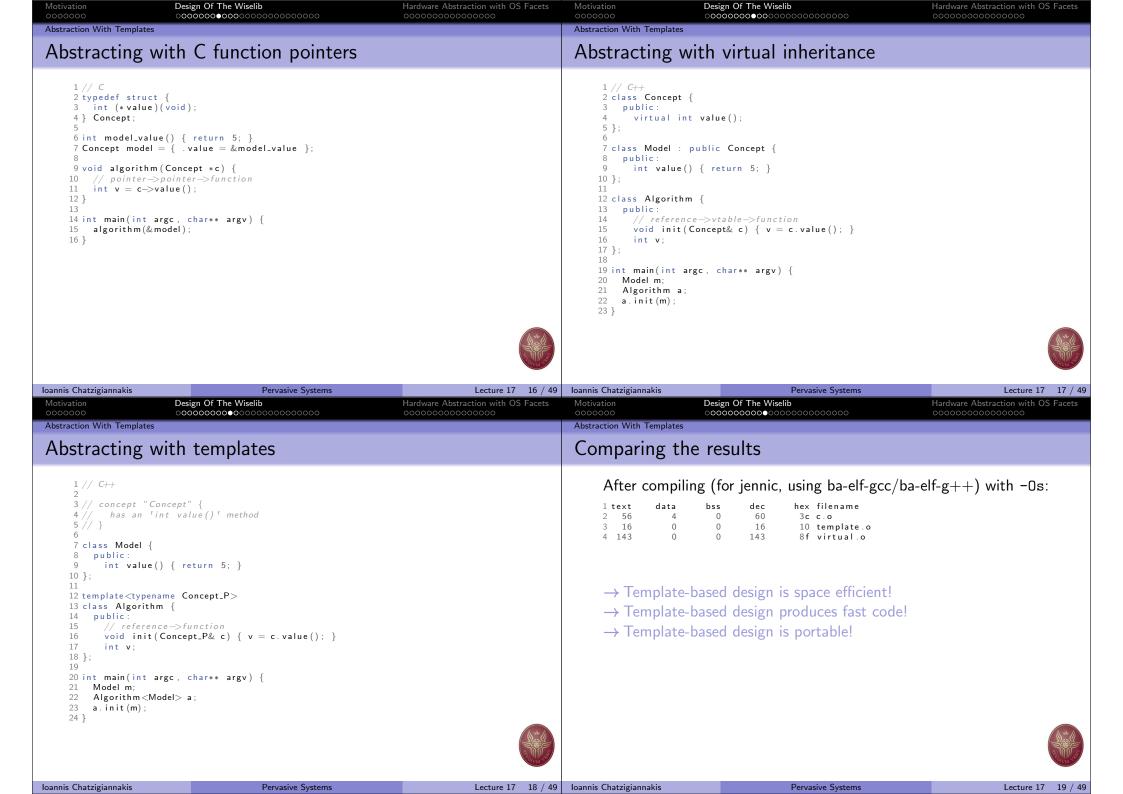
### Model

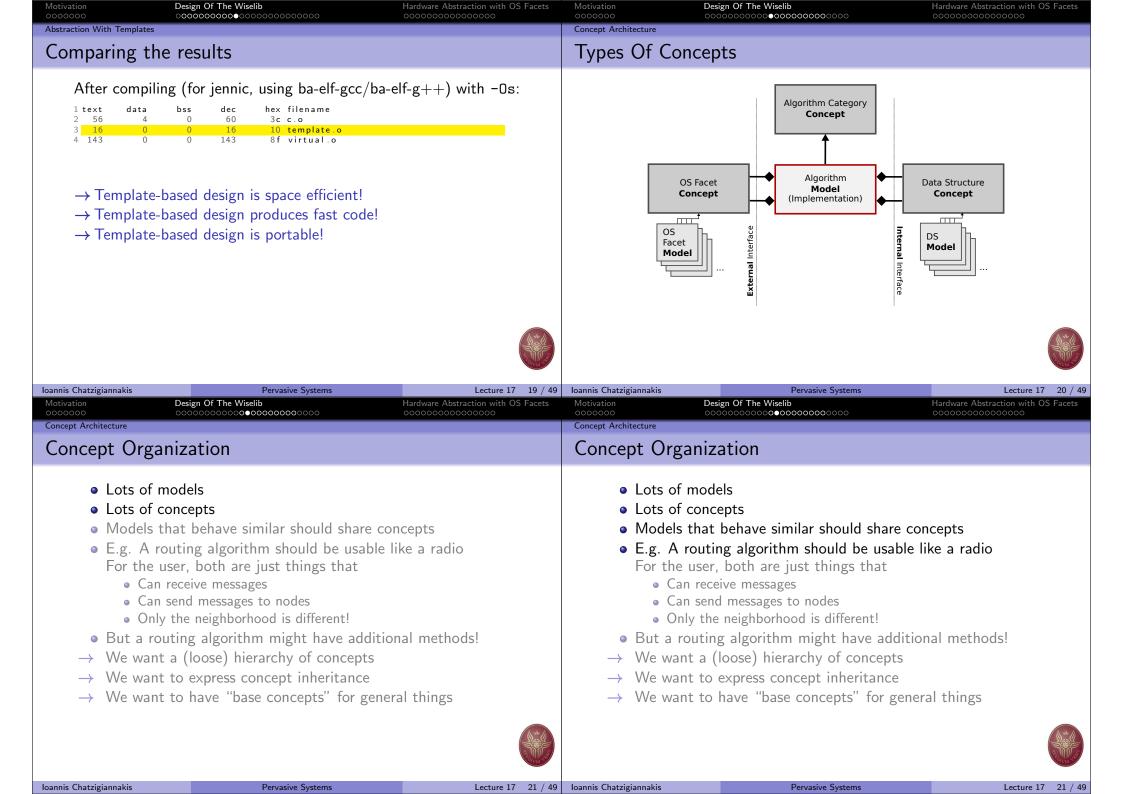
- Actual class
- Implements any number of concepts
- E.g. A routing protocol may implement the radio concept
- ...so it can be used like one



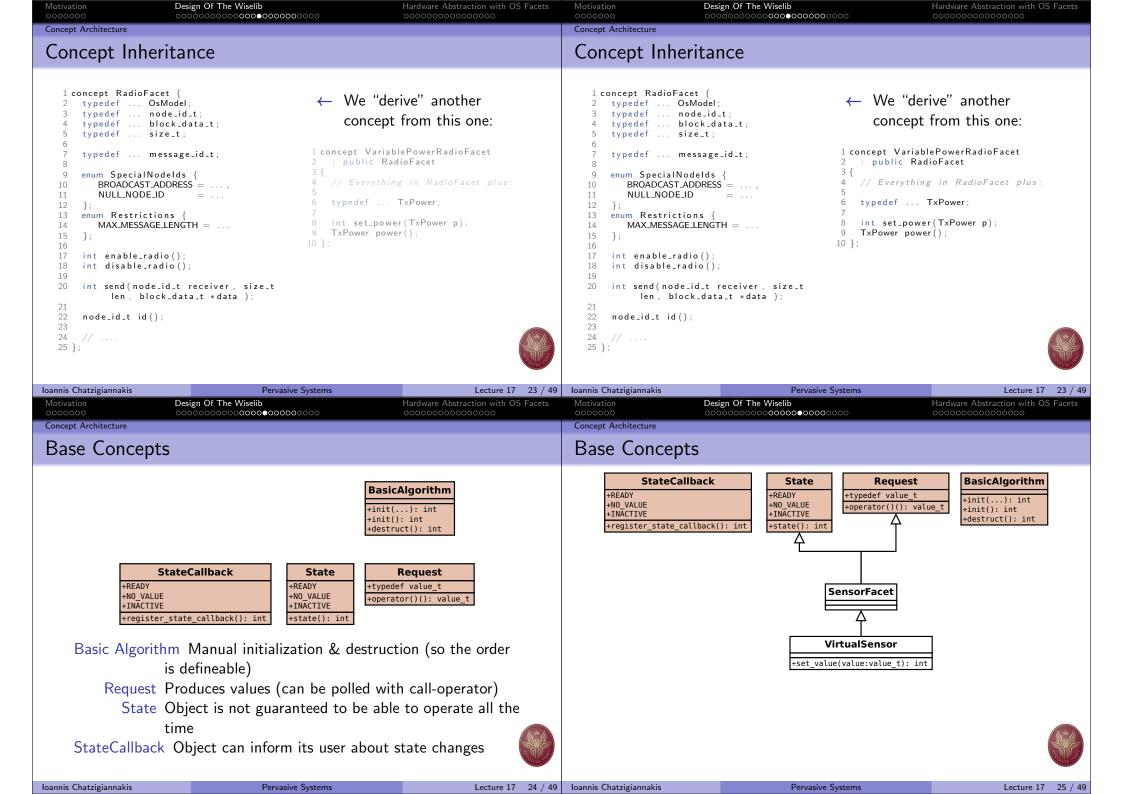
Notivation	Design Of The Wiselib ○○○○○●○○○○○○○○○○○○○○○○○○○○	Hardware Abstraction with OS Facets	Motivat 00000	00	Design Of The Wiselib ○0000€00000000000000000000000000000000	Hardware Abstraction with C	/S Face
Abstraction With Tem				tion With Template	25		
<ul> <li>E.g.</li> <li>Only</li> </ul> Model <ul> <li>Actu</li> <li>Imp</li> <li>E.g.</li> </ul>	cribes behaviour of components . "A Radio has a void send(char y documentation ual class lements any number of concepts . A routing protocol may implem o it can be used like one			<ul> <li>E.g. ",</li> <li>Only d</li> </ul> Model <ul> <li>Actual</li> <li>Implen</li> <li>E.g. A</li> </ul>	bes behaviour of compone A Radio has a void send(c locumentation class nents any number of conce routing protocol may imp can be used like one	har*) method" epts	
nis Chatzigiannakis tivation 00000 straction With Tem bstractio	Design Of The Wiselib ○0000●00000000000000000000000000000000	Lecture 17 14 / 49 Hardware Abstraction with OS Facets 000000000000000000000000000000000000	Motivat 00000 Abstrac	100 tion With Template	Pervasive Syste Design Of The Wiselib 000000-000000000000000000000000000000	Hardware Abstraction with C	
• E.g.	cribes behaviour of components . "A Radio has a void send(char y documentation	*) method"		• In	are other ways to provide C, one would usually abstra C++ one would use <b>virtua</b> l	ct with <b>function pointers</b>	
• Imp • E.g.	ual class lements any number of concepts . A routing protocol may implem o it can be used like one			Ho	ow do they compare to the	e template approach?	

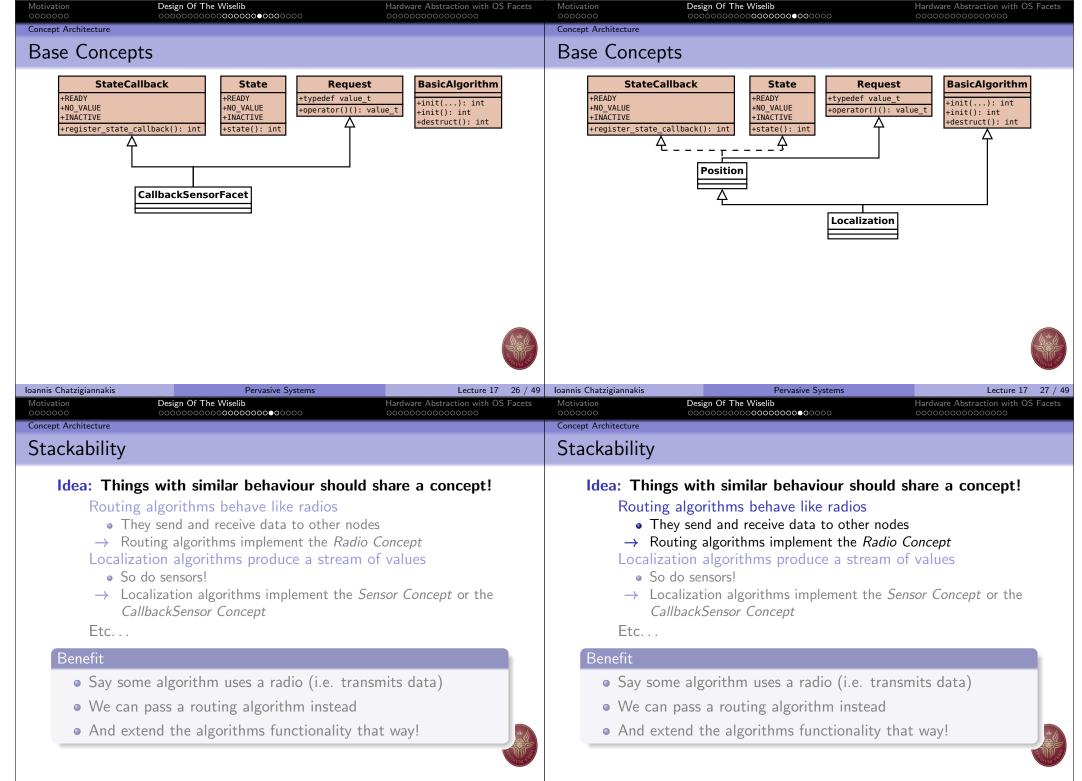
Ioannis Chatzigiannakis



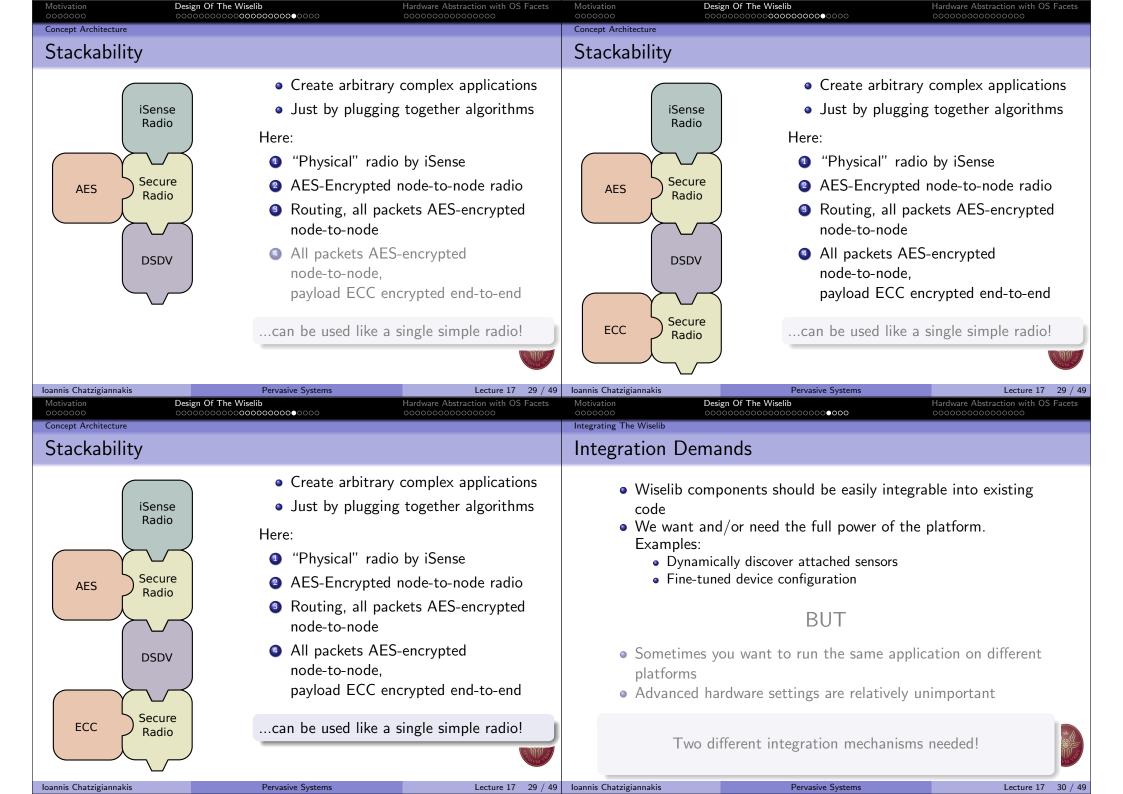


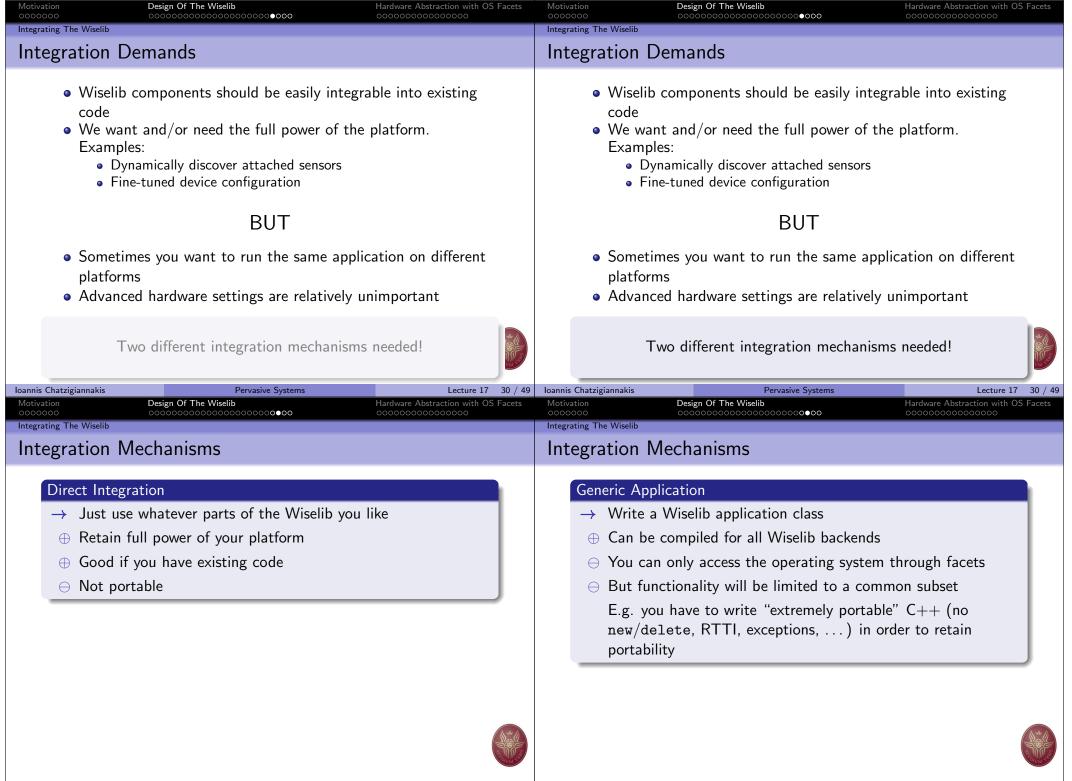
Motivation 0000000 Concept Architecture	Design Of The Wiselib ०००००००००० <b>००००००००</b> ००००	Hardware Abstraction with OS Facets	Motivation 0000000	Design Of The Wiselib ○○○○○○○○○●●○○○○○○○○○○○	Hardware Abstraction with OS Fa
	rganization		Concept Architecture Concept Org	anization	
• Lots • Mod • E.g. For t • • • • • • • • • • • • • • • • • • •	of models of concepts els that behave similar should shar A routing algorithm should be usa the user, both are just things that Can receive messages Can send messages to nodes Only the neighborhood is different! a routing algorithm might have ad want a (loose) hierarchy of concept want to express concept inheritance want to have "base concepts" for g	ble like a radio ditional methods! ts e	<ul> <li>E.g. A For the</li> <li>Ca</li> <li>Ca</li> <li>On</li> <li>But a r</li> <li>→ We war</li> <li>→ We war</li> </ul>		able like a radio dditional methods! ots ce
annis Chatzigiannakis Notivation Soncept Architecture	Pervasive Systems Design Of The Wiselib 000000000000000000000000000000000000	Lecture 17 21 / 49 Hardware Abstraction with OS Facets 000000000000000000000000000000000000	Ioannis Chatzigiannakis Motivation ooooooo Concept Architecture The OsMode	Pervasive Systems Design Of The Wiselib 000000000000000000000000000000000000	Lecture 17 2 Hardware Abstraction with OS Fa 000000000000000000000000000000000000
<ul> <li>Lots</li> <li>Lots</li> <li>Mode</li> <li>E.g.</li> <li>For t</li> <li>But a</li> <li>→ We w</li> <li>→ We w</li> </ul>	of models of concepts els that behave similar should shar A routing algorithm should be usa the user, both are just things that Can receive messages Can send messages to nodes Only the neighborhood is different! a routing algorithm might have ad want a (loose) hierarchy of concept want to express concept inheritance want to have "base concepts" for g	ble like a radio ditional methods! ts e	1 concept OsMoo 2 typedef 3 typedef 4 enum Return 5 6 typedef 7 typedef 9 10 static cons WISELI 11 } • Holds p • Constan • Inco • Ma	del { size_t; block_data_t; // "byte"-like type fo Values { SUCCESS =, ERR_UNSPEC = Radio; // Wireless communication fac	<pre> }; TLE_ENDIAN or ess, size type, etc) NSPEC (unspecified error)</pre>
annis Chatzigiannakis	Pervasive Systems	Lecture 17 21 / 49	Ioannis Chatzigiannakis	Pervasive Systems	Lecture 17

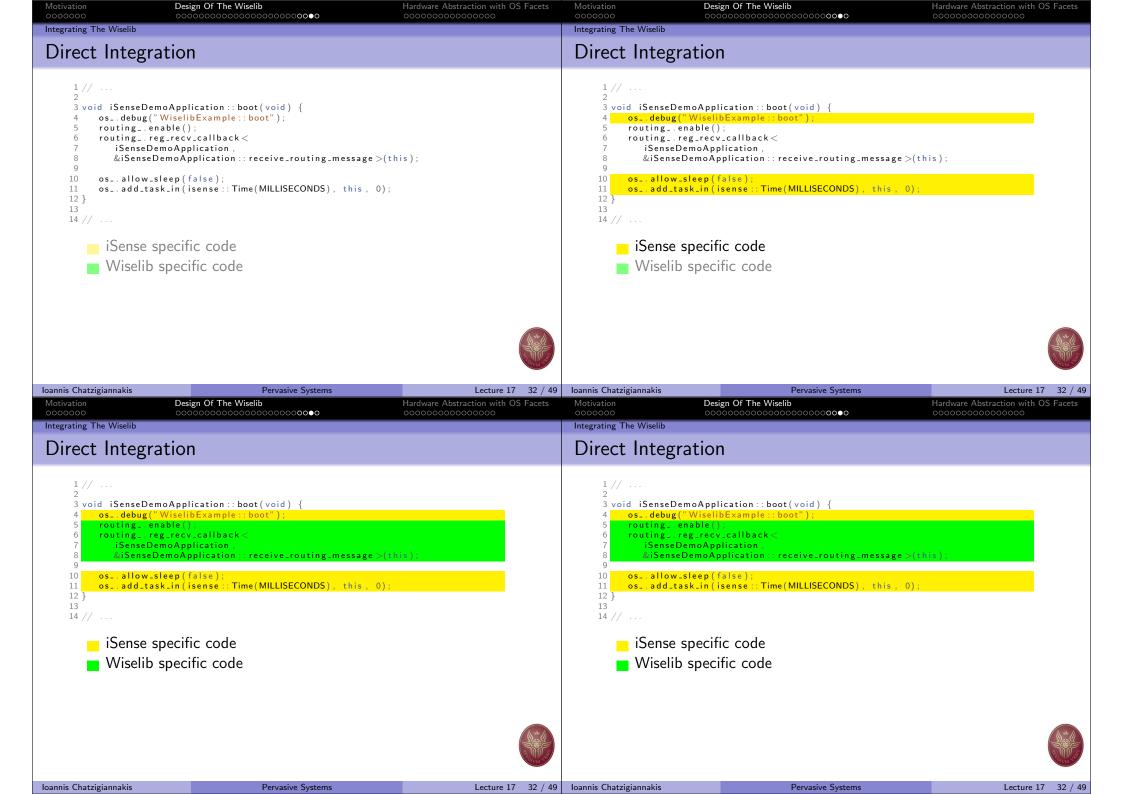




Ioannis Chatzigiannakis







```
Design Of The Wiselib
                                                                     Hardware Abstraction with OS Facets
                                                                                                                               Design Of The Wiselib
                                                                                                                                                                           Hardware Abstraction with OS Facets
                                                                                                      Motivation
                         Integrating The Wiselib
                                                                                                      Integrating The Wiselib
Generic Application
                                                                                                      Generic Application
     1#include "external_interface/external_interface.h"
                                                                                                            1#include "external_interface/external_interface.h"
     2#include "external_interface/external_interface_testing.h"
                                                                                                            2 #include "external_interface/external_interface_testing.h"
     3 // ...
                                                                                                            3 // ...
     Δ
     5 typedef wiselib :: PCOsModel Os;
                                                                                                            5 typedef wiselib :: PCOsModel Os;
     6 class DemoApplication {
                                                                                                            6 class DemoApplication {
     7
         public
                                                                                                            7
                                                                                                               public
                                                                                                           8
     8
            void init(Os::AppMainParameter& amp) {
                                                                                                                 void init(Os::AppMainParameter& amp) {
              radio_ = & wiselib :: FacetProvider <Os, Os :: Radio > :: get_facet (amp);
     9
                                                                                                           9
                                                                                                                    radio_ = & wiselib :: FacetProvider < Os, Os :: Radio > :: get_facet (amp);
     10
              debug_ = & wiselib :: FacetProvider < Os, Os :: Debug > :: get_facet (amp);
                                                                                                           10
                                                                                                                    debug_ = & wiselib :: FacetProvider < Os, Os :: Debug > :: get_facet (amp);
     11
                                                                                                           11
     12
              algorithm_.init();
                                                                                                           12
                                                                                                                    algorithm_.init();
     13
                                                                                                           13
     14
              radio_->enable_radio();
                                                                                                           14
                                                                                                                    radio_->enable_radio();
                                                                                                           15
     15
              debug_->debug("Initialized.\n");
                                                                                                                   debug_->debug("Initialized.\n");
     16
                                                                                                           16
    17
                                                                                                           17
     18
         private
                                                                                                           18
                                                                                                               private
    19
           Os::Debug::self_pointer_t debug_;
                                                                                                           19
                                                                                                                 Os::Debug::self_pointer_t debug_;
     20
                                                                                                           20
           Os::Radio::self_pointer_t radio_;
                                                                                                                 Os::Radio::self_pointer_t radio_;
     21
           SomeAlgorithm algorithm_;
                                                                                                           21
                                                                                                                 SomeAlgorithm algorithm_;
     22 };
                                                                                                           22 };
     23
                                                                                                           23
                                                                                                           24 wiselib :: WiselibApplication <0s, DemoApplication > demo_app;
     24 wiselib :: WiselibApplication <Os, DemoApplication > demo_app;
     25 void application_main(Os::AppMainParameter& amp) {
                                                                                                           25 void application_main (Os:: AppMainParameter& amp) {
     26 demo_app.init(amp);
                                                                                                           26 demo_app.init(amp);
                                                                                                           27 }
     27 }
                                                                                                                                           Platform selection
Ioannis Chatzigiannakis
                                                                                   Lecture 17 33 / 49
                                                                                                     Ioannis Chatzigiannakis
                                                                                                                                                                                         Lecture 17 33 / 49
                                          Pervasive Systems
                                                                                                                                                Pervasive Systems
                         Design Of The Wiselib
                                                                     Hardware Abstraction with OS Facets
                                                                                                      Motivation
                                                                                                                               Design Of The Wiselib
                                                                                                                                                                           Hardware Abstraction with OS Facets
Motivation
                                             0000000
                                                                                                                                                 0000000000
Integrating The Wiselib
                                                                                                      Integrating The Wiselib
Generic Application
                                                                                                      Generic Application
                                                                                                            1#include "external_interface/external_interface.h"
     1#include "external_interface/external_interface.h"
     2#include "external_interface/external_interface_testing.h"
                                                                                                            2#include "external_interface/external_interface_testing.h"
     3 // ...
                                                                                                            3 // ...
     Λ
     5 typedef wiselib :: PCOsModel Os;
                                                                                                            5 typedef wiselib :: PCOsModel Os;
     6 class DemoApplication {
                                                                                                            6 class DemoApplication {
        public :
                                                                                                            7 public:
     7
     8
           void init(Os::AppMainParameter& amp) {
                                                                                                                  void init(Os::AppMainParameter& amp) {
                                                                                                           8
             radio_ = & wiselib :: FacetProvider < Os, Os :: Radio > :: get_facet (amp)
                                                                                                           9
                                                                                                                    radio_ = & wiselib :: FacetProvider < Os, Os :: Radio > :: get_facet (amp);
     10
             debug_ = & wiselib :: FacetProvider < Os, Os :: Debug > :: get_facet (amp)
                                                                                                           10
                                                                                                                    debug_ = & wiselib :: FacetProvider < Os, Os :: Debug > :: get_facet (amp);
     11
                                                                                                           11
                                                                                                           12
     12
              algorithm_.init()
                                                                                                                    algorithm_.init();
     13
                                                                                                           13
     14
              radio_->enable_radio();
                                                                                                           14
                                                                                                                    radio_->enable_radio();
                                                                                                           15
     15
              debug_->debug("Initialized.\n");
                                                                                                                   debug_->debug("Initialized.\n");
     16
                                                                                                           16
     17
                                                                                                           17
     18
         private
                                                                                                           18
                                                                                                               private
     19
           Os::Debug::self_pointer_t debug_;
                                                                                                           19
                                                                                                                 Os::Debug::self_pointer_t debug_;
     20
           Os::Radio::self_pointer_t radio_;
                                                                                                           20
                                                                                                                 Os::Radio::self_pointer_t radio_;
     21
           SomeAlgorithm algorithm_;
                                                                                                           21
                                                                                                                 SomeAlgorithm algorithm_;
     22 };
                                                                                                           22 };
     23
                                                                                                           23
                                                                                                           24 wiselib :: WiselibApplication < Os, DemoApplication > demo_app
     24 wiselib :: WiselibApplication <Os, DemoApplication > demo_app;
     25 void application_main(Os::AppMainParameter& amp) {
                                                                                                           25 void application_main (Os:: AppMainParameter& amp) {
     26 demo_app.init(amp);
                                                                                                           26
                                                                                                              demo_app.init(amp);
                                                                                                           27 }
     27 }
      Initialization: FacetProvider for OS facets / Manual for algorithms
                                                                                                                application main getting called by Wiselib \leftrightarrow OS adaptor
Ioannis Chatzigiannakis
                                                                                   Lecture 17 33 / 49
                                                                                                     Ioannis Chatzigiannakis
                                                                                                                                                                                         Lecture 17 33 / 49
                                          Pervasive Systems
                                                                                                                                                Pervasive Systems
```

	Abstraction with OS Facets		Design Of The Wiselib 000000000000000000000000000000000000	Hardware Abstraction with OS Facet
Nhat is a Facet?		What is a Facet	t?	
<ul> <li>Connection between algorithms and OS</li> <li>OS Facets (Concepts) <ul> <li>OS Facet</li> <li>Radio Facet</li> <li>Timer Facet</li> <li></li> </ul> </li> <li>For each supported OS at least one model per facet</li> <li>iSenseOsModel</li> <li>ContikiRadioModel</li> <li>ShawnTimerModel</li> <li></li> </ul> <li>Possible to provide muliple models per facet <ul> <li>ContikiRimeRadioModel</li> <li>Contiki6LowPanRadioModel</li> <li></li> </ul> </li>	cet	<ul> <li>OS Facets</li> <li>OS Facets</li> <li>OS Facets</li> <li>Radio</li> <li>Timer</li> <li></li> <li>For each s</li> <li>iSensed</li> <li>Contik</li> <li>Shawn</li> <li></li> <li>Possible to</li> <li>Contik</li> </ul>	cet Facet Facet supported OS at least <b>one mode</b>	
	Lecture 17 34 / 49 Abstraction with OS Facets		Pervasive Systems Design Of The Wiselib 000000000000000000000000000000000000	Lecture 17 34 / Hardware Abstraction with OS Face
oduction /hat is a Facet?		What is a Facet		
<ul> <li>Connection between algorithms and OS</li> <li>OS Facets (Concepts) <ul> <li>OS Facet</li> <li>Radio Facet</li> <li>Timer Facet</li> <li></li> </ul> </li> <li>For each supported OS at least one model per facet</li> <li>iSenseOsModel</li> <li>ContikiRadioModel</li> <li>ShawnTimerModel</li> <li></li> </ul> <li>Possible to provide muliple models per facet <ul> <li>ContikiRimeRadioModel</li> <li>ContikiGLowPanRadioModel</li> <li></li> </ul> </li>	cet	<ul> <li>OS Facets</li> <li>OS Facets</li> <li>OS Facets</li> <li>Radio</li> <li>Timer</li> <li></li> <li>For each s</li> <li>iSensed</li> <li>Contik</li> <li>Shawn</li> <li></li> <li>Possible to</li> <li>Contik</li> </ul>	cet Facet Facet supported OS at least <b>one mode</b>	
nnis Chatzigiannakis Pervasive Systems	Lecture 17 34 / 49	Ioannis Chatzigiannakis	Pervasive Systems	Lecture 17 34

Motivation 000000			he Wiselib 0000000000	000000	00				ostraction with OS Facets	Motivation 0000000	Design Of The Wiselib 000000000000000000000000000000000000	Hardware Abstraction with OS Face
OS Facet Overview									Exchangeability with Algorithms			
	WP2 OSA Contiki TinyOS iSense ScatterWeb Shawn (⊕ = fully sup	$ \begin{array}{c} \oplus\\ $			$ \begin{array}{c} \oplus\\ $	⊕•	○ ⊕	Positif	4/7 5/7 2/7 7/7 4/7 6/7	<ul> <li>Pass</li> <li>Exan</li> <li></li> <li< th=""><th>c design issue: Flexibility an algorithm where a facet nples Pass routing algorithm where rac ⇒ Enable flexible multihop neig Pass time-synchronization algori ⇒ Enable system-wide time basi Pass localization algorithm wher ⇒ Only some nodes in the netwo Pass routing-based debug model ⇒ Debug nodes that are not con antage: Totally transparent for</th><th>dio is expected hborhoods thm where clock is expected is e position is expected ork need to know their position where debug facet is expected nnected to a gateway position</th></li<></ul>	c design issue: Flexibility an algorithm where a facet nples Pass routing algorithm where rac ⇒ Enable flexible multihop neig Pass time-synchronization algori ⇒ Enable system-wide time basi Pass localization algorithm wher ⇒ Only some nodes in the netwo Pass routing-based debug model ⇒ Debug nodes that are not con antage: Totally transparent for	dio is expected hborhoods thm where clock is expected is e position is expected ork need to know their position where debug facet is expected nnected to a gateway position
innis Chatzigiann otivation ocococo troduction	akis Desig	n Of Tł	Perv he Wiselib 000000000	vasive Sys	stems		Har	dware A	Lecture 17 35 / 49 Destraction with OS Facets	Ioannis Chatzigiannakis Motivation οοοοοοο Introduction Exchanαea	Pervasive Systems Design Of The Wiselib 000000000000000000000000000000000000	Lecture 17 36 Hardware Abstraction with OS Face 00€0000000000000000000000000000000000
• B • Pa • E	asic design i ass an algor kamples ● Pass routi ⇒ Enable ● Pass time ⇒ Enable ● Pass local ⇒ Only so ● Pass routi	ing a ing a flexi syst izatio ome ing-b	:: Flexib n where Igorithm ible multi chronizati chronizati con algorit nodes in pased deb es that a	ility a fac where ihop n ion alg time thm w the ne bug mo	e radio neight gorith basis /here etwor odel v	o is exp oorhood m whe positio k need where d nected	bected ds re clo to kn ebug to a g	ock is expect low the facet	ed neir position	• Basia • Pass • Exan •	c design issue: <b>Flexibility</b> an <b>algorithm</b> where a <b>facet</b>	dio is expected hborhoods thm where clock is expected is re position is expected ork need to know their position where debug facet is expected nnected to a gateway position
nnis Chatzigiann				vasive Sys					Lecture 17 36 / 49	Ioannis Chatzigiannakis	Pervasive Systems	Lecture 17 36

Motivation 0000000	Design Of The Wiselib ೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦	Hardware Abstraction with OS Facets 00●0000000000000000000000000000000000	Motivation 0000000	Design Of The Wiselib ೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦	Hardware Abstraction with OS Facets		
Introduction Exchange	eability with Algorithms		Exchangeab	ility with Algorithms			
• Pa • E>	<ul> <li>asic design issue: Flexibility</li> <li>ass an algorithm where a facet is examples</li> <li>Pass routing algorithm where radio i ⇒ Enable flexible multihop neighbor</li> <li>Pass time-synchronization algorithm ⇒ Enable system-wide time basis</li> <li>Pass localization algorithm where pot ⇒ Only some nodes in the network in</li> <li>Pass routing-based debug model where ⇒ Debug nodes that are not connected wantage: Totally transparent for a</li> </ul>	s expected hoods where clock is expected sition is expected need to know their position ere debug facet is expected tted to a gateway position	<ul> <li>Exchangeability with Algorithms</li> <li>Basic design issue: Flexibility</li> <li>Pass an algorithm where a facet is expected</li> <li>Examples <ul> <li>Pass routing algorithm where radio is expected</li> <li>Enable flexible multihop neighborhoods</li> <li>Pass time-synchronization algorithm where clock is expected</li> <li>Enable system-wide time basis</li> <li>Pass localization algorithm where position is expected</li> <li>Only some nodes in the network need to know their position</li> <li>Pass routing-based debug model where debug facet is expected</li> <li>Debug nodes that are not connected to a gateway position</li> </ul> </li> <li>Advantage: Totally transparent for algorithm</li> </ul>				
oannis Chatzigianna Motivation ooooooo Introduction	Design Of The Wiselib 000000000000000000000000000000000000	Lecture 17 36 / 49 Hardware Abstraction with OS Facets OO●○○○○○○○○○○○	Ioannis Chatzigiannakis Motivation 0000000 Introduction	Pervasive Systems Design Of The Wiselib 000000000000000000000000000000000000	Lecture 17 36 / Hardware Abstraction with OS Face 000000000000000000000000000000000000		
Exchange	eability with Algorithms		Exchangeab	ility with Algorithms			
• Pa	asic design issue: <b>Flexibility</b> ass an <b>algorithm</b> where a <b>facet</b> is examples • Pass routing algorithm where radio i $\Rightarrow$ Enable flexible multihop neighbor • Pass time-synchronization algorithm $\Rightarrow$ Enable system-wide time basis • Pass localization algorithm where po $\Rightarrow$ Only some nodes in the network p	s expected hoods where clock is expected sition is expected	● Pass a ● Examp ● P = ● P = ● P	design issue: <b>Flexibility</b> an <b>algorithm</b> where a <b>facet</b> is end ples ass routing algorithm where radio > Enable flexible multihop neighbor ass time-synchronization algorithm > Enable system-wide time basis ass localization algorithm where pre- > Only some nodes in the network	is expected rhoods where clock is expected osition is expected		

- $\Rightarrow$  Only some nodes in the network need to know their position
- Pass routing-based debug model where debug facet is expected
  - $\Rightarrow$  Debug nodes that are not connected to a gateway position
- Advantage: Totally transparent for algorithm

• Advantage: Totally transparent for algorithm

• Pass routing-based debug model where debug facet is expected

 $\Rightarrow$  Debug nodes that are not connected to a gateway position

```
Hardware Abstraction with OS Facets
                                                              Hardware Abstraction with OS Facets
Motivation
                                                                                           Motivation
                                                              Important Facets
                                                                                           Important Facets
The OS Facet
                                                                                           The OS Facet
     1 concept OsFacet
                                                                                                1 concept OsFacet {
     2 typedef ... size_t;
                                                                                                2 typedef ... size_t;
       typedef ... block_data_t; // "byte"-like type for buffers
                                                                                                   typedef ... block_data_t; // "byte"-like type for buffers
     3
        enum ReturnValues { SUCCESS, EUNSPEC, ... }; // Define constants for return
                                                                                                   enum ReturnValues { SUCCESS, EUNSPEC, ... }; // Define constants for return
             values
                                                                                                         values
     6
        typedef ... Radio; // Wireless communication facet
                                                                                                6
                                                                                                   typedef ... Radio; // Wireless communication facet
        typedef ... Timer;
                                                                                                    typedef ... Timer;
     7
        typedef ... Debug; // Send debug messages
                                                                                                   typedef ... Debug; // Send debug messages
     8
                                                                                                8
                                                                                                Q
        static const Endianess endianess; // WISELIB_LITTLE_ENDIAN or
                                                                                                  static const Endianess endianess; // WISELIB_LITTLE_ENDIAN or
    10
                                                                                               10
             WISELIB_BIG_ENDIAN
                                                                                                         WISELIB_BIG_ENDIAN
    11 }
                                                                                               11 }
        • Only facet which does not need to be instantiated
                                                                                                   • Only facet which does not need to be instantiated
        • Provide type definitions and constants
                                                                                                   • Provide type definitions and constants
        • Platform properties (endianess, size type, ...)
                                                                                                   • Platform properties (endianess, size type, ...)

    Constants for return values.

    Constants for return values

             • Include at least SUCCESS and ERR_UNSPEC (unspecified error)

    Include at least SUCCESS and ERR_UNSPEC (unspecified error)

             • May/will include more, similar to errno
                                                                                                         • May/will include more, similar to errno
                                                                                                   • Default types for basic OS Facets
        • Default types for basic OS Facets
                                                                          Lecture 17 37 / 49
                                                                                          Ioannis Chatzigiannakis
                                                                                                                                                                      Lecture 17 37 / 49
Ioannis Chatzigiannakis
                                     Pervasive Systems
                                                                                                                                 Pervasive Systems
                      Design Of The Wiselib
                                                              Hardware Abstraction with OS Facets
                                                                                           Motivation
                                                                                                                  Design Of The Wiselib
                                                                                                                                                         Hardware Abstraction with OS Facets
Motivation
                                                              Important Facets
                                                                                           Important Facets
The OS Facet
                                                                                           The OS Facet
     1 concept OsFacet {
                                                                                                1 concept OsFacet {
     2 typedef ... size_t;
                                                                                                2 typedef ... size_t;
       typedef ... block_data_t; // "byte"-like type for buffers
                                                                                                   typedef ... block_data_t; // "byte"-like type for buffers
     3
        enum ReturnValues { SUCCESS, EUNSPEC, ... }; // Define constants for return
                                                                                                   enum ReturnValues { SUCCESS, EUNSPEC, ... }; // Define constants for return
             values
                                                                                                         values
     5
                                                                                                5
     6
        typedef ... Radio; // Wireless communication facet
                                                                                                6
                                                                                                   typedef ... Radio; // Wireless communication facet
        typedef ... Timer
                                                                                                    typedef ... Timer;
     7
        typedef ... Debug; // Send debug messages
                                                                                                   typedef ... Debug; // Send debug messages
     8
                                                                                                8
                                                                                                0
    10
        static const Endianess endianess; // WISELIB_LITTLE_ENDIAN or
                                                                                               10
                                                                                                   static const Endianess endianess; // WISELIB_LITTLE_ENDIAN or
             WISELIB_BIG_ENDIAN
                                                                                                         WISELIB_BIG_ENDIAN
    11 }
                                                                                               11 }
        • Only facet which does not need to be instantiated
                                                                                                   • Only facet which does not need to be instantiated
        • Provide type definitions and constants
                                                                                                   • Provide type definitions and constants
        • Platform properties (endianess, size type, ...)
                                                                                                   • Platform properties (endianess, size type, ...)

    Constants for return values

    Constants for return values

    Include at least SUCCESS and ERR_UNSPEC (unspecified error)

             • Include at least SUCCESS and ERR_UNSPEC (unspecified error)
             • May/will include more, similar to errno

    May/will include more, similar to errno

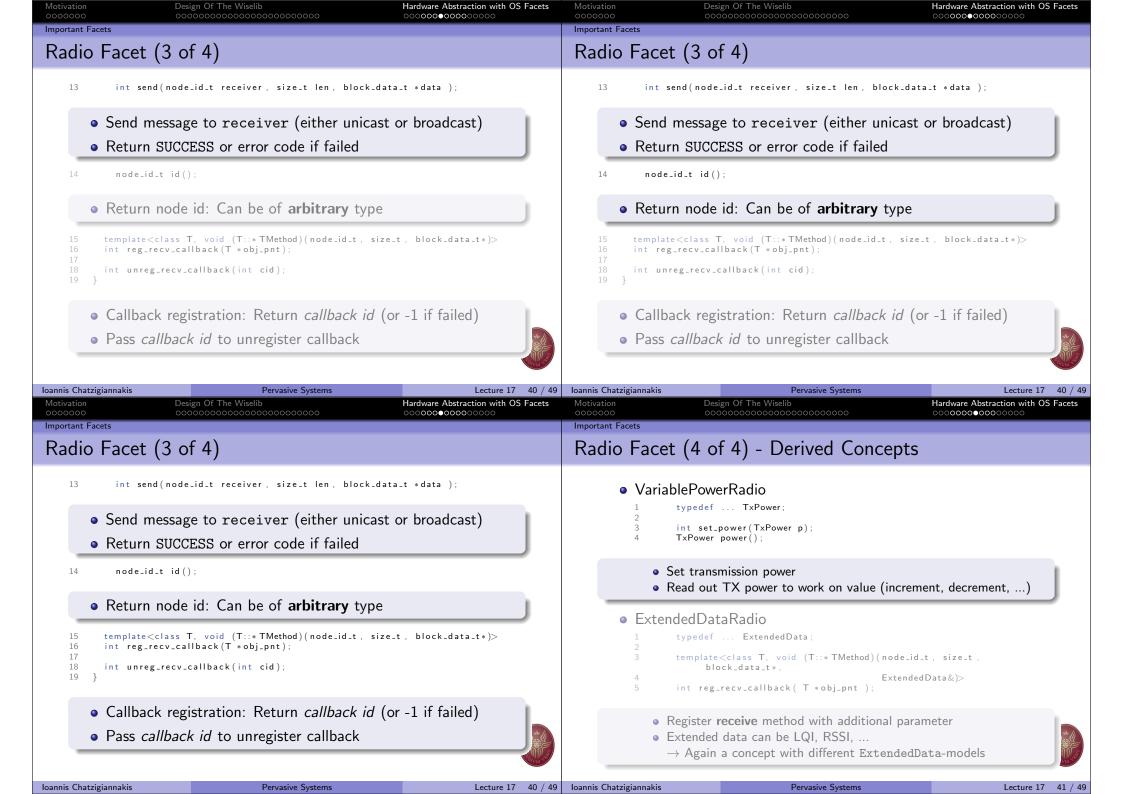
        • Default types for basic OS Facets

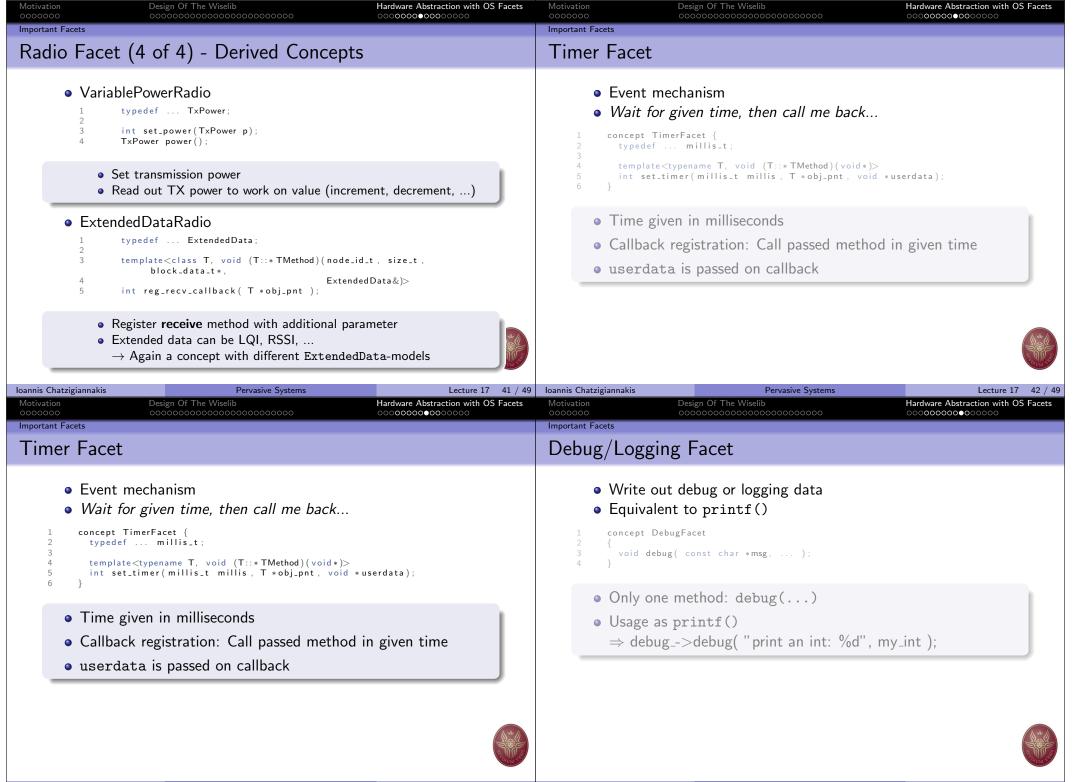
    Default types for basic OS Facets

                                                                          Lecture 17 37 / 49 Ioannis Chatzigiannakis
                                                                                                                                                                      Lecture 17 37 / 49
Ioannis Chatzigiannakis
                                      Pervasive Systems
                                                                                                                                 Pervasive Systems
```

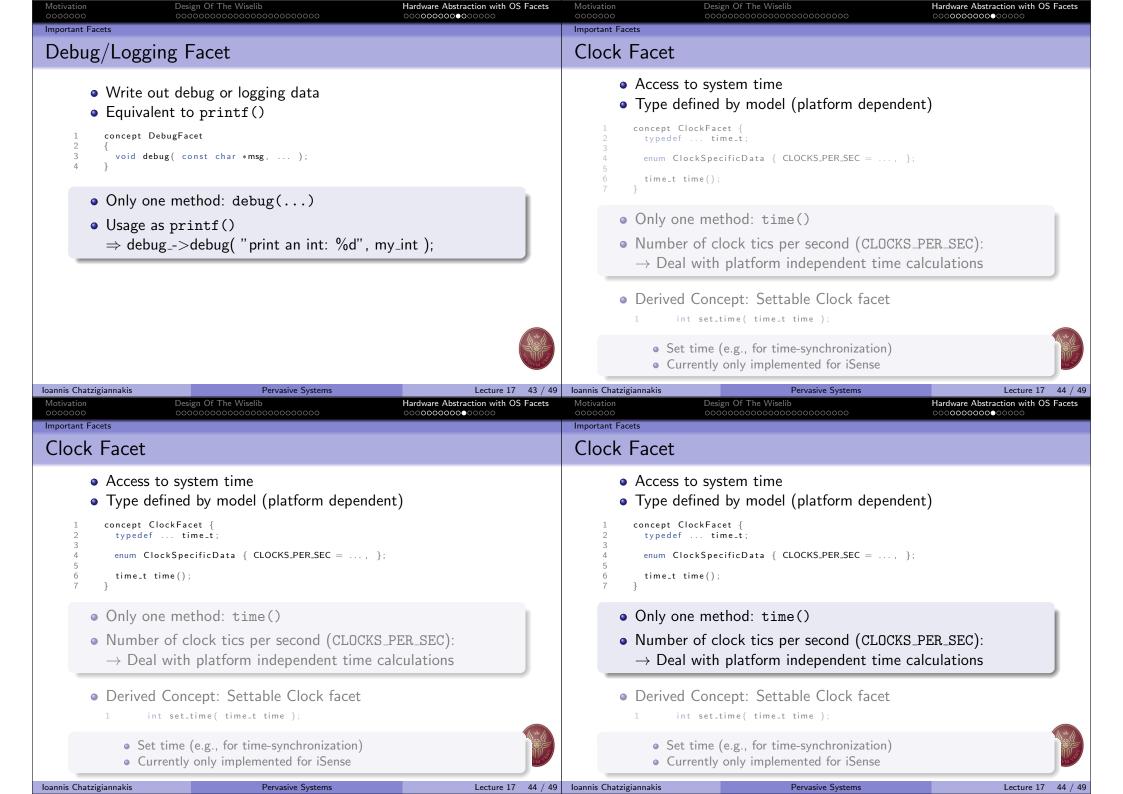
Motivation 0000000	Design Of The Wiselib 000000000000000000000000000000000000	Hardware Abstraction with OS Facets	Motivation 0000000	Design Of The Wiselib ೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦	Hardware Abstraction with OS Facets
mportant Facets The OS Fa	acet		Important Facets Radio Facet (	(1 of 4)	
3 typedef 4 enum Ret val 5 6 typedef 7 typedef 9 10 static c WIS 11 } • Only • Prov • Platf • Cons •	SFacet { size_t; block_data_t; // "byte"-like type for turnValues { SUCCESS, EUNSPEC, }; // I lues Radio; // Wireless communication face Timer; Debug; // Send debug messages const Endianess endianess; // WISELIB_LITT SELIB_BIG_ENDIAN y facet which does <b>not need</b> to be vide <b>type definitions</b> and <b>constan</b> form properties (endianess, size type stants for return values Include at least SUCCESS and ERR_UN May/will include more, similar to error oult types for basic OS Facets	Define constants for return et "LE_ENDIAN or e instantiated ts De,) ISPEC (unspecified error)	<ul> <li>Con</li> <li>Virt</li> <li>Send me</li> <li>Callback</li> <li>Provide <ul> <li>Nod</li> <li>E.g.</li> </ul> </li> </ul>	ssues straction to underlying hardware nplex routing algorithms <i>tual</i> radio providing <i>virtual</i> ids essages to other nodes (registration for received mes node id (and its <b>type</b> !) de id type is defined <b>per radio</b> ., provide IP addresses, but run o y restriction: Be passed to size	ssages on 16-bit addresses
oannis Chatzigiannakis Motivation 0000000 Important Facets	Pervasive Systems Design Of The Wiselib 000000000000000000000000000000000000	Lecture 17 37 / 49 Hardware Abstraction with OS Facets ○○○○●○○○○○○○○	Ioannis Chatzigiannakis Motivation 0000000 Important Facets	Pervasive Systems Design Of The Wiselib 000000000000000000000000000000000000	Lecture 17 38 / Hardware Abstraction with OS Facets
Radio Face	et (1 of 4)		Radio Facet (	(1 of 4)	
• Send • Callb • Prov	gn issues Abstraction to underlying hardware ra Complex routing algorithms <i>Virtual</i> radio providing <i>virtual</i> ids I messages to other nodes back registration for received messe ride node id (and its <b>type</b> !) Node id type is defined <b>per radio</b> E.g., provide IP addresses, but run or Only restriction: Be passed to sizeo	sages n 16-bit addresses	<ul> <li>Con</li> <li>Virt</li> <li>Send me</li> <li>Callback</li> <li>Provide <ul> <li>Nod</li> <li>E.g.</li> </ul> </li> </ul>	ssues straction to underlying hardware nplex routing algorithms <i>tual</i> radio providing <i>virtual</i> ids essages to other nodes c registration for received mes node id (and its <b>type</b> !) de id type is defined <b>per radio</b> ., provide IP addresses, but run o y restriction: Be passed to size	<b>ssages</b> on 16-bit addresses
oannis Chatzigiannakis	Pervasive Systems	Lecture 17 38 / 49	Ioannis Chatzigiannakis	Pervasive Systems	Lecture 17 38 /

Motivation 0000000	Design Of The Wiselib 000000000000000000000000000000000000	Hardware Abstraction with OS Facets ○○○○●○○○○○○○○○○	Motivation 0000000	Design Of The Wiselib ೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦	Hardware Abstraction with OS Facets ○○○○○●○○○○○○○○○○
Radio Face	et (1 of 4)		Important Facets Radio Face	et (2 of 4)	
• • Senc • Call • Prov •	ign issues Abstraction to underlying hardware radi Complex routing algorithms <i>Virtual</i> radio providing <i>virtual</i> ids d messages to other nodes back registration for received messsag vide node id (and its <b>type</b> !) Node id type is defined <b>per radio</b> E.g., provide IP addresses, but run on 1 Only restriction: Be passed to sizeof (	ges 6-bit addresses	2 type 3 type 4 type 5 type • Abili • Mess 9 enum 10 enum • Basic • Max 11 int 12 int • Turn	<pre>tr RadioFacet {     def node_id_t;     def block_data_t;     def block_data_t;     def size_t;     def message_id_t;  ty to provide arbitrary node ID sage ID type to identify received     SpecialNodelds { BROADCAST_ADDRESS =     Restrictions { MAX_MESSAGE_LENGTH =     c constants for broadcasting and     imal message length defined pe     enable_radio();     disable_radio();     on/off radio     rn SUCCESS or error code if fail</pre>	d messages , NULL_NODE_ID = }; }; d unknown nodes er radio
oannis Chatzigiannakis Motivation 0000000	Pervasive Systems Design Of The Wiselib 000000000000000000000000000000000000	Lecture 17 38 / 49 Hardware Abstraction with OS Facets	loannis Chatzigiannakis Motivation 0000000	Pervasive Systems Design Of The Wiselib 000000000000000000000000000000000000	Lecture 17 39 / Hardware Abstraction with OS Facets
Important Facets	et (2 of 4)		Important Facets Radio Face		
1 conce 2 typ 3 typ 4 typ	<pre>pt RadioFacet { edef node_id_t; edef block_data_t; edef size_t; edef message_id_t;</pre>		1 concep 2 type 3 type 4 type	ot RadioFacet { def node_id_t; def block_data_t; def size_t; def message_id_t;	
• Mes	ity to provide <b>arbitrary</b> node ID type sage ID type to identify received mes <sup>n</sup> SpecialNodelds { BROADCAST_ADDRESS =, <sup>n</sup> Restrictions { MAX_MESSAGE_LENGTH =, };	sages	• Mess	ty to provide <b>arbitrary</b> node ID sage ID type to identify received SpecialNodelds { BROADCAST_ADDRESS = Restrictions { MAX_MESSAGE_LENGTH =	d messages
• Basi	ic constants for broadcasting and unk simal message length defined <b>per rad</b>		• Basi	c constants for broadcasting and imal message length defined <b>pe</b>	d unknown nodes
12 int • Turr	enable_radio(); disable_radio(); n on/off radio urn SUCCESS or error code if failed Pervasive Systems	Lecture 17 39 / 49	12 int • Turn	enable_radio(); disable_radio(); n on/off radio ern SUCCESS or error code if fail Pervasive Systems	ed



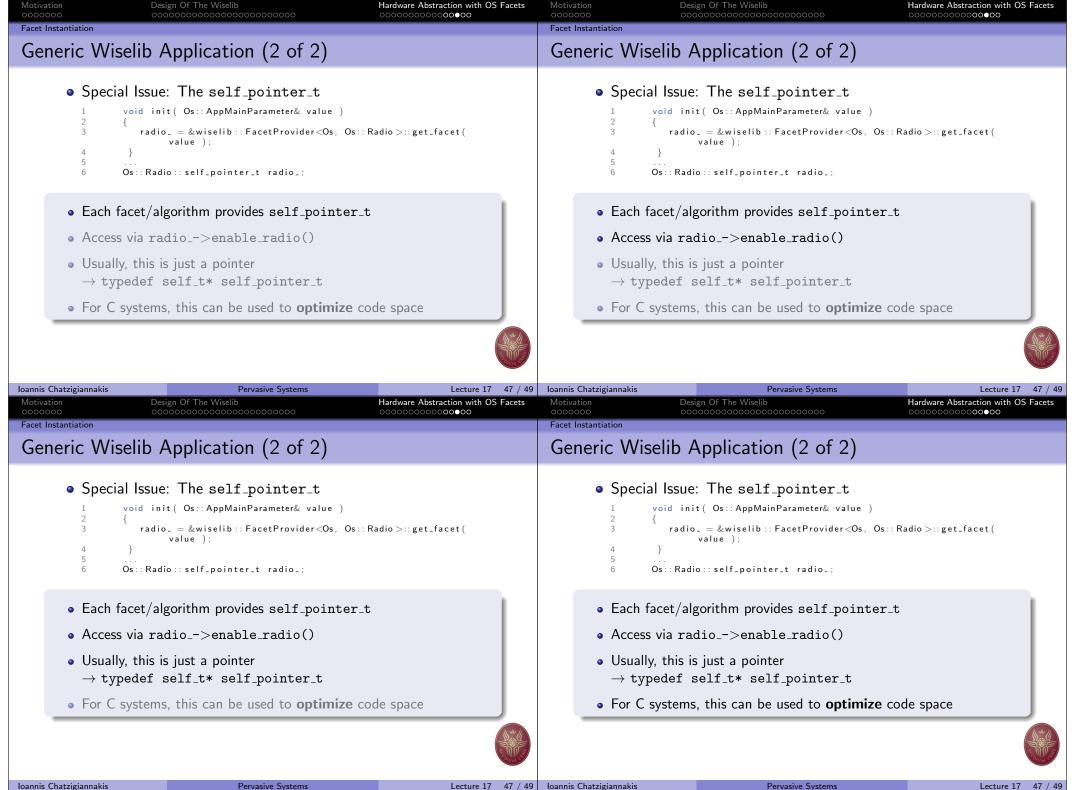


Ioannis Chatzigiannakis



Motivation 0000000	Design Of The Wiselib 000000000000000000000000000000000000	Hardware Abstraction with OS Facets	0000000 00	esign Of The Wiselib 000000000000000000000000000000000000	Hardware Abstraction with OS Facets
Important Facets Clock Facet			Facet Instantiation Facet Structure		
<pre>• Access to system time • Type defined by model (platform dependent) concept ClockFacet { typedef time_t; enum ClockSpecificData { CLOCKS_PER_SEC =, }; time_t time(); }</pre>			<ul> <li>Construction of facets system dependent         <ul> <li>Shawn: A facet needs to know to which processor it belongs</li> <li>iSense: Require access to isense::0s</li> <li>Contiki: Only calls to C functions</li> </ul> </li> <li>Each system with own constructors</li> <li>Generic Wiselib Application         <ul> <li>Construction must be hidden for user</li> </ul> </li> </ul>		
<ul> <li>Only one method: time()</li> <li>Number of clock tics per second (CLOCKS_PER_SEC):         <ul> <li>→ Deal with platform independent time calculations</li> </ul> </li> </ul>		<ul> <li>Solution: Template based facet provider</li> <li>Direct Integration <ul> <li>Facets are known to user</li> <li>Directly initialize facets</li> </ul> </li> </ul>			
	d Concept: Settable Clock facet nt set_time( time_t time );				
	t time (e.g., for time-synchronization) urrently only implemented for iSense				
annis Chatzigiannakis Activation 0000000	Pervasive Systems Design Of The Wiselib 000000000000000000000000000000000000	Lecture 17 44 / 49 Hardware Abstraction with OS Facets		Pervasive Systems ssign Of The Wiselib 2000000000000000000000000000000000000	Lecture 17 45 / Hardware Abstraction with OS Facets
acet Instantiation			Facet Instantiation		
acet Struct	ure		Facet Structure		
<ul> <li>Construction of facets system dependent <ul> <li>Shawn: A facet needs to know to which processor it belongs</li> <li>iSense: Require access to isense::0s</li> <li>Contiki: Only calls to C functions</li> </ul> </li> <li>Each system with own constructors</li> <li>Generic Wiselib Application <ul> <li>Construction must be hidden for user</li> <li>Solution: Template based facet provider</li> </ul> </li> <li>Direct Integration <ul> <li>Facets are known to user</li> <li>Directly initialize facets</li> </ul> </li> </ul>			<ul> <li>Construction of facets system dependent <ul> <li>Shawn: A facet needs to know to which processor it belongs</li> <li>iSense: Require access to isense::0s</li> <li>Contiki: Only calls to C functions</li> </ul> </li> <li>Each system with own constructors</li> <li>Generic Wiselib Application <ul> <li>Construction must be hidden for user</li> <li>Solution: Template based facet provider</li> </ul> </li> <li>Direct Integration <ul> <li>Facets are known to user</li> <li>Directly initialize facets</li> </ul> </li> </ul>		
annis Chatzigiannakis	Pervasive Systems	Lecture 17 45 / 49	loannis Chatzigiannakis	Pervasive Systems	Lecture 17 45 /

Motivation 0000000 Facet Instantiation	Design Of The Wiselib ೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦	Hardware Abstraction with OS Facets ○○○○○○○○○●○○○○	Motivation 0000000 Facet Instantiation	Design Of The Wiselib 000000000000000000000000000000000000	Hardware Abstraction with OS Facets ○○○○○○○○○○●○○○
Facet Instantiation Facet Structur	re		Generic Wisel	lib Application (1 of 2)	
<ul> <li>Shaw</li> <li>iSens</li> <li>Cont</li> <li>Each syst</li> <li>Generic V</li> <li>Cons</li> <li>Solut</li> <li>Direct Into</li> <li>Face</li> </ul>	ction of facets system dependent wn: A facet needs to know to which se: Require access to isense::Os tiki: Only calls to C functions tem with own constructors Wiselib Application struction must be hidden for user tion: Template based facet provide tegration ets are known to user ectly initialize facets	processor it belongs	$\rightarrow \text{Intern}$ $\stackrel{1}{} \stackrel{\text{tern}}{} \stackrel{2}{} \stackrel{\text{class}}{} \stackrel{2}{} \stackrel{\text{class}}{} \stackrel{2}{} \stackrel{\text{class}}{} \stackrel{\text{class}}{ \stackrel{\text{class}}{} \text$	<pre>se FacetProvider nals in Session 4 mplate<typename facet_p="" osmodel_p,="" typename=""> ass FacetProvider { static Facet&amp; get_facet( AppMainParam se specialization for different p get_facet() returns reference id init( Os::AppMainParameter&amp; value radio_ = &amp;wiselib ::FacetProvider<os, );<="" td="" value=""><td>blatforms ce to facet</td></os,></typename></pre>	blatforms ce to facet
annis Chatzigiannakis Motivation 5000000 Facet Instantiation Generic Wiseli	Pervasive Systems Design Of The Wiselib 000000000000000000000000000000000000	Lecture 17 45 / 49 Hardware Abstraction with OS Facets	Ioannis Chatzigiannakis Motivation 0000000 Facet Instantiation Generic Wisel	Pervasive Systems Design Of The Wiselib 000000000000000000000000000000000000	Lecture 17 46 / 4 Hardware Abstraction with OS Facets
<ul> <li>Template → Intern         <sup>1</sup>/<sub>2</sub> <sup>1</sup>/<sub>3</sub> <sup>1</sup>/<sub>4</sub> <sup>1</sup>/<sub>2</sub> <sup>1</sup>/<sub>4</sub> <sup>1</sup>/<sub>4</sub> <sup>1</sup>/<sub>4</sub> <sup>1</sup>/<sub>4</sub> <sup>1</sup>/<sub>4</sub> <sup>1</sup>/<sub>4</sub> <sup>1</sup>/<sub>4</sub> <sup>1</sup>/<sub>5</sub> <sup>1</sup>/<sub>4</sub> <sup>1</sup>/<sub>4</sub> <sup>1</sup>/<sub>5</sub> <sup>1</sup>/<sub>4</sub> <sup>1</sup>/<sub>5</sub> <sup>1</sup>/<sub>4</sub> <sup>1</sup>/<sub>5</sub> <sup>1</sup>/<sub>4</sub> <sup>1</sup>/<sub>5</sub> <sup>1</sup>/<sub>4</sub> <sup>1</sup>/<sub>5</sub> <sup>1</sup>/<sub>4</sub> <sup>1</sup>/<sub>5</sub> <sup>1</sup>/<sub>4</sub> <sup>1</sup>/<sub>4</sub> <sup>1</sup>/<sub>5</sub> <sup>1</sup>/<sub>4</sub> <sup>1</sup>/<sub>5</sub> <sup>1</sup>/<sub>4</sub> <sup>1</sup>/<sub></sub></li></ul>	e FacetProvider hals in Session 4 iplate <typename osmodelp,<br="">typename Facet_P&gt; ss FacetProvider { tatic Facet&amp; get_facet( AppMainParameter e specialization for different plat: get_facet() returns reference t d init( Os::AppMainParameter&amp; value ) radio_ = &amp;wiselib::FacetProvider<os, os:<br="">value ); :Radio::self_pointer_t radio_;</os,></typename>	forms to facet	• Templat $\rightarrow$ Intern 1 ten 2 cla 4 5 5 } • Templat • Method 1 voi 2 { 3 cla 4 5 5 }	<pre>te FacetProvider nals in Session 4 mplate<typename facet_p="" osmodelp,="" typename=""> ass FacetProvider { static Facet&amp; get_facet( AppMainParan te specialization for different p get_facet() returns reference id init( Os::AppMainParameter&amp; value radio_ = &amp;wiselib::FacetProvider<os, );="" ::radio::self_pointer_t="" pre="" radio_;<="" value=""></os,></typename></pre>	platforms ce to facet
annis Chatzigiannakis	Pervasive Systems	Lecture 17 46 / 49	loannis Chatzigiannakis	Pervasive Systems	Lecture 17 46 /



000000	Design Of The Wiselib ೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦	Hardware Abstraction with OS Facets ○○○○○○○○○○○○●○	Motivation 0000000	Design Of The Wiselib ೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦	Hardware Abstraction with OS Facet: ○○○○○○○○○○○○○○○			
Sense Ap	plication		Facet Instantiation	lication				
1 2 3 4 5 6 7 8 9 10 11 11 2 3 4 5 6 7 8 9 10 11	<pre>https://www.secking.com/secking/s</pre>	); nse_radio.h"	1 2 3 4 5 6 7 8 9 10 11 2 3 4 5 6 7 8 9 10 11 2 3 4 5 6 7 8 9 10 11 2 3 4 5 6 7 8 9 10 11 2 3 4 5 6 7 8 9 10 11 2 3 4 5 6 7 8 9 10 11 2 11 11 11 11 11 11 11 11	<pre>te facets usually expect isense:: template<typename osmodel_p=""> class iSenseRadioModel : public isense::Receiver { iSenseRadioModel( isense::Os&amp; os ) : os_(os) { osdispatcher().add_receiver( this }  } ttly used as members #include "external_interface/isense/ise typedef wiselib::iSenseOsModel Os; class iSenseDemoApplication { iSenseDemoApplication { isense::Application(os), radio_(os) {} Os::Radio radio_;</typename></pre>	s ); ense_radio.h"			
12 Dannis Chatzigiannakis Motivation	} s Pervasive Systems Design Of The Wiselib	Lecture 17 48 / 49 Hardware Abstraction with OS Facets	12 Ioannis Chatzigiannakis Motivation	} Pervasive Systems Design Of The Wiselib	Lecture 17 48 / Hardware Abstraction with OS Face			
acet Instantiation	000000000000000000000000000000000000000	000000000000000	0000000 Facet Instantiation	000000000000000000000000000000000000000	00000000000 <b>0000</b>			
Shawn Ap	plication	hawn Application			Shawn Application			
→ 1 2 3 4 5 6 7	<pre>wwn facets usually expect ShawnOs i Defined in external_interface/s template<typename osmodel_p=""> class ShawnRadioModel { ShawnRadioModel( ShawnOs&amp; os ) : os_(os) {} ShawnOs&amp; os_;</typename></pre>		• Shaw $\rightarrow D$ 1 2 3 4 5 6 7	<pre>/n facets usually expect ShawnOs efined in external_interface/s template<typename osmodel_p=""> class ShawnRadioModel {     ShawnRadioModel ( ShawnOs&amp; os )         : os_(os)     {}      ShawnOs&amp; os_; </typename></pre>				
→ 1 2 3 4 5 6 7	Defined in external_interface/s template <typename osmodel_p=""> class ShawnRadioModel { ShawnRadioModel(ShawnOs&amp;os) : os-(os) {}  ShawnOs&amp; os-; ectly used as members #include "external_interface/shawn/shaw</typename>	hawn/shawn_types.h	• Shaw $\rightarrow D$ 1 2 3 4 5 6 7	<pre>vn facets usually expect ShawnOs efined in external_interface/s template<typename osmodel_p=""> class ShawnRadioModel {    ShawnRadioModel(ShawnOs&amp; os)         : os_(os)    {}   </typename></pre>	shawn/shawn_types.h			
→ 1 2 3 4 5 6 7	Defined in external_interface/s template <typename osmodel_p=""> class ShawnRadioModel { ShawnRadioModel(ShawnOs&amp; os) : os.(os) {} ShawnOs&amp; os.; ectly used as members</typename>	hawn/shawn_types.h	• Shaw $\rightarrow D$ 1 2 3 4 5 6 7	<pre>vn facets usually expect ShawnOs efined in external_interface/s template<typename osmodel_p=""> class ShawnRadioModel {    ShawnRadioModel(ShawnOs&amp; os)         : os_(os)         {}     ShawnOs&amp; os_; etly used as members</typename></pre>	shawn/shawn_types.h			