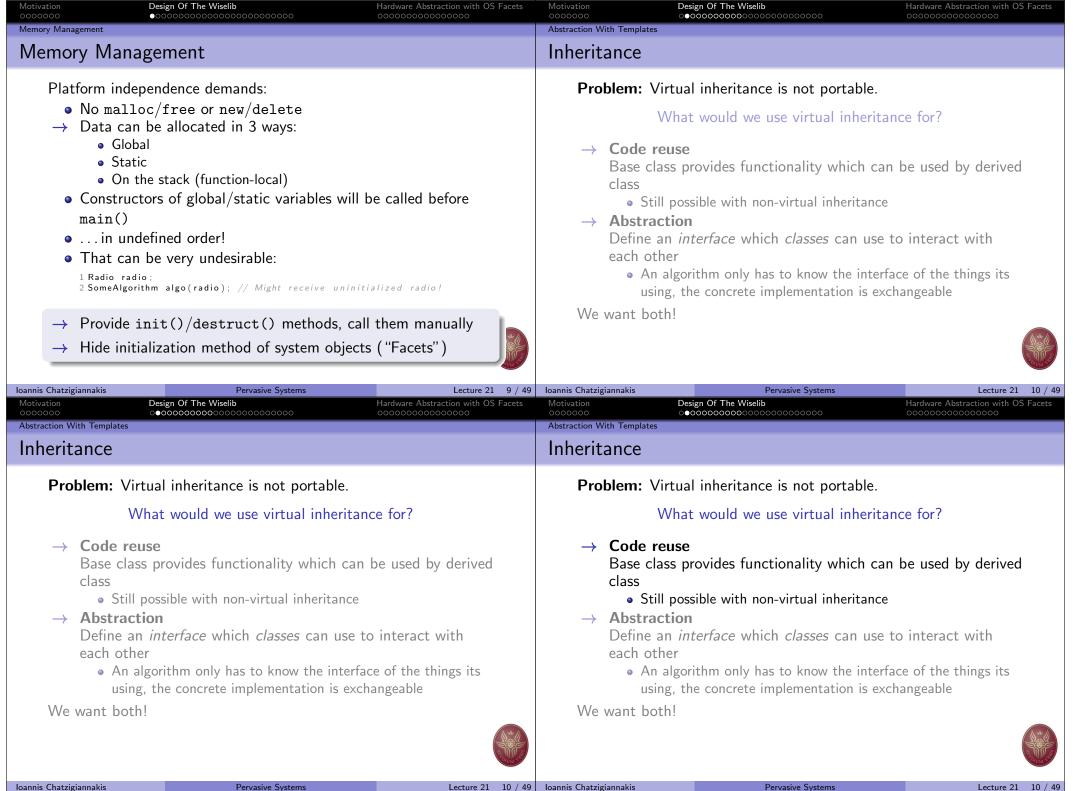
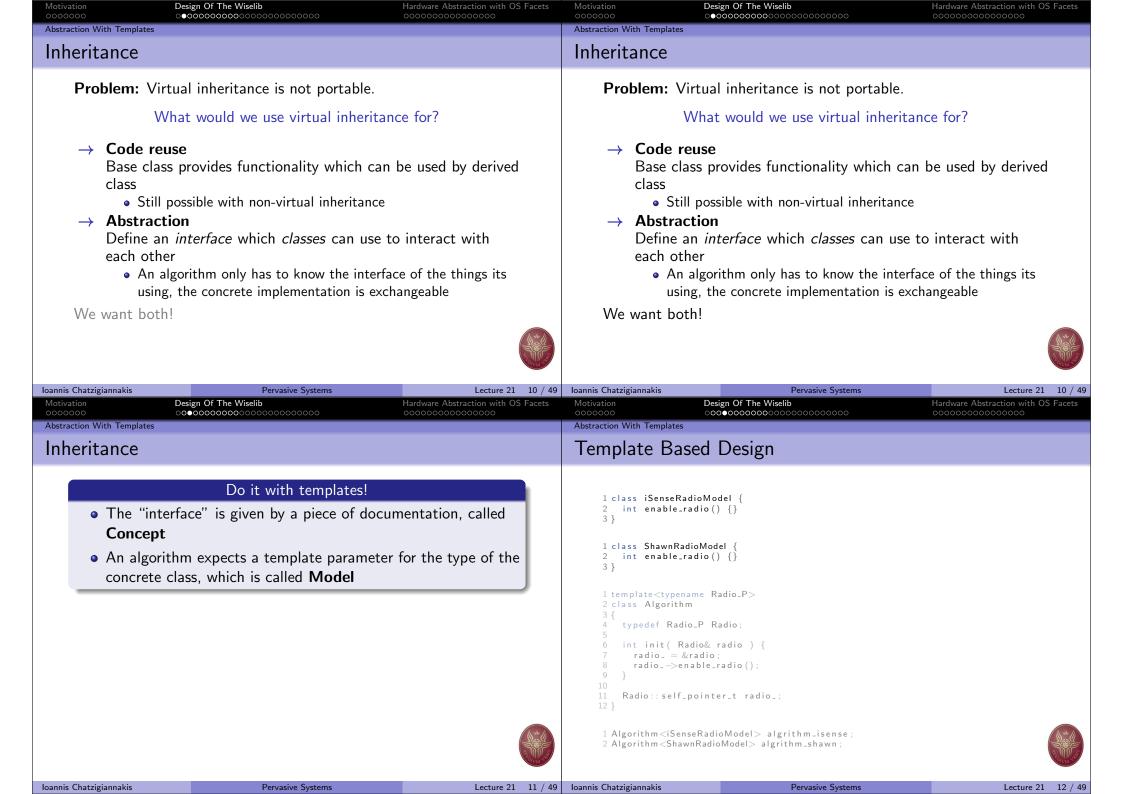


••••••		vare Abstraction with OS Facets Motiv ••••••	000	Design Of The Wiselib 00000000000000000000000000	Hardware Abstraction with OS Facets
A Library Of Algorithms Typical Problems I	n WSN Programming		ary Of Algorithms pical Problen	ns In WSN Programmii	ng
 Ideally they And do not Practioners are Just need a Without ha ⇒ There is need for With lots of alg 			 Ideally And de Practioners Just ne Without ⇒ There is nee With lots of 		gorithms late code ask vears
		vare Abstraction with OS Facets Motiv 000000000000000000000000000000000000		Pervasive Systems Design Of The Wiselib 000000000000000000000000000000000000	Lecture 21 2 / 4 Hardware Abstraction with OS Facets 000000000000000
A library of about 5 • Extensible • Combineable • Exchangeable	The Wiselib 0 algorithms, lots more to come! he following algorithm categories • Metrics • Routing • Synchronizat • Topology Co • Tracking	These are	 A C++ pro Free (as in NOT a m githul There you'll find The Viseli The Wiseli The Bugtra 	oject freedom), licensed under LG iddleware (we will see later w b.com/ibr-alg d: nentation Wiki b Sourcecode	^{hy)} 5/wiselib
oannis Chatzigiannakis	Pervasive Systems	Lecture 21 3 / 49 Ioannis	Chatzigiannakis	Pervasive Systems	Lecture 21 4 / 4

ooooo .ibrary Of Algorithms	000000000000000000000000000000000000000	0000000	Hardware Abstraction	0000	Motivation 0000000 Platform Independence	Design Of The Wiselib 000000000000000000000000000000000000	Hardware Abstraction with OS
/iselib Distrib	outions				Platform Inde	ependence	
• New thin	velopment ssarily tested on gs that may still early, release oft	change their int	erface		use diffe The Wi So Wh In lots c But	cientists all over the world work erent experimentation environm selib aims to be versatile it can be used for different tasks nich also require different hardwa of applications we need heterog t do not want to write the same co	re geneous nodes
Stable						h node type	
	n all supported p s will not change				$\rightarrow W$	e want the Wiselib to be platfo	rm independent!
us Chatzigiannakis	Dor	vasive Systems		Lecture 21 5 / 40	Ioannis Chatzigiannakis	Parussive Systems	Lecture 21
ivation 00000 form Independence	Design Of The Wiselib 000000000000000000000000000000000000	vasive Systems 00000000	Hardware Abstracti 0000000000000		Ioannis Chatzigiannakis Motivation 000000 Platform Independence	Pervasive Systems Design Of The Wiselib 000000000000000000000000000000000000	Lecture 21 Hardware Abstraction with OS 00000000000000000
nis Chatzigiannakis tivation 00000 tform Independence latform Indep	Design Of The Wiselib 000000000000000000000000000000000000	0000000	00000000000000	on with OS Facets	Motivation ○○○○○●	Design Of The Wiselib 000000000000000000000000000000000000	Hardware Abstraction with OS
ivation form Independence atform Indep iSense	Design Of The Wiselib 000000000000000000000000000000000000			on with OS Facets	Motivation 000000 Platform Independence Platform Independence • Some p • And/or • Some da • No • So The "extrem	Design Of The Wiselib 000000000000000000000000000000000000	Hardware Abstraction with OS 000000000000000000000000000000000000
ivation Doooo form Independence latform Indep	Design Of The Wiselib 000000000000000000000000000000000000	0000000	00000000000000	on with OS Facets	Motivation 000000 Platform Independence Platform Independence • Some platform Independence • And/or • Some da • No • So • So • C (exce	Design Of The Wiselib cococococococococococococococococococo	Hardware Abstraction with OS 000000000000000000000000000000000000
tivation bood of tform Independence Iatform Independence iSense Weight of the second sec	Design Of The Wiselib 000000000000000000000000000000000000	OCCOORDINATION OF CONTRACTOR O	MSP 430	on with OS Facets	Motivation 000000 Platform Independence Platform Independence • Some pl • And/or • Some da • No • So The "extrem • C (exce • Static n	Design Of The Wiselib cocccocccoccccccccccccccccccccccccccc	Hardware Abstraction with OS 000000000000000000000000000000000000
tivation DOO O tform Independence iSense Hardware Jennic Operating System iSense	Design Of The Wiselib 000000000000000000000000000000000000	ScatterWeb MSB	Tmote Sky	on with OS Facets	Motivation COCOCO Platform Independence Platform Independence Platform Independence Some platform Independence And/or Some du Some du Some du Some du Some function Some du Some function Some du Some function Some du Some function Some function	Design Of The Wiselib coccoccoccoccoccoccoccoccoccoccoccoccoc	Hardware Abstraction with OS 000000000000000000000000000000000000
tivation DOO O O tform Independence Iatform Independence iSense	Design Of The Wiselib 000000000000000000000000000000000000	OCCOORDINATION OF CONTRACTOR O	MSP 430	on with OS Facets	Motivation OCOCOCO Platform Independence Platform Independence Platform Independence OCOCOCO Some da OCOCOCO OCOCOCO OCOCOCO OCOCOCOCOCO	Design Of The Wiselib cococococococococococococococococococo	Hardware Abstraction with OS cocococococococo c memory ent ual inheritance, etc
tivation DOO O tform Independence Iatform Independence iSense Hardware Jennic Operating System iSense ROM / RAM 128kB / 92kB Memory Managemen	Design Of The Wiselib cococococococococococococococococococo	ScatterWeb MSB	Contiki / TinyOS 48kB / 10kB	on with OS Facets	Motivation OCOCOCO Platform Independence Platform Independence Platform Independence OCOCOCO Some da OCOCOCO OCOCOCO OCOCOCO OCOCOCOCOCO	Design Of The Wiselib coccoccoccoccoccoccoccoccoccoccoccoccoc	Hardware Abstraction with OS cocococococococo c memory ent ual inheritance, etc
ivation form Independence isense isense Hardware Jennic Operating System isense ROM / RAM 128kB / 92kB Memory Managemen Dynamic	Design Of The Wiselib occocococococococococo Dendence iMote2 Intel XScale TinyOS 32MB / 32MB t Dynamic	ScatterWeb MSB	Contiki / TinyOS	on with OS Facets	Motivation OCOCOCO Platform Independence Platform Independence Platform Independence OCOCOCO Some plate And/or Some da No Some da No Some da Cococococococococococococococo The "extrem Cocococococococococococococococococococ	Design Of The Wiselib cooccoccoccoccoccoccoccoccoccoccoccocco	Hardware Abstraction with OS cocococococococo c memory ent ual inheritance, etc <cmath>)</cmath>
ivation form Independence atform Independence iSense Hardware Jennic Operating System iSense ROM / RAM 128kB / 92kB Memory Managemen	Design Of The Wiselib occocococococococococo Dendence iMote2 Intel XScale TinyOS 32MB / 32MB t Dynamic	ScatterWeb MSB	Contiki / TinyOS 48kB / 10kB	on with OS Facets	Motivation OCOCOCO Platform Independence Platform Independence Platform Independence OCOCOCO Some plate And/or Some da No Some da No Some da Cococococococococococococococo The "extrem Cocococococococococococococococococococ	Design Of The Wiselib cococococococococococococococococococo	Hardware Abstraction with OS cocococococococo c memory ent ual inheritance, etc <cmath>)</cmath>

Ammony Management Memory Management Platform independence demands: • No malloc/free or new/delete • No malloc/free or new/delete • On the allocated in 3 ways: • Global • Static • On the stack (function-local) • Clobal • Static • On the stack (function-local) • Constructors of global/static variables will be called before main() • On the stack (function-local) • in undefined order! • That can be very undesirable: • Isadio radic; Isadio radic; • Provide init()/destruct() methods, call them manually • Hide initialization method of system objects ("Facets")	Design Of The Wiselib Hardware Abstraction with OS Facets 0000 000000000000000000000000000000000000	s Motivation Design Of The Wiselib Hardware Abstraction with OS Fa 0000000 0000000000000000000000000000
 No malloc/free or new/delete Data can be allocated in 3 ways: Global Static On the stack (function-local) Constructors of global/static variables will be called before main() in undefined order! That can be very undesirable: isomotigenable isomotigenable isomotigenable Provide init()/destruct() methods, call them manually Hide initialization method of system objects ("Facets") Constructors of global/static variables will be called before main() in undefined order! That can be very undesirable: isomotigenable in		
is Chatzigiannakis No mail oc/free or new/delete No mail oc/free or new/delete • On malloc/free or new/delete • Data can be allocated in 3 ways: • Global • Static • On the stack (function-local) • Static • On the stack (function-local) • Constructors of global/static variables will be called before main() •in undefined order! • That can be very undesirable: I Radio radio: 2.5smeAlgorithm algo(radio); // Might receive uninitialized radio! Provide init()/destruct() methods, call them manually Provide init()/destruct() methods, call them manually Provide init()/destruct() methods, call them manually 	 No malloc/free or new/delete → Data can be allocated in 3 ways: Global Static On the stack (function-local) Constructors of global/static variables will be called before main() in undefined order! That can be very undesirable: I Radio radio; SomeAlgorithm algo(radio); // Might receive uninitialized radio! Provide init()/destruct() methods, call them manually 	 No malloc/free or new/delete → Data can be allocated in 3 ways: Global Static On the stack (function-local) Constructors of global/static variables will be called before main() in undefined order! That can be very undesirable: 1 Radio radio; 2 SomeAlgorithm algo(radio); // Might receive uninitialized radio! Provide init()/destruct() methods, call them manually
Management Memory Management Platform independence demands: No malloc/free or new/delete Data can be allocated in 3 ways: Global Static On the stack (function-local) Platform independence demands: • Constructors of global/static variables will be called before main() • Constructors of global/static variables will be called before main() • That can be very undesirable: I Radio radio; 2 SomeAlgorithm algo(radio); // Might receive uninitialized radio! → Provide init()/destruct() methods, call them manually Memory Management	is Chatzigiannakis Pervasive Systems Lecture 21 9 / ivation Design Of The Wiselib Hardware Abstraction with OS Facets	49 Ioannis Chatzigiannakis Pervasive Systems Lecture 21 s Motivation Design Of The Wiselib Hardware Abstraction with OS F.
Platform independence demands: No malloc/free or new/delete Data can be allocated in 3 ways: Global Static On the stack (function-local) Constructors of global/static variables will be called before main() Constructors of global/static variables will be called before main() in undefined order! That can be very undesirable: IRadio radio: SomeAlgorithm algo(radio); // Might receive uninitialized radio) Provide init()/destruct() methods, call them manually 	nory Management	Memory Management
	<pre>Platform independence demands: No malloc/free or new/delete Data can be allocated in 3 ways: Global Static On the stack (function-local) Constructors of global/static variables will be called before main() in undefined order! That can be very undesirable: IRadio radio; </pre>	<pre>Platform independence demands: • No malloc/free or new/delete → Data can be allocated in 3 ways: • Global • Static • On the stack (function-local) • Constructors of global/static variables will be called before main() •in undefined order! • That can be very undesirable: • Radio_radio;</pre>





Motivation Design Of The Wiselib 000000000000000000000000000000000000	Hardware Abstraction with OS Facets	Motivation Design Of The Wiselib 0000000 000000000000000000000000000000000000	Hardware Abstraction with OS Facets
Abstraction With Templates Template Based Design		Abstraction With Templates Template Based Design	
<pre>1 class iSenseRadioModel { 2 int enable_radio() {} 3 } 1 class ShawnRadioModel { 2 int enable_radio() {} 3 }</pre>		<pre>1 class iSenseRadioModel { 2 int enable_radio() {} 3 } 1 class ShawnRadioModel { 2 int enable_radio() {} 3 }</pre>	
<pre>1 template<typename radio_p=""> 2 class Algorithm 3 { 4 typedef Radio_P Radio; 5 6 int init(Radio& radio) { 7 radio_ = &radio 8 radio_=>enable_radio(); 9 } 10 11 Radio::self_pointer_t radio_; 12 }</typename></pre>		<pre>1 template<typename radio_p=""> 2 class Algorithm 3 { 4 typedef Radio_P Radio; 5 6 int init(Radio& radio) { 7 radio_ = &radio 8 radio>enable_radio(); 9 } 10 11 Radio::self_pointer_t radio_; 12 }</typename></pre>	
1 Algorithm <isenseradiomodel> algrithm_isense 2 Algorithm<shawnradiomodel> algrithm_shawn;</shawnradiomodel></isenseradiomodel>		1 Algorithm <isenseradiomodel> algrithm_isense; 2 Algorithm<shawnradiomodel> algrithm_shawn;</shawnradiomodel></isenseradiomodel>	
Dannis Chatzigiannakis Pervasive System Motivation Design Of The Wiselib Docococo COCOCOCOCOCOCOCOCOCOCOCOCOCOCOCOCOCOCO	Hardware Abstraction with OS Facets	Ioannis Chatzigiannakis Pervasive Systems Motivation Design Of The Wiselib 000000000000000000000000000000000000	Lecture 21 12 / - Hardware Abstraction with OS Facets
Abstraction With Templates		Abstraction With Templates	
Template Based Design		Template Based Design	
		Template Dased Design	
1 class iSenseRadioModel { 2 int enable_radio() {} 3 }		1 class iSenseRadioModel { 2 int enable_radio() {} 3 }	
2 int enable_radio() {}		1 class iSenseRadioModel { 2 int enable_radio() {}	
<pre>2 int enable_radio() {} 3 } 1 class ShawnRadioModel { 2 int enable_radio() {}</pre>		<pre>1 class iSenseRadioModel { 2 int enable_radio() {} 3 } 1 class ShawnRadioModel { 2 int enable_radio() {}</pre>	
<pre>2 int enable_radio() {} 3 } 1 class ShawnRadioModel { 2 int enable_radio() {} 3 } 1 template<typename radio_p=""> 2 class Algorithm 3 { 4 typedef Radio_P Radio; </typename></pre>		<pre>1 class iSenseRadioModel { 2 int enable_radio() {} 3 } 1 class ShawnRadioModel { 2 int enable_radio() {} 3 } 1 template<typename radio_p=""> 2 class Algorithm 3 { 4 typedef Radio_P Radio; </typename></pre>	
<pre>2 int enable_radio() {} 3 } 1 class ShawnRadioModel { 2 int enable_radio() {} 3 } 1 template<typename radio_p=""> 2 class Algorithm 3 {</typename></pre>		<pre>1 class iSenseRadioModel { 2 int enable_radio() {} 3 } 1 class ShawnRadioModel { 2 int enable_radio() {} 3 } 1 template<typename_radio_p> 2 class Algorithm 3 {</typename_radio_p></pre>	
<pre>2 int enable_radio() {} 3 } 1 class ShawnRadioModel { 2 int enable_radio() {} 3 } 1 template<typename radio_p=""> 2 class Algorithm 3 { 4 typedef Radio_P Radio; 5 int init(Radio& radio) { 7 radio_ = &radio 8 radio_= = &radio 9 } </typename></pre>		<pre>1 class iSenseRadioModel { 2 int enable_radio() {} 3 } 1 class ShawnRadioModel { 2 int enable_radio() {} 3 } 1 class Algorithm 3 { 4 typedef Radio_P Radio; 5 6 int init(Radio& radio) { 7 radio_ = & radio; 8 radio_ = & enable_radio(); 9 } </pre>	
<pre>2 int enable_radio() {} 3 } 1 class ShawnRadioModel { 2 int enable_radio() {} 3 } 1 template<typename radio_p=""> 2 class Algorithm 3 { 4 typedef Radio_P Radio; 5 6 int init(Radio& radio) { 7 radio_ = & radio; 8 radio>enable_radio(); </typename></pre>		<pre>1 class iSenseRadioModel { 2 int enable_radio() {} 3 } 1 class ShawnRadioModel { 2 int enable_radio() {} 3 } 1 class Algorithm 3 { 4 typedef Radio_P Radio; 5 int init(Radio& radio) { 7 radio_ = & radio; 8 radio_>enable_radio(); 9 } </pre>	
<pre>2 int enable_radio() {} 3 } 1 class ShawnRadioModel { 2 int enable_radio() {} 3 } 1 template<typename radio_p=""> 2 class Algorithm 3 { 4 typedef Radio_P Radio; 5 6 int init(Radio& radio) { 7 radio_ = & radio; 8 radio>enable_radio(); 9 } 10 11 Radio::self_pointer_t radio_; </typename></pre>		<pre>1 class iSenseRadioModel { 2 int enable_radio() {} 3 } 1 class ShawnRadioModel { 2 int enable_radio() {} 3 } 1 template<typename radio_p=""> 2 class Algorithm 3 { 4 typedef Radio_P Radio; 5 int init(Radio& radio) { 7 radio_ = & radio; 8 radio_=>enable_radio() 9 } 10 11 Radio_self_pointer_t radio_; </typename></pre>	

0000000 Abstraction	14/2-1	T 1.
Motivation		

Design Of The Wiselib

Hardware Abstraction with OS Facets

Abstraction

Concept

- Describes behaviour of components
- E.g. "A Radio has a void send(char*) method"
- Only documentation

Model

- Actual class
- Implements any number of concepts
- E.g. A routing protocol may implement the radio concept
- ...so it can be used like one

Abstraction With Templates

Design Of The Wiselib

Hardware Abstraction with OS Facets

Abstraction

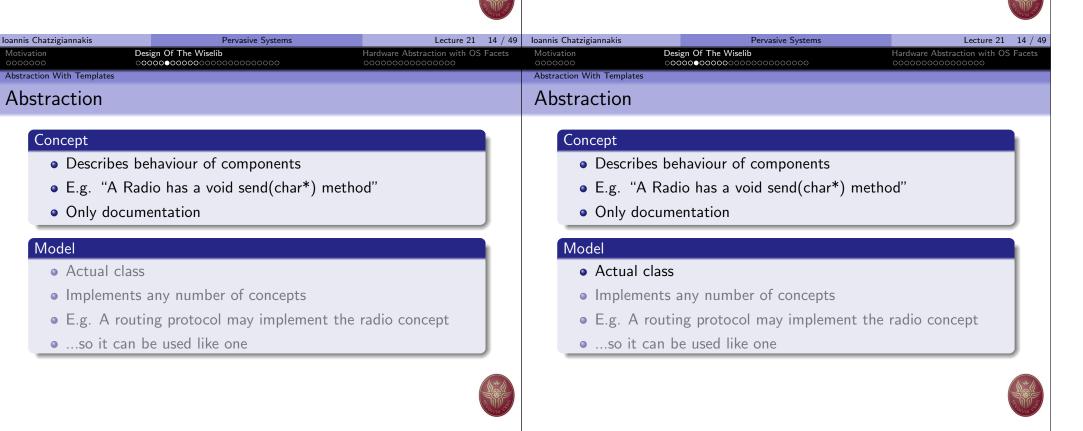
Motivation

Concept

- Describes behaviour of components
- E.g. "A Radio has a void send(char*) method"
- Only documentation

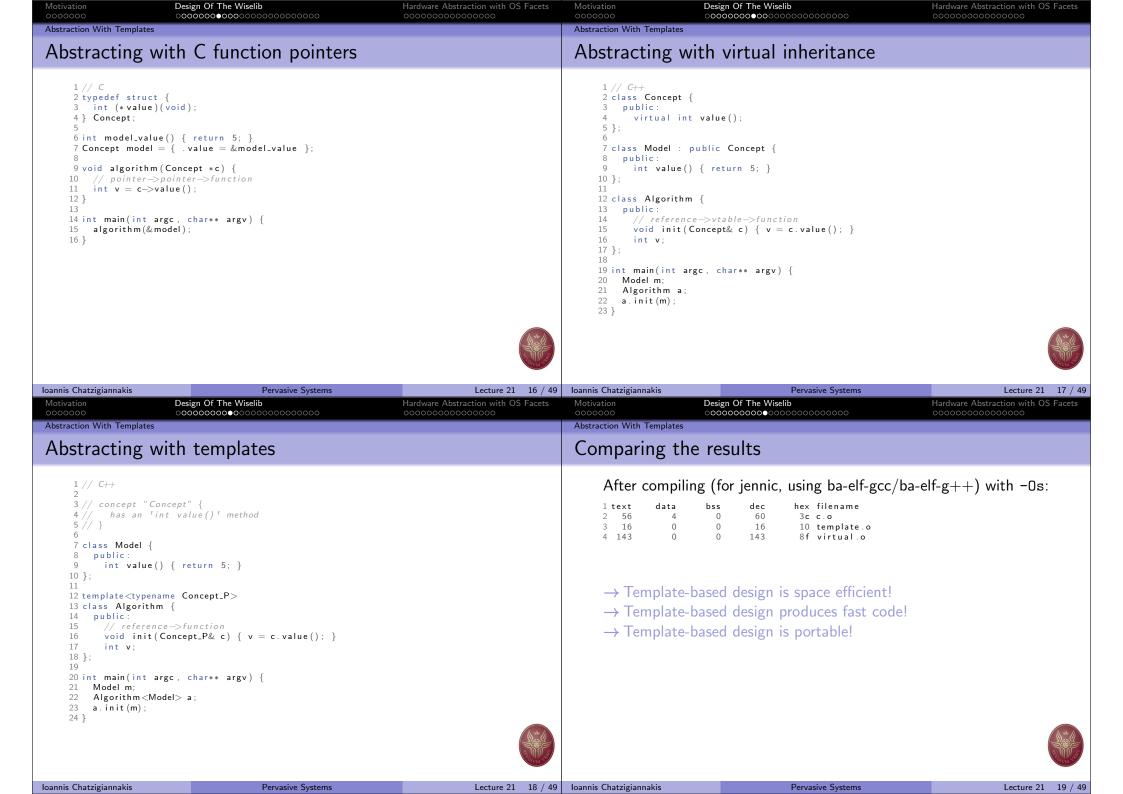
Model

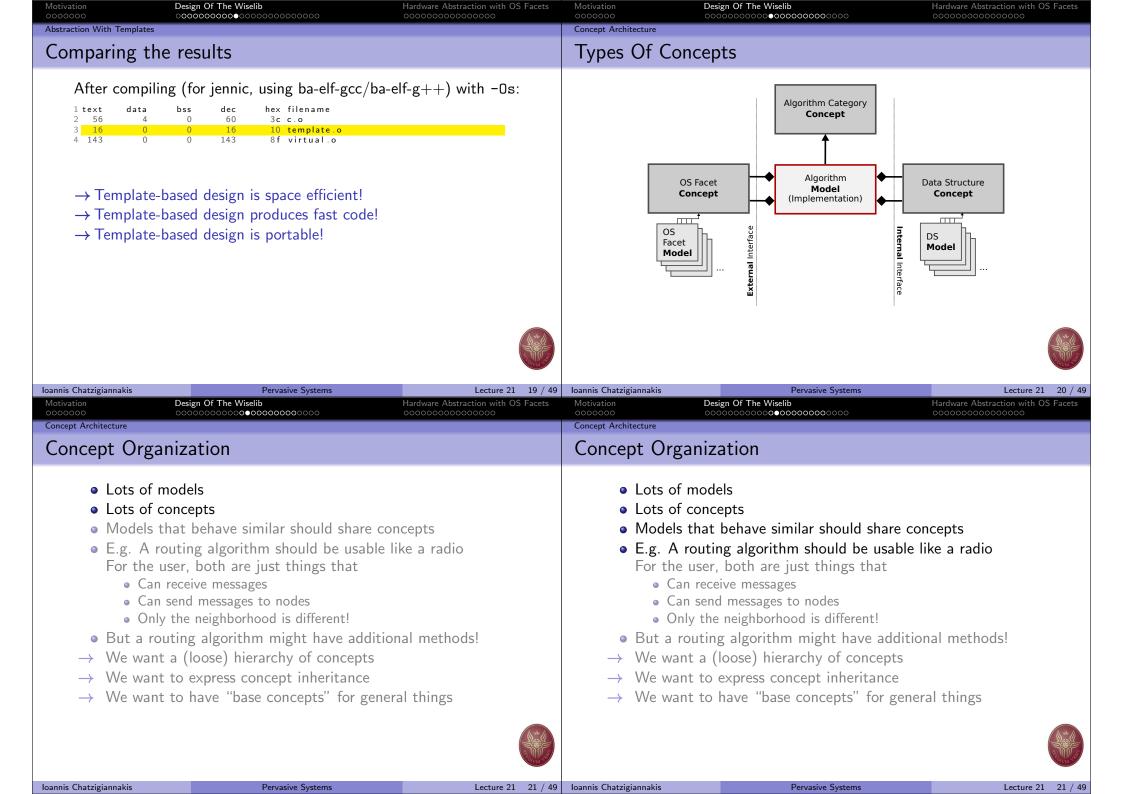
- Actual class
- Implements any number of concepts
- $\bullet\,$ E.g. A routing protocol may implement the radio concept
- ...so it can be used like one



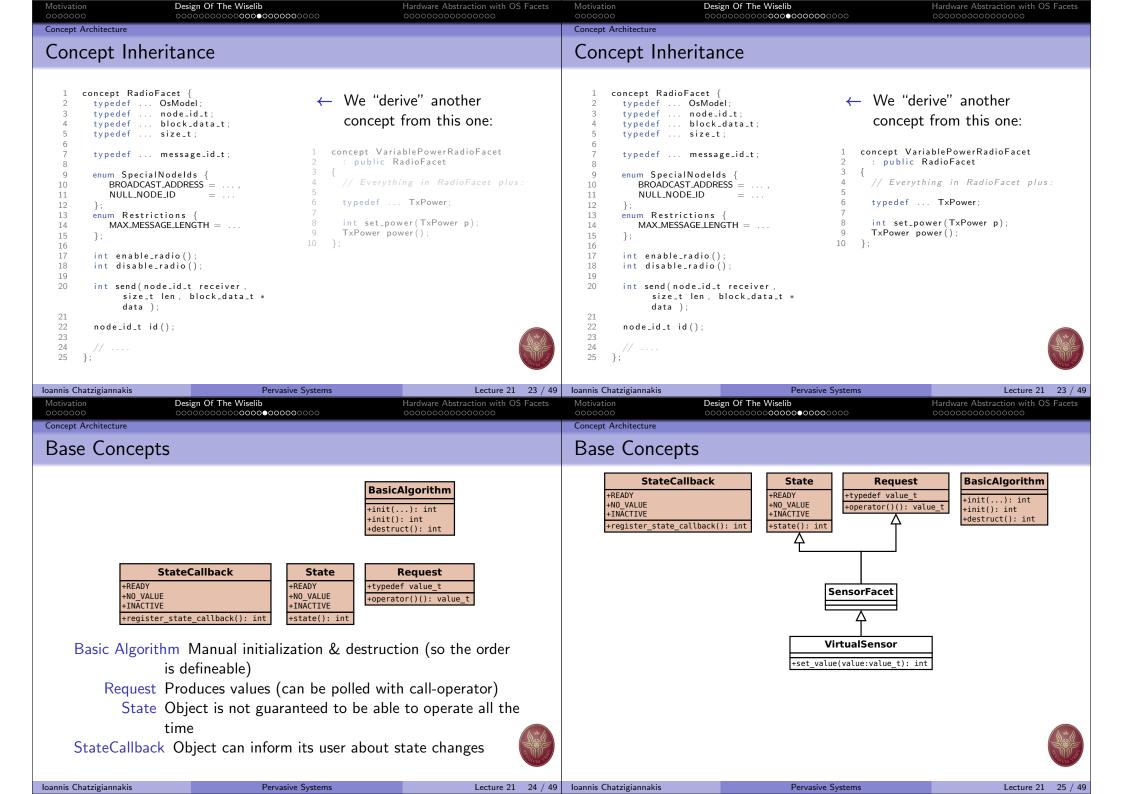
Motivation	Design Of The Wiselib ○0000€00000000000000000000000000000000	Hardware Abstraction with OS Facets	0000	vation 0000	Design Of The Wiselib ○○○○○●○○○○○○○○○○○○○○○○○○	Hardware Abstraction wi	th OS Face
bstraction With Ten	·			raction With Templat			
 E.g Onl Model Act Imp E.g 	scribes behaviour of components . "A Radio has a void send(char* ly documentation cual class elements any number of concepts . A routing protocol may implement o it can be used like one			 E.g. " Only of Model Actua Impler E.g. A 	ibes behaviour of componer 'A Radio has a void send(ch documentation I class ments any number of conce A routing protocol may impl t can be used like one	nar*) method"	
nis Chatzigiannaki vivation 000000 straction With Ten bstractic	Design Of The Wiselib ○0000€00000000000000000000000000000000	Lecture 21 14 / 49 Hardware Abstraction with OS Facets 000000000000000000000000000000000000	Motiv 0000 Abstr	is Chatzigiannakis vation oooo raction With Templat	Pervasive System Design Of The Wiselib 000000000000000000000000000000000000	Hardware Abstraction wi	
• E.g	scribes behaviour of components . "A Radio has a void send(char* ly documentation) method"		● Ir	are other ways to provide an C, one would usually abstrace n C++ one would use virtual	t with function pointers	
• Imp • E.g	cual class plements any number of concepts . A routing protocol may impleme o it can be used like one	ent the radio concept		H	ow do they compare to the	template approach?	
							A CONTRACT OF A CONTRACTACT OF A CONTRACT OF

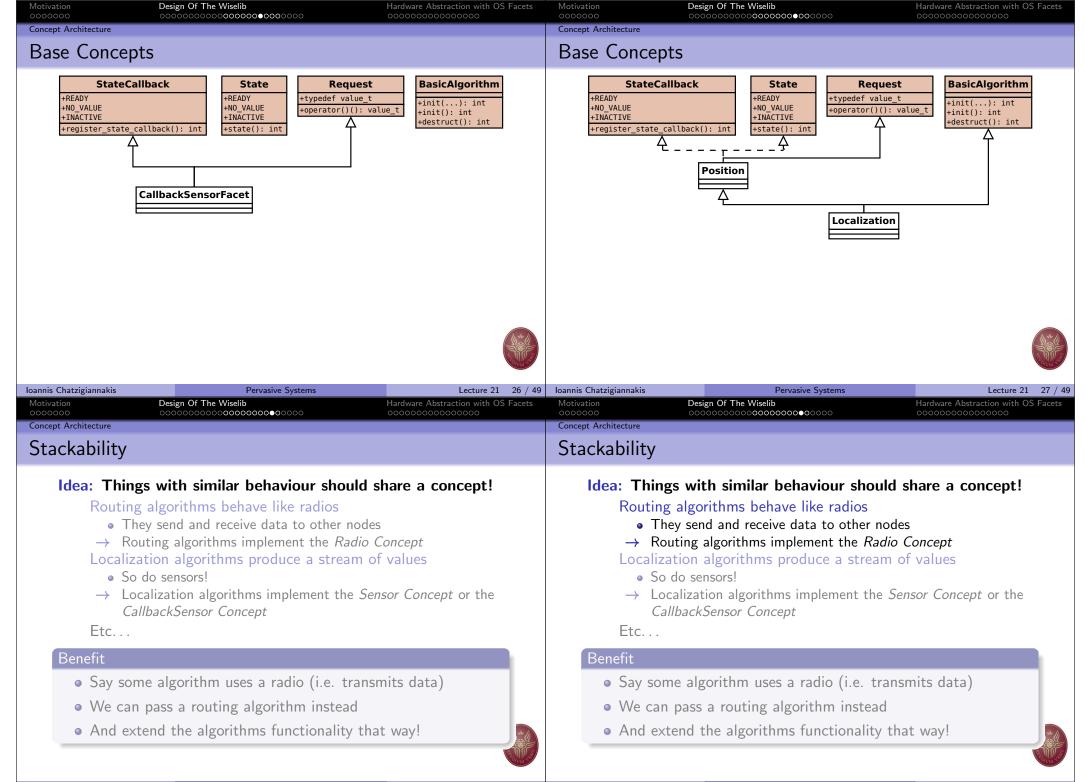
Ioannis Chatzigiannakis



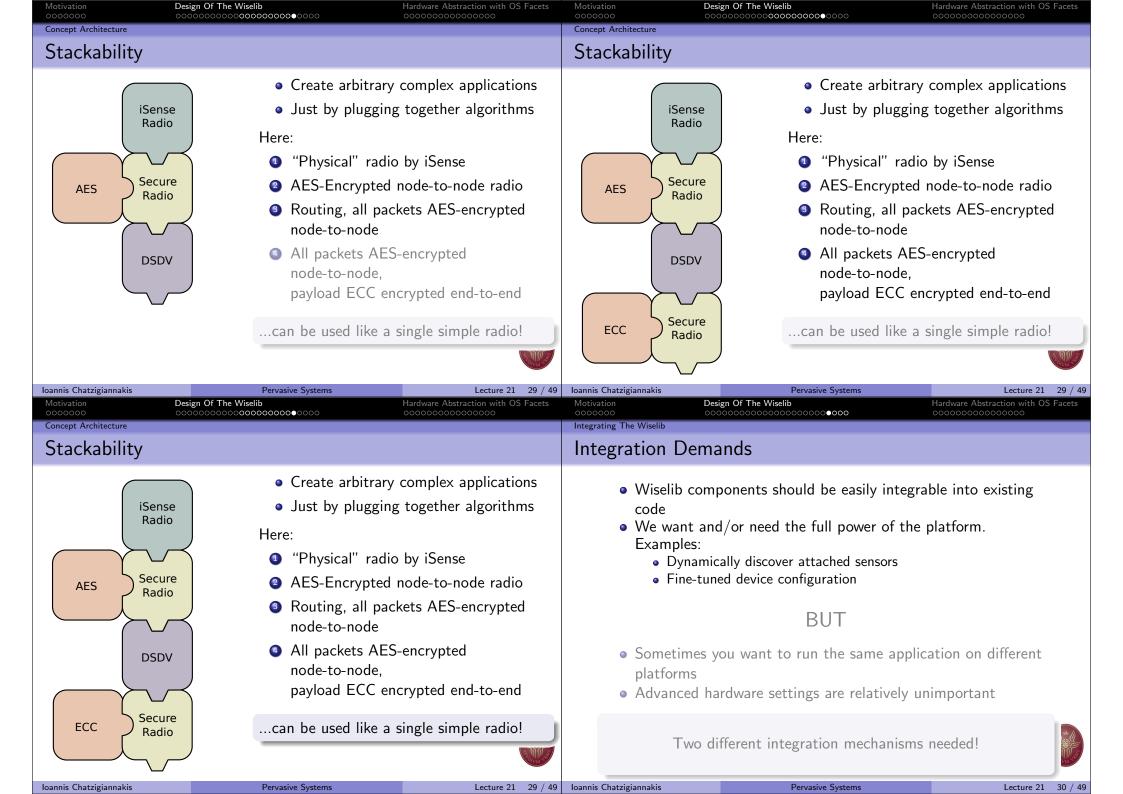


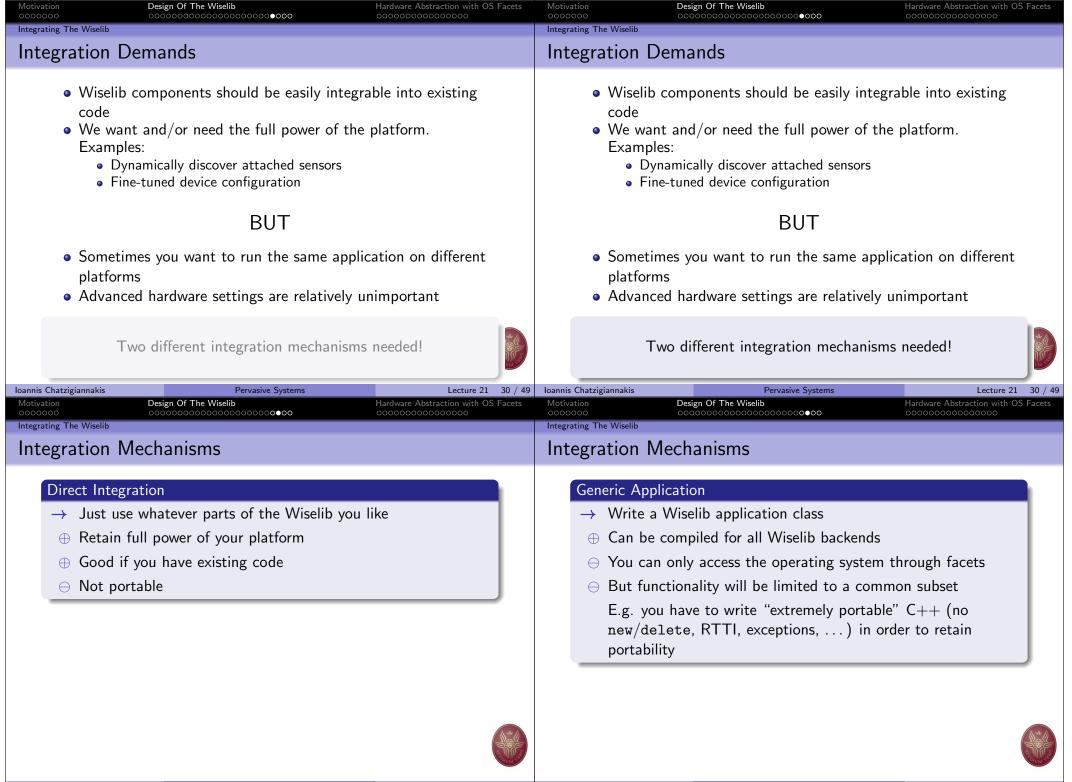
Motivation 0000000	Design Of The Wiselib ०००००००००० ●०००००० ००००	Hardware Abstraction with OS Facets	Motivation 0000000	Design Of The Wiselib ०००००००००० 00000000 00000	Hardware Abstraction with OS Fac
Concept Architecture	Organization		Concept Architecture Concept Org	anization	
• Lots • Mod • E.g. For t • • But \rightarrow We v \rightarrow We v	a of models of concepts lels that behave similar should shar A routing algorithm should be usa the user, both are just things that Can receive messages Can send messages to nodes Only the neighborhood is different! a routing algorithm might have ad want a (loose) hierarchy of concept want to express concept inheritance want to have "base concepts" for g	ble like a radio ditional methods! ts e	 E.g. A For the Ca Ca Or But a r → We was → We was 		able like a radio dditional methods! pts ce
annis Chatzigiannakis Motivation Doocooo Concept Architecture	Pervasive Systems Design Of The Wiselib 000000000000000000000000000000000000	Lecture 21 21 / 49 Hardware Abstraction with OS Facets 000000000000000	Ioannis Chatzigiannakis Motivation 0000000 Concept Architecture The OsMode	Pervasive Systems Design Of The Wiselib 000000000000000000000000000000000000	Lecture 21 21 Hardware Abstraction with OS Fac 0000000000000000
 Lots Lots Mod E.g. For t But → We v → We v 	a of models s of concepts lels that behave similar should shar A routing algorithm should be usa the user, both are just things that Can receive messages Can send messages to nodes Only the neighborhood is different! a routing algorithm might have ad want a (loose) hierarchy of concept want to express concept inheritance want to have "base concepts" for g	ble like a radio ditional methods! ts e	1 concept OsM 2 typedef . 3 typedef . 4 enum Retu 5 6 typedef . 7 typedef . 9 10 static co WISH 11 } • Holds p • Constal • Inc • Ma		=, }; facet ITTLE_ENDIAN or ess, size type, etc) NSPEC (unspecified error)
annis Chatzigiannakis	Pervasive Systems	Lecture 21 21 / 49	Ioannis Chatzigiannakis	Pervasive Systems	Lecture 21 2:

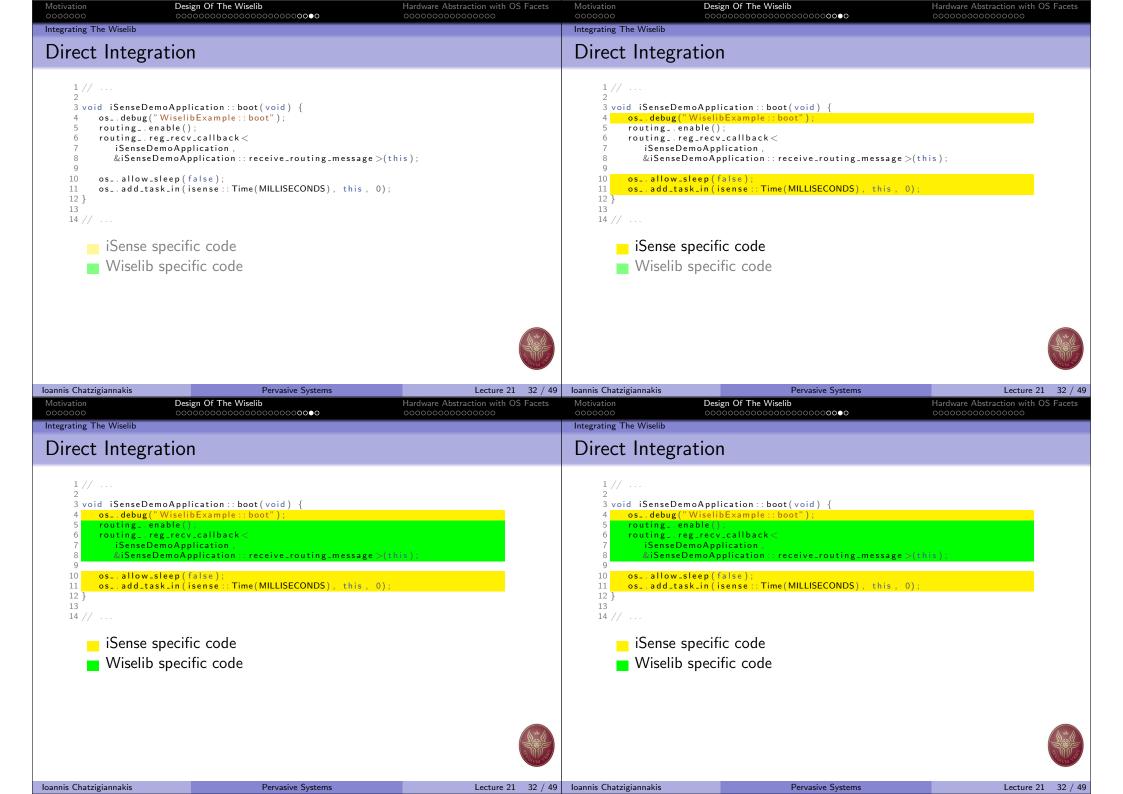


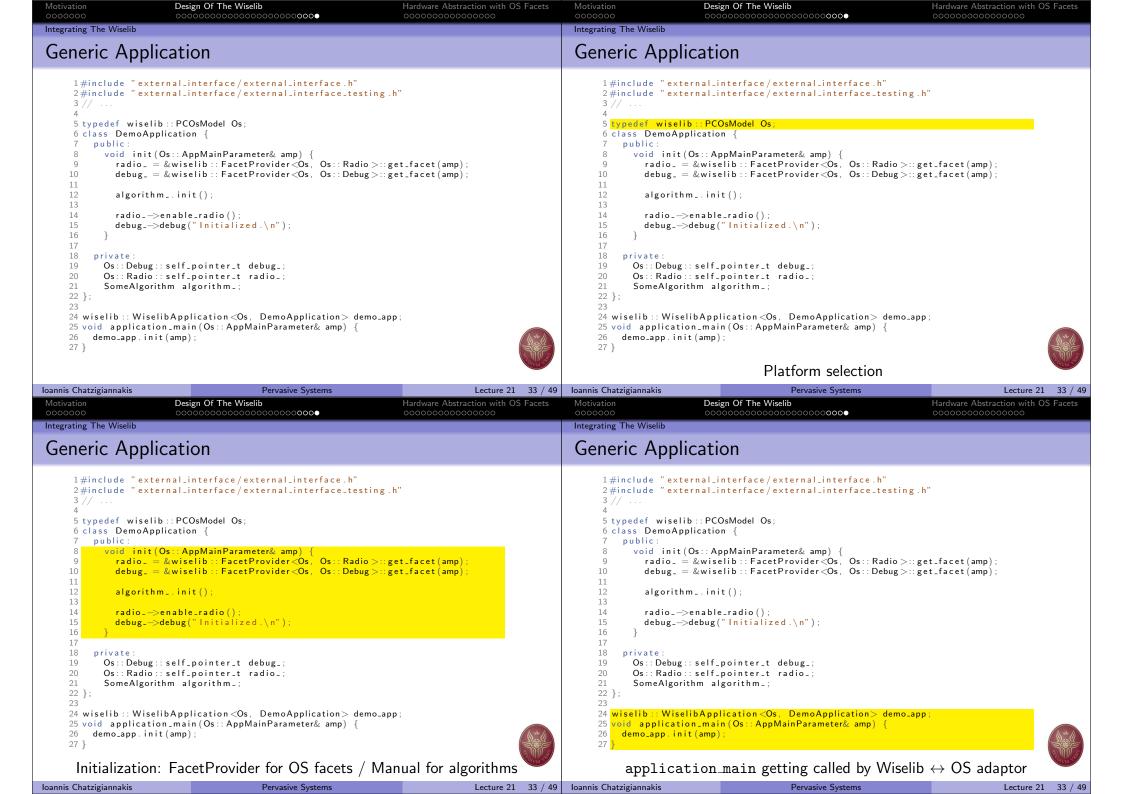


Ioannis Chatzigiannakis









	Abstraction with OS Facets	Motivation 0000000	Design Of The Wiselib 000000000000000000000000000000000000	Hardware Abstraction with OS Facet
Nhat is a Facet?		What is a Fac	et?	
 Connection between algorithms and OS OS Facets (Concepts) OS Facet Radio Facet Timer Facet For each supported OS at least one model per facet ScontikiRadioModel ShawnTimerModel Possible to provide muliple models per facet ContikiRimeRadioModel ContikiGLowPanRadioModel 	ocet	 OS Face OS F Radi Time For each iSens Cont Shav Possible Cont 	Eion between algorithms and OS ets (Concepts) Facet to Facet er Facet supported OS at least one mod seOsModel tikiRadioModel vnTimerModel to provide muliple models per f tikiRimeRadioModel tiki6LowPanRadioModel	
	Lecture 21 34 / 49 Abstraction with OS Facets	loannis Chatzigiannakis Motivation 0000000	Pervasive Systems Design Of The Wiselib 000000000000000000000000000000000000	Lecture 21 34 / Hardware Abstraction with OS Facet
Vhat is a Facet?		What is a Fac	et?	
 Connection between algorithms and OS OS Facets (Concepts) OS Facet Radio Facet Timer Facet For each supported OS at least one model per facet iSenseOsModel ContikiRadioModel ShawnTimerModel Possible to provide muliple models per facet ContikiRimeRadioModel ContikiGLowPanRadioModel 	icet	 OS Face OS F Radi Time For each iSens Cont Shav Possible Cont 	cion between algorithms and OS ets (Concepts) Facet io Facet er Facet a supported OS at least one mod seOsModel tikiRadioModel wnTimerModel to provide muliple models per f tikiRimeRadioModel tiki6LowPanRadioModel	
nnis Chatzigiannakis Pervasive Systems	Lecture 21 34 / 49	Ioannis Chatzigiannakis	Pervasive Systems	Lecture 21 34

Motivation 0000000			he Wiselib DOOOOOOOOO	000000	0				Abstraction with OS Facets	Motivation 0000000	Design Of	The Wiselib 000000000000000000000		bstraction with OS Fac
or SFace	t Overvie	W								Exchange	ability with	Algorithms		
	WP2 OSA Contiki TinyOS iSense ScatterWeb Shawn (⊕ = fully sup	$ \begin{array}{c} \oplus\\ $			$ \begin{array}{c} \oplus\\ $	⊕	Setter Set	○ ⊕	4/7 5/7 2/7 7/7 4/7 6/7	• Pas	 amples Pass routing → Enable flex Pass time-syn → Enable sys Pass localizat → Only some Pass routing- → Debug nod 	e: Flexibility m where a facet is a algorithm where radio kible multihop neighbor chronization algorithm tem-wide time basis ion algorithm where p nodes in the network based debug model wh des that are not conne lly transparent for a	is expected orhoods n where clock is osition is expect need to know th nere debug facet ected to a gatew	ed neir position is expected
annis Chatzigiann lotivation oooooo itroduction -xchang	akis Desig	n Of TI	Perv he Wiselib 0000000000	vasive Syst	tems O	осрі, е –	Har	dware A	Lecture 21 35 / 49 Lecture 21 35 / 49 Abstraction with OS Facets	Ioannis Chatzigiannak Motivation 0000000 Introduction Fxchange	Design Of ⁻ 00000000	Pervasive Systems The Wiselib SOOOOOOOOOOOOOOOO Algorithms	Hardware A oo●○○○○○	Lecture 21 36 bstraction with OS Fac
• B • P: • E:	asic design i ass an algor kamples • Pass routi ⇒ Enable • Pass time ⇒ Enable • Pass local ⇒ Only so • Pass routi	issue rithn ing a e flexi e syst lizatio ome ing-b ; nod	: Flexib n where lgorithm ible multi chronizati cem-wide on algorit nodes in ased deb es that a	ility a fac where ihop n ion alg time l thm w the ne ug mo re not	et is radio eight gorith basis here etwor odel w conr	o is exp oorhood m whe positio k need where d pected	bected ds re clo n is e to kn ebug to a g	ock is expection t facet		• Bas • Pas • Exa	sic design issue ss an algorith amples ■ Pass routing = ⇒ Enable flex ■ Pass time-syn ⇒ Enable sys ■ Pass localizat ⇒ Only some ■ Pass routing- ⇒ Debug noo	C	is expected orhoods n where clock is osition is expect need to know th nere debug facet ected to a gatew	ed neir position is expected
annis Chatzigiann	- Via		Dam	vasive Sys	toms				Lecture 21 36 / 49	Ioannis Chatzigiannak		Pervasive Systems		Lecture 21 36

Motivation 0000000	Design Of The Wiselib ೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦	Hardware Abstraction with OS Facets 00●0000000000000000000000000000000000	Motivation 0000000	Design Of The Wiselib ೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦	Hardware Abstraction with OS Facets 000000000000000000000000000000000000
ntroduction Exchange	eability with Algorithms		Exchangeab	ility with Algorithms	
• Pa • Ex	 asic design issue: Flexibility ass an algorithm where a facet is examples Pass routing algorithm where radio i ⇒ Enable flexible multihop neighbor Pass time-synchronization algorithm ⇒ Enable system-wide time basis Pass localization algorithm where por ⇒ Only some nodes in the network r Pass routing-based debug model where ⇒ Debug nodes that are not connected wantage: Totally transparent for a 	s expected hoods where clock is expected sition is expected need to know their position ere debug facet is expected tted to a gateway position	 Pass a Examp P P<td>design issue: Flexibility an algorithm where a facet is e ples Pass routing algorithm where radio \Rightarrow Enable flexible multihop neighbor Pass time-synchronization algorithm \Rightarrow Enable system-wide time basis Pass localization algorithm where p \Rightarrow Only some nodes in the network Pass routing-based debug model wh \Rightarrow Debug nodes that are not conne- intage: Totally transparent for a</td><td>is expected orhoods in where clock is expected osition is expected need to know their position here debug facet is expected cted to a gateway position</td>	design issue: Flexibility an algorithm where a facet is e ples Pass routing algorithm where radio \Rightarrow Enable flexible multihop neighbor Pass time-synchronization algorithm \Rightarrow Enable system-wide time basis Pass localization algorithm where p \Rightarrow Only some nodes in the network Pass routing-based debug model wh \Rightarrow Debug nodes that are not conne- intage: Totally transparent for a	is expected orhoods in where clock is expected osition is expected need to know their position here debug facet is expected cted to a gateway position
annis Chatzigianna lotivation oooooo itroduction	Design Of The Wiselib 000000000000000000000000000000000000	Lecture 21 36 / 49 Hardware Abstraction with OS Facets OO●○○○○○○○○○○	Ioannis Chatzigiannakis Motivation 0000000 Introduction	Pervasive Systems Design Of The Wiselib 000000000000000000000000000000000000	Lecture 21 36 / Hardware Abstraction with OS Face 00●0000000000000000000000000000000000
exchange	eability with Algorithms		Exchangeab	ility with Algorithms	
• Pa • Ex	asic design issue: Flexibility ass an algorithm where a facet is ex- camples • Pass routing algorithm where radio i \Rightarrow Enable flexible multihop neighbor • Pass time-synchronization algorithm \Rightarrow Enable system-wide time basis • Pass localization algorithm where por \Rightarrow Only some nodes in the network r	s expected hoods where clock is expected sition is expected	● Pass a ● Examp ● P = ● P = ● P	design issue: Flexibility an algorithm where a facet is e ples Pass routing algorithm where radio > Enable flexible multihop neighbo Pass time-synchronization algorithm > Enable system-wide time basis Pass localization algorithm where p > Only some nodes in the network	is expected orhoods or where clock is expected osition is expected

- Pass routing-based debug model where debug facet is expected ⇒ Debug nodes that are not connected to a gateway position
- Advantage: Totally transparent for algorithm

• Advantage: Totally transparent for algorithm

• Pass routing-based debug model where debug facet is expected

 \Rightarrow Debug nodes that are not connected to a gateway position

```
Hardware Abstraction with OS Facets
                                                              Hardware Abstraction with OS Facets
Motivation
                                                                                           Motivation
                                                              Important Facets
                                                                                           Important Facets
The OS Facet
                                                                                           The OS Facet
     1 concept OsFacet
                                                                                                1 concept OsFacet {
     2 typedef ... size_t;
                                                                                                2 typedef ... size_t;
       typedef ... block_data_t; // "byte"-like type for buffers
                                                                                                   typedef ... block_data_t; // "byte"-like type for buffers
     3
        enum ReturnValues { SUCCESS, EUNSPEC, ... }; // Define constants for return
                                                                                                   enum ReturnValues { SUCCESS, EUNSPEC, ... }; // Define constants for return
             values
                                                                                                         values
     6
        typedef ... Radio; // Wireless communication facet
                                                                                                6
                                                                                                   typedef ... Radio; // Wireless communication facet
        typedef ... Timer;
                                                                                                    typedef ... Timer;
     7
        typedef ... Debug; // Send debug messages
                                                                                                   typedef ... Debug; // Send debug messages
     8
                                                                                                8
                                                                                                Q
        static const Endianess endianess; // WISELIB_LITTLE_ENDIAN or
                                                                                                  static const Endianess endianess; // WISELIB_LITTLE_ENDIAN or
    10
                                                                                               10
             WISELIB_BIG_ENDIAN
                                                                                                         WISELIB_BIG_ENDIAN
    11 }
                                                                                               11 }
        • Only facet which does not need to be instantiated
                                                                                                   • Only facet which does not need to be instantiated
        • Provide type definitions and constants
                                                                                                   • Provide type definitions and constants
        • Platform properties (endianess, size type, ...)
                                                                                                   • Platform properties (endianess, size type, ...)

    Constants for return values.

    Constants for return values

             • Include at least SUCCESS and ERR_UNSPEC (unspecified error)

    Include at least SUCCESS and ERR_UNSPEC (unspecified error)

             • May/will include more, similar to errno
                                                                                                         • May/will include more, similar to errno
                                                                                                   • Default types for basic OS Facets
        • Default types for basic OS Facets
                                                                          Lecture 21 37 / 49
                                                                                                                                                                      Lecture 21 37 / 49
Ioannis Chatzigiannakis
                                                                                           Ioannis Chatzigiannakis
                                     Pervasive Systems
                                                                                                                                 Pervasive Systems
                      Design Of The Wiselib
                                                              Hardware Abstraction with OS Facets
                                                                                           Motivation
                                                                                                                  Design Of The Wiselib
                                                                                                                                                         Hardware Abstraction with OS Facets
Motivation
                                                              Important Facets
                                                                                           Important Facets
The OS Facet
                                                                                           The OS Facet
     1 concept OsFacet {
                                                                                                1 concept OsFacet {
     2 typedef ... size_t;
                                                                                                2 typedef ... size_t;
       typedef ... block_data_t; // "byte"-like type for buffers
                                                                                                   typedef ... block_data_t; // "byte"-like type for buffers
     3
        enum ReturnValues { SUCCESS, EUNSPEC, ... }; // Define constants for return
                                                                                                   enum ReturnValues { SUCCESS, EUNSPEC, ... }; // Define constants for return
             values
                                                                                                         values
     5
                                                                                                5
     6
        typedef ... Radio; // Wireless communication facet
                                                                                                6
                                                                                                   typedef ... Radio; // Wireless communication facet
        typedef ... Timer
                                                                                                    typedef ... Timer;
     7
        typedef ... Debug; // Send debug messages
                                                                                                   typedef ... Debug; // Send debug messages
     8
                                                                                                8
                                                                                                Q
    10
        static const Endianess endianess; // WISELIB_LITTLE_ENDIAN or
                                                                                               10
                                                                                                   static const Endianess endianess; // WISELIB_LITTLE_ENDIAN or
             WISELIB_BIG_ENDIAN
                                                                                                         WISELIB_BIG_ENDIAN
    11 }
                                                                                               11 }
        • Only facet which does not need to be instantiated
                                                                                                   • Only facet which does not need to be instantiated
        • Provide type definitions and constants
                                                                                                   • Provide type definitions and constants
        • Platform properties (endianess, size type, ...)
                                                                                                   • Platform properties (endianess, size type, ...)

    Constants for return values

    Constants for return values

    Include at least SUCCESS and ERR_UNSPEC (unspecified error)

             • Include at least SUCCESS and ERR_UNSPEC (unspecified error)
             • May/will include more, similar to errno

    May/will include more, similar to errno

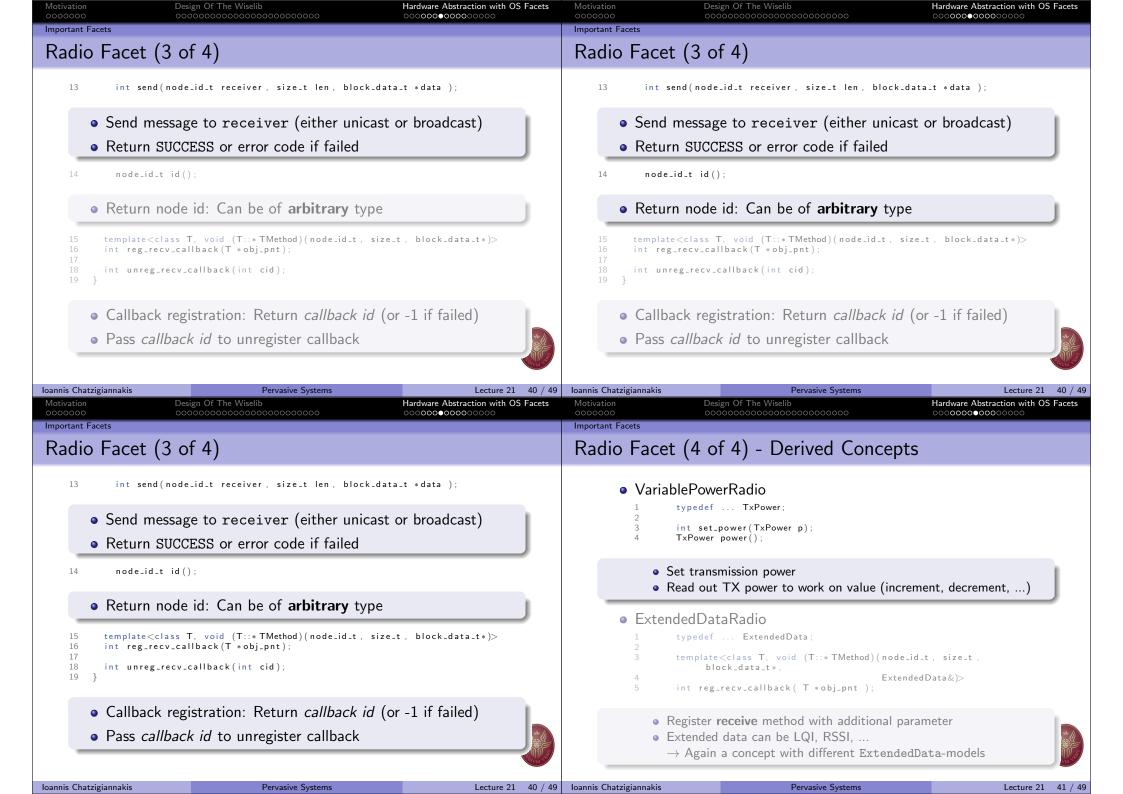
        • Default types for basic OS Facets

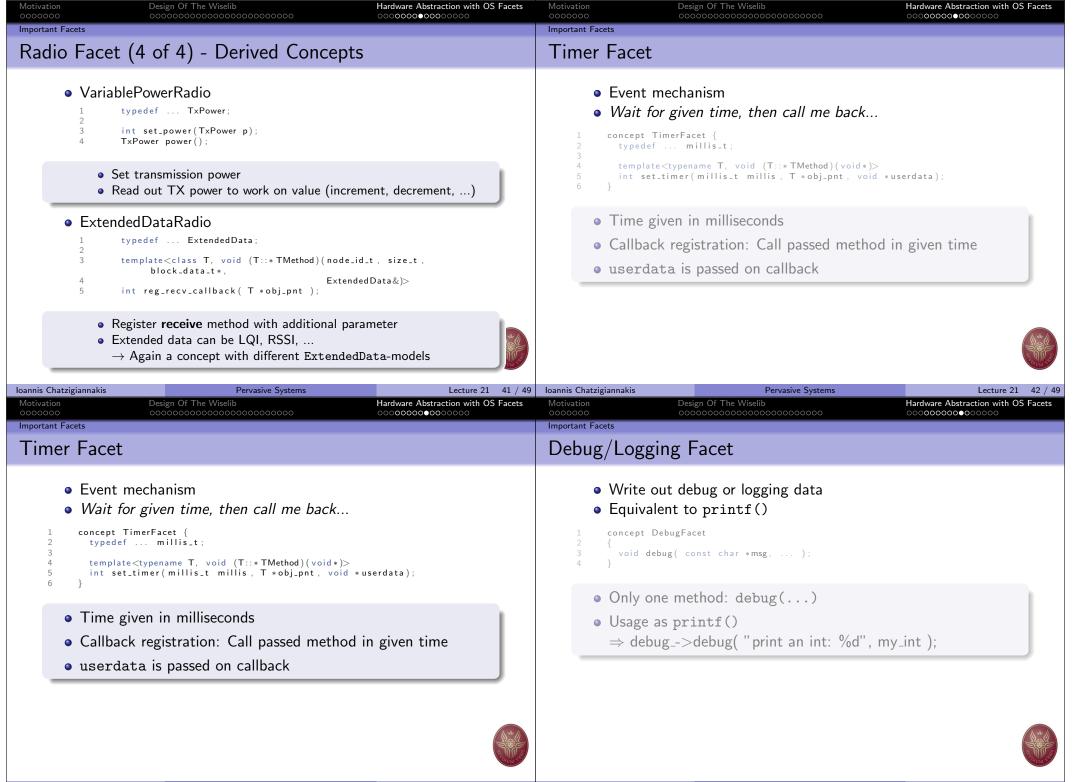
    Default types for basic OS Facets

                                                                          Lecture 21 37 / 49 Ioannis Chatzigiannakis
                                                                                                                                                                      Lecture 21 37 / 49
Ioannis Chatzigiannakis
                                      Pervasive Systems
                                                                                                                                 Pervasive Systems
```

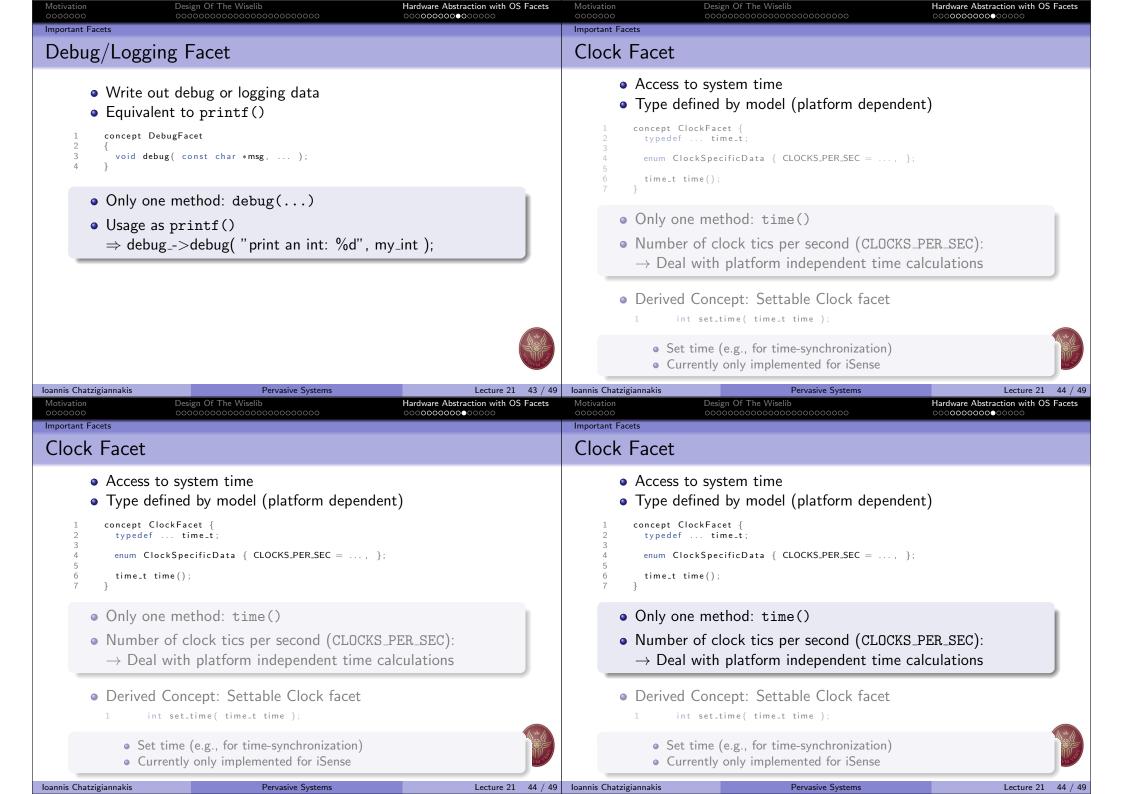
Motivation 0000000	Design Of The Wiselib ೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦	Hardware Abstraction with OS Facets	Motivation 0000000	Design Of The Wiselib 000000000000000000000000000000000000	Hardware Abstraction with OS Facets
The OS Facets	cet		Important Facets Radio Facet ((1 of 4)	
3 typedef . 4 enum Retu valu 5 6 typedef . 7 typedef . 9 10 static co WISE 11 } • Only • Provid • Const • Late • N	<pre> size_t; block_data_t; // "byte"-like type for urnValues { SUCCESS, EUNSPEC, }; // ues Radio; // Wireless communication fact</pre>	Define constants for return et "LE_ENDIAN or e instantiated ts De,) "SPEC (unspecified error)	 Cor Virt Send me Callback Provide Noc E.g 	issues straction to underlying hardware r mplex routing algorithms <i>tual</i> radio providing <i>virtual</i> ids essages to other nodes < registration for received mess node id (and its type !) de id type is defined per radio ., provide IP addresses, but run o ly restriction: Be passed to sized	sages n 16-bit addresses
oannis Chatzigiannakis Motivation 0000000 Important Facets	Pervasive Systems Design Of The Wiselib 000000000000000000000000000000000000	Lecture 21 37 / 49 Hardware Abstraction with OS Facets 0000●00000000000	Ioannis Chatzigiannakis Motivation 00000000 Important Facets	Pervasive Systems Design Of The Wiselib 000000000000000000000000000000000000	Lecture 21 38 / Hardware Abstraction with OS Facets
Radio Facet	t (1 of 4)		Radio Facet ((1 of 4)	
 C Send Callba Provid N E 	n issues Abstraction to underlying hardware r Complex routing algorithms <i>Virtual</i> radio providing <i>virtual</i> ids messages to other nodes ack registration for received mess de node id (and its type !) Node id type is defined per radio E.g., provide IP addresses, but run of Only restriction: Be passed to sized	sages n 16-bit addresses	 Cor Virt Send me Callback Provide Noc E.g 	issues straction to underlying hardware r mplex routing algorithms <i>tual</i> radio providing <i>virtual</i> ids essages to other nodes < registration for received mess node id (and its type !) de id type is defined per radio ., provide IP addresses, but run o ly restriction: Be passed to sized	sages n 16-bit addresses
oannis Chatzigiannakis	Pervasive Systems	Lecture 21 38 / 49	Ioannis Chatzigiannakis	Pervasive Systems	Lecture 21 38 /

Motivation 0000000	Design Of The Wiselib 000000000000000000000000000000000000	Hardware Abstraction with OS Facets	Motivation 0000000	Design Of The Wiselib ೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦	Hardware Abstraction with OS Facets		
Radio Facet (1 of 4)			Radio Facet (2 of 4)				
 Design issues Abstraction to underlying hardware radio Complex routing algorithms Virtual radio providing virtual ids Send messages to other nodes Callback registration for received messsages Provide node id (and its type!) Node id type is defined per radio E.g., provide IP addresses, but run on 16-bit add Only restriction: Be passed to sizeof() 		es 5-bit addresses	 concept RadioFacet { typedef node_id_t; typedef block_data_t; typedef block_data_t; typedef size_t; typedef message_id_t; e Ability to provide arbitrary node ID types e Message ID type to identify received messages enum SpecialNodelds { BROADCAST_ADDRESS =, NULL_NODE_ID = }; enum Restrictions { MAX_MESSAGE_LENGTH = }; e Basic constants for broadcasting and unknown nodes e Maximal message length defined per radio int enable_radio(); int disable_radio(); e Turn on/off radio 				
loannis Chatzigiannakis Motivation 0000000	Design Of The Wiselib	Lecture 21 38 / 49 Hardware Abstraction with OS Facets	Ioannis Chatzigiannakis Motivation 0000000	rn SUCCESS or error code if fail Pervasive Systems Design Of The Wiselib	Lecture 21 39 / · Hardware Abstraction with OS Facets		
Important Facets	et (2 of 4)		Important Facets Radio Face	et (2 of 4)			
1 conce 2 typ 3 typ 4 typ	<pre>pt RadioFacet { edef node_id_t; edef block_data_t; edef size_t; edef message_id_t;</pre>		1 concep 2 type 3 type 4 type	t RadioFacet { def node_id_t; def block_data_t; def size_t; def message_id_t;			
• Mes	ity to provide arbitrary node ID type sage ID type to identify received mes ^m SpecialNodelds { BROADCAST_ADDRESS =, N ^m Restrictions { MAX_MESSAGE_LENGTH =, };	sages	• Mess	ty to provide arbitrary node ID sage ID type to identify received SpecialNodelds { BROADCAST_ADDRESS = Restrictions { MAX_MESSAGE_LENGTH =	<pre>// messages // NULL_NODE_ID = // };</pre>		
• Basi	ic constants for broadcasting and unk kimal message length defined per rad		• Basic	c constants for broadcasting and imal message length defined pe	d unknown nodes		
12 int • Turr	enable_radio(); disable_radio(); n on/off radio urn SUCCESS or error code if failed		12 int • Turn	enable_radio(); disable_radio(); on/off radio rn SUCCESS or error code if fail	ed		



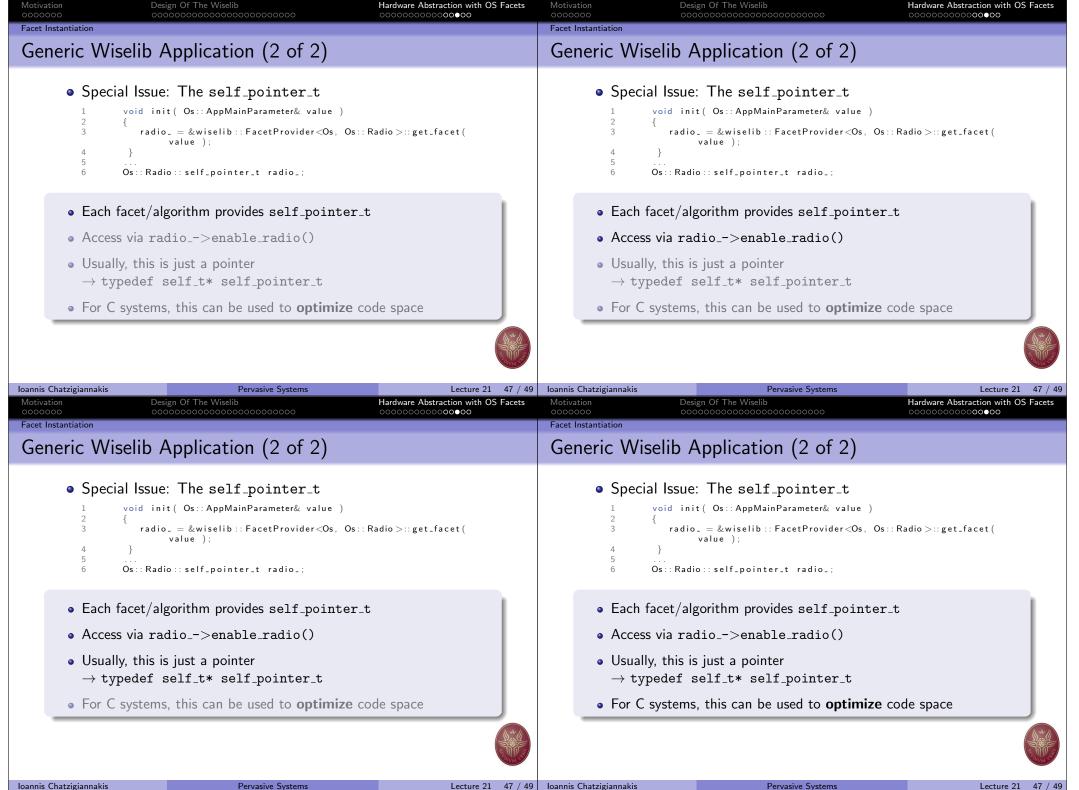


Ioannis Chatzigiannakis



Motivation 0000000	Design Of The Wiselib 000000000000000000000000000000000000	Hardware Abstraction with OS Facets	Motivation 000000	Design Of The Wiselib 000000000000000000000000000000000000	Hardware Abstraction with OS Facet ○○○○○○○○○●○○○○	
mportant Facets Clock Facet			Facet Instantiation Facet Structure			
 Type defi concept Clo typedef. enum Cloc time_t tin Only one Number of 	 Access to system time Type defined by model (platform dependent) concept ClockFacet { typedef time_t; enum ClockSpecificData { CLOCKS_PER_SEC =, }; time_t time(); } Only one method: time() Number of clock tics per second (CLOCKS_PER_SEC): 			 Construction of facets system dependent Shawn: A facet needs to know to which processor it belongs iSense: Require access to isense::0s Contiki: Only calls to C functions Each system with own constructors Generic Wiselib Application Construction must be hidden for user Solution: Template based facet provider Direct Integration Facets are known to user 		
• Derived (with platform independent time c Concept: Settable Clock facet set_time(time_t time);		● Dire	ctly initialize facets		
	ime (e.g., for time-synchronization) ently only implemented for iSense					
annis Chatzigiannakis lotivation 000000	Pervasive Systems Design Of The Wiselib 000000000000000000000000000000000000	Lecture 21 44 / 49 Hardware Abstraction with OS Facets	Ioannis Chatzigiannakis Motivation 0000000	Pervasive Systems Design Of The Wiselib 000000000000000000000000000000000000	Lecture 21 45 / Hardware Abstraction with OS Facet	
acet Instantiation			Facet Instantiation			
acet Structur	ſe		Facet Structur	e		
 Construction of facets system dependent Shawn: A facet needs to know to which processor it belongs iSense: Require access to isense::0s Contiki: Only calls to C functions Each system with own constructors Generic Wiselib Application Construction must be hidden for user Solution: Template based facet provider Direct Integration Facets are known to user Directly initialize facets 			 Construction of facets system dependent Shawn: A facet needs to know to which processor it belongs iSense: Require access to isense::0s Contiki: Only calls to C functions Each system with own constructors Generic Wiselib Application Construction must be hidden for user Solution: Template based facet provider Direct Integration Facets are known to user Directly initialize facets 			
nnis Chatzigiannakis	Pervasive Systems	Lecture 21 45 / 49	Ioannis Chatzigiannakis	Pervasive Systems	Lecture 21 45 ,	

Motivation 0000000 Facet Instantiation	Design Of The Wiselib ೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦೦	Hardware Abstraction with OS Facets ○○○○○○○○○○○○○○○○	Motivation 0000000 Facet Instantiation	Design Of The Wiselib 00000000000000000000000000	Hardware Abstraction with OS Facets ○○○○○○○○○●○○○	
Facet Structure			Generic Wiselib Application (1 of 2)			
 Construction of facets system dependent Shawn: A facet needs to know to which processor it belongs iSense: Require access to isense::0s Contiki: Only calls to C functions Each system with own constructors Generic Wiselib Application Construction must be hidden for user Solution: Template based facet provider Direct Integration Facets are known to user Directly initialize facets 			 Template FacetProvider Internals in Session 4 template<typename li="" osmodel_p,<=""> typename Facet_P> class FacetProvider { static Facet& get_facet (AppMainParameter& os); Template specialization for different platforms </typename> Method get_facet() returns reference to facet void init(Os::AppMainParameter& value) fradio_= & wiselib ::FacetProvider<os, os::radio="">::get_facet()</os,> Os::Radio::self_pointer_t_radio_; 			
aannis Chatzigiannakis Motivation 5000000 Facet Instantiation Generic Wiseli	Pervasive Systems Design Of The Wiselib 000000000000000000000000000000000000	Lecture 21 45 / 49 Hardware Abstraction with OS Facets 00000000000000000	Ioannis Chatzigiannakis Motivation 0000000 Facet Instantiation Generic Wise	Pervasive Systems Design Of The Wiselib 000000000000000000000000000000000000	Lecture 21 46 / 4 Hardware Abstraction with OS Facets	
 Template → Intern ¹/₂ ¹/₃ clas ⁴/₅ ⁵/₄ ⁵/₄ ¹/₄ ¹/₄ ¹/₄ ¹/₄ ¹/₄ ¹/₄ ¹/₄ ¹/₅ ¹/₄ ¹/₄ ¹/₅ ¹/₄ 	<pre>FacetProvider als in Session 4 plate<typename facet_p="" osmodel_p,="" typename=""> is FacetProvider { satic Facet& get_facet(AppMainParameter specialization for different plat get_facet() returns reference f init(Os::AppMainParameter& value) radio_ = &wiselib::FacetProvider<os,);="" os:="" pre="" radio::self_pointer_t="" radio_;<="" value=""></os,></typename></pre>	forms to facet	• Templat \rightarrow Inter 1 te 2 3 cl 4 5 } • Templat • Method 1 vc 2 { 3 cl 4 5 } • Templat • Method 1 vc 2 { 3 cl 4 5 } •	te FacetProvider rnals in Session 4 emplate <typename osmodel_p,<br="">typename Facet_P> lass FacetProvider { static Facet& get_facet(AppMainParar te specialization for different p get_facet() returns reference pid init(Os::AppMainParameter& value radio_ = &wiselib::FacetProvider<os, value); s::Radio::self_pointer_t radio_;</os, </typename>	platforms ce to facet	
annis Chatzigiannakis	Pervasive Systems	Lecture 21 46 / 49	loannis Chatzigiannakis	Pervasive Systems	Lecture 21 46 / 4	



Motivation 0000000	Design Of The Wiselib 000000000000000000000000000000000000	Hardware Abstraction with OS Facets	Motivation 0000000	Design Of The Wiselib 000000000000000000000000000000000000	Hardware Abstraction with OS Facets		
	Sense Application			Facet Instantiation iSense Application			
1 2 3 4 5 6 7 8 9 10 11 2 3 4 5 6 7 8 9 10 11	<pre>nse facets usually expect isense::(template<typename osmodel_p=""> class iSenseRadioModel : public isense :: Receiver { iSenseRadioModel(isense :: Os& os) : os_(os) { os(os) { osdispatcher().add_receiver(this } } rectly used as members #include "external_interface/isense/isen typedef wiselib :: iSenseOsModel Os; class iSenseDemoApplication { iSense1: Application(isense :: Os& os : isense :: Application(os), radio_(os) {} Os:: Radio radio_; } }</typename></pre>); nse_radio.h"	1 2 3 4 5 6 7 8 9 10 11 10 11 2 3 4 5 6 7 8 9 10 11	<pre>se facets usually expect isense: template<typename osmodel_p=""> class iSenseRadioModel : public isense :: Receiver { iSenseRadioModel(isense :: Os& os) : os_(os) { osdispatcher().add_receiver(thi } } ctly used as members #include " external_interface/isense/ise typedef wiselib :: iSenseOsModel Os; class iSenseDemoApplication { iSenseDemoApplication { iSenseDemoApplication { isense :: Application(isense :: Os& of : isense :: Application(os), radio_(os) {} Os:: Radio radio_;</typename></pre>	is); ense_radio.h"		
12 oannis Chatzigiannak Motivation 0000000	} is Pervasive Systems Design Of The Wiselib 000000000000000000000000000000000000	Lecture 21 48 / 49 Hardware Abstraction with OS Facets	12 Ioannis Chatzigiannakis Motivation 0000000	} Pervasive Systems Design Of The Wiselib 000000000000000000000000000000000000	Lecture 21 48 Hardware Abstraction with OS Face ○○○○○○○○○○○○●		
Facet Instantiation Shawn Ap	oplication		Facet Instantiation Shawn App	olication			
→ 1 2 3 4 5 6 7	<pre>● Shawn facets usually expect ShawnOs in constructor → Defined in external_interface/shawn/shawn_types.h 1 template<typename osmodel_p=""> 2 class ShawnRadioModel { 3 ShawnRadioModel(ShawnOs& os) 4 : os_(os) 5 {} 6 7 ShawnOs& os_;</typename></pre>			 Shawn facets usually expect ShawnOs in constructor → Defined in external_interface/shawn/shawn_types.h template<typename osmodel_p=""> class ShawnRadioModel { ShawnRadioModel { ShawnRadioModel (ShawnOs& os)</typename>			
1 2 3 4 5 6 7 8 9 10 11 12 13 14	<pre>#include " external_interface/shawn/shaw typedef wiselib::ShawnOsModel Os; class WiselibExampleProcessor : public virtual ExtlfaceProcessor { WiselibExampleProcessor() : wiselib_radio_ (os_) {} void boot() { osproc = this; } ShawnOs os_; Os::Radio wiselib_radio_;</pre>	n_radio.h"	1 2 3 4 5 6 7 8 9 10 11 12 13 14	<pre>#include " external_interface/shawn/sha typedef wiselib::ShawnOsModel Os; class WiselibExampleProcessor : public virtual ExtIfaceProcessor { WiselibExampleProcessor() : wiselib_radio_ (os_) {} void boot() { osproc = this; } ShawnOs os_; Os::Radio wiselib_radio_;</pre>			